Parallel computing

, processor performance o "History" : From 1980's to ~ 2004 to frequency scaling increased mainly due (increased clack vate).

Codes would my fæster and faster without changes Ruytime = Instruction x Cycles x time program x instruction cycle

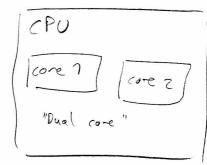
Power consumption in this : Paf

force socond so increased f -> increased P (as expected!)

> Problems of overheating etc.

from 17004; performance increase mainly from shift to pavellel comp, and in part mylticone processors

o Challengy & Requires changes on software side! Need to distribute tasks or data across Threads or processes !



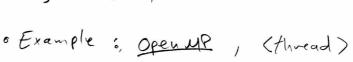
Old-school parallelization:

Gise each student in a deas

their own # eq to solve

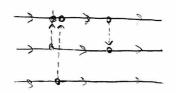
· Two main approches

- 1) · Shared memory
 - · Threading
 - · Single computer/unde



· Single process, can switch between one and multiple threads

- 2) · Distributed memory
 - · Message passing
 - o can be used on singly computer/hads or between computers/hades
 - o Example: MPI
 - e Multiple indep processes



- · These approaches can be combined:
 - Multiple processes, each spourity multiple threads (sharing that process' memory)



Meution GAMDIT

o Parallelizator comes with some overhead, from spowning threads, poising messages, etc.

Only useful if strack > storound. (Also: romes with substantial room for mitakes and bags ...)

· Parallelitation (cont. from last time)

- · Recap:
 - o Two approaches: Shared memory (e.g. OpenMP) Distributed memory (e.g. MPI)
- · Co through Open UP examples in the Git repo]
- o Different approaches to parallelize project 4: AH1) Parallelize loop over temperatures (simplest!)
 - Altz) For each temp., use parallelization to run maltiple MCM(chairs (waltiple "walkers")
 - o Increase number of threads and decrease cycles per thread needs burn-in

=) some accuracy, shorter time

- · Increase number of Threeds while heeping number of cycles per thread fixed
 - => higher accuracy (more MC somples), same time
- AH3) For each temperature and each MC cycle, purallelize the "sweep" over the spin matrix
 - · Most complicated (Don't do this ...)
 - o Most overhead

o thow to define speedup from parallelization

Speedup =
$$\frac{\text{time with single thread/processes}}{\text{time with } n \text{ thread/processes}} = \frac{T_1}{T_n}$$

- o Ideal case: $n \text{ threads} \iff T_n = \frac{T_1}{n} \iff \text{ speedup}$ factor is n
 - In most cases we will not get ideal speedup
 - = In rare cases we can get better than ideal spreadup (e.g. through changes in memory across)
- can still be a better choice than a simple elgorithm with better (ideal?)

 parallelization speedup!
 - · Example: Find the maximum of a complicated, high-dim
 - 1) random sampling (ideal speedup, "embarrasingly parallelizable")
 - 2) sophisticated optimization algorithm, e.g. differential evolution

Show example from paper

(needs communication and synchronitation > Less)

- · Opper bound on speedup:
 - A task takes time to on single thread/process
 - * Fraction of time spent in perfectly parallelizable rade: f
 - Non-parallelizable fraction: 7-f
 - · Single thread/process ;

$$T_{1} = (1-f)T_{1} + fT_{2}$$

o On u threads/processes;

$$T_n = (7-f)T_n + f\frac{T_n}{h}$$

· Speedup :

$$\frac{T_1}{T_n} = \frac{T_2}{(2-f)T_1 + f \frac{T_1}{T_2}} = \frac{1}{(2-f) + \frac{f}{2n}}$$

$$\lim_{N\to\infty}\frac{T_1}{T_N}=\frac{1}{1-f}$$
 And a his less

Example: It 99% of a task is parallelizable (f=0.99) the maximum possible speedup tactor $\frac{1}{1-0.99} = \frac{1}{0.01} = 100$

Example 2: f = 0.80 = max speedup is 5