

# RSA vs. ECC Comparison for Embedded Systems

#### Introduction

Author: Kerry Maletsky

Modern cryptographic protocols increasingly use asymmetric algorithms such as RSA and ECC because of their flexibility and enhanced ability to manage keys. Developed in the late '70s, the Rivest, Shamir and Adelman algorithm (RSA) has become the algorithm of choice for internet security. Elliptic Curve Cryptography (ECC), which was first proposed in the '80s, is becoming more widely used for many reasons. There are important differences between the two which warrant a careful comparison.

## **Security Matters**

The level of security in systems is a primary concern. Most cryptographic experts recommend that current systems offer at least 128 bits of security. This does not represent the key length. Security comes from the combination of the specific algorithm and its key length. For example, it is generally thought that 128 bits of security can be achieved with 128-bit AES keys, 256-bit Elliptic Curve keys and 3072-bit RSA keys. If implementation issues are ignored, then these algorithms with the specified key lengths will generally have the same level of security. See <a href="https://www.keylength.com">www.keylength.com</a>(1) for recommendations from various sources. Typical RSA implementations currently employ 1024-bit or 2048-bit keys, yet both are less secure than AES-128.

Table 1. Security Comparison for Various Algorithm Key Size Combinations (Source: NSA)<sup>(5)</sup>

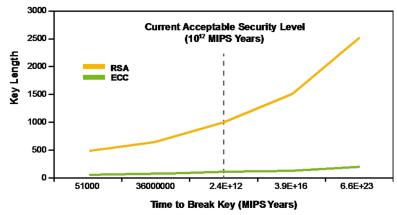
Security Bits	Symmetric Encryption Algorithm	Minimum Size (Bits) of Public Keys	
		RSA	ECC
80	Skipjack	1024	160
112	3DES	2048	224
128	AES-128	3072	256
192	AES-192	7680	384
256	AES-256	15360	512

Key lengths generally increase over time as the computation available to attackers continues to increase. This is a manifestation of the cryptographic arms rate. Some experts suggest that AES-256 be employed for data encryption rather than the prior accepted AES-128 protocol. If elliptic curves are used for the key management (i.e., the encryption/decryption session key) of an AES-256 session, then a 512-bit elliptic curve session key is required (see Table 1). To achieve the same level of security with RSA encryption, 15,360-bit keys are required, which is computationally infeasible in embedded systems today. This stark contrast between the feasibility of ECC over RSA indicates that ECC is the algorithm of the future for embedded systems.

Algorithm security is irrelevant if an attacker can obtain the keys via other methods. This point cannot be emphasized enough. Security starts and ends with how well the keys are protected. In addition to poor key storage, weak or faulty algorithm implementations, bad random number generation, and/or aggressive attacks on end point systems can also degrade security.

© 2020 Microchip Technology Inc. White Paper DS00003442A-page 1

Figure 1. RSA and ECC Performance<sup>(6)</sup>



This chart presents what key lengths of each algorithm provide a level of security measured in time in MIPS-years to break the security. This illustrates that ECC is more efficient.

## Performance Anxiety

When it comes to performance at 128-bit security levels, RSA is generally reported to be ten times slower than ECC for private key operations such as signature generation or key management. The performance disparity expands dramatically at 256-bit security levels, where RSA is 50 to 100 times slower. RSA's key generation is also very slow compared to ECC's key generation, with the RSA's being 100 to 1000 times slower. However, this may or may not be a significant consideration in systems that generate keys infrequently. It does matter for certain protocols or policies that require more frequent key generation.

Public key signature validation is generally faster with RSA compared to ECC, which can provide a benefit.

#### **Bandwidth**

When it comes to network bandwidth, the main concern relates to the symmetric algorithm used for message encryption and Message Authentication Coding (MAC) for integrity checking (this is unrelated to the choice of RSA versus ECC). Smaller embedded systems may start sessions more frequently, or the asymmetric authentication may be a larger percentage of the overall traffic and the size of the keys and signatures can make a difference. At the 128-bit security level, public keys and signatures are six times larger for RSA. Private keys are 12 times larger for RSA compared to ECC, at the 128-bit security level. The key size generally has no impact on performance, but size does matter when it comes to the cost of secure storage of the keys.

# **Government and Industry Standard Recommendations**

Based upon the trade-offs noted earlier, there is an almost endless list of new standards that are mandating the use of ECC. A small selection is noted below:

- ZigBee Networking Standards This standard includes the use of asymmetric algorithms for authentication and key management and specifies Elliptic Curve Digital Signature Algorithm (ECDSA) and Elliptic-curve Diffie-Hellman (ECDH) as the algorithm of choice. See "Securing Ad Hoc Embedded Wireless Networks with Public-Key Cryptography". (2)
- Security Module PP Standards The Federal Office for Information Security in Germany, Bundesamt für Sicherheit in der Informationstechnik, abbreviated as BSI, has published a set of standards for energy metering gateway security, which specifies elliptic curves as the authentication algorithm. See "Protection Profile for the Security Module of a Smart Meter Gateway (Security Module PP)".(3)
- Intelligent Transportation Systems (ITS) Standards This U.S. Department of Transportation program documents standards for the automotive industry to include elliptic curves as the algorithm of choice for Vehicle-Vehicle communication security. See "Low-Latency ECDSA Signature Verification - A Road Towards Safer Traffic".(4)

DS00003442A-page 2 White Paper

Commercial National Security Algorithm (CNSA) Suite – This U.S. National Security initiative publishes a set
of standard algorithms approved for use in non-defense applications called "Commercial National Security
Algorithm Suite". This set of standards replaces the "Suite B Cryptography Standards", previously specified by
the US Government. (5)

#### Conclusion

Due to security issues, most new cryptographic protocols are moving away from RSA to elliptic curves. This transition is expedited in the embedded space because the ECC cost/performance benefits are quickly realized.

#### References

- BlueKrypt "Cryptographic Key Length Recommendation", 2015.
- 2. Mitch Blaser "Securing Ad Hoc Embedded Wireless Networks with Public-Key Cryptography", 2006.
- 3. Bundesamt für Sicherheit in der Informationstechnik "Protection Profile for the Security Module of a Smart Meter Gateway (Security Module PP)", 2014.
- 4. Miroslav Knežević, Ventzislav Nikov, and Peter Rombouts "Low-Latency ECDSA Signature Verification A Road Towards Safer Traffic", 2014.
- 5. National Security Agency "Commercial National Security Algorithm Suite", 2015.
- 6. M. Alimohammadi and A. A. Pouyan "Performance Analysis of Cryptography Methods for Secure Message Exchanging in VANET", 2014.

# **Revision History**

Revision	Date	Description
Α	04/2020	Original release of this document. This document replaces Atmel – 8951A – 07/2015.

© 2020 Microchip Technology Inc. White Paper DS00003442A-page 3

### The Microchip Website

Microchip provides online support via our website at http://www.microchip.com/. This website is used to make files and information easily available to customers. Some of the content available includes:

- Product Support Data sheets and errata, application notes and sample programs, design resources, user's guides and hardware support documents, latest software releases and archived software
- General Technical Support Frequently Asked Questions (FAQs), technical support requests, online discussion groups, Microchip design partner program member listing
- Business of Microchip Product selector and ordering guides, latest Microchip press releases, listing of seminars and events, listings of Microchip sales offices, distributors and factory representatives

## Product Change Notification Service

Microchip's product change notification service helps keep customers current on Microchip products. Subscribers will receive email notification whenever there are changes, updates, revisions or errata related to a specified product family or development tool of interest.

To register, go to http://www.microchip.com/pcn and follow the registration instructions.

## **Customer Support**

Users of Microchip products can receive assistance through several channels:

- Distributor or Representative
- Local Sales Office
- Embedded Solutions Engineer (ESE)
- **Technical Support**

Customers should contact their distributor, representative or ESE for support. Local sales offices are also available to help customers. A listing of sales offices and locations is included in this document.

Technical support is available through the website at: http://www.microchip.com/support

# Microchip Devices Code Protection Feature

Note the following details of the code protection feature on Microchip devices:

- Microchip products meet the specification contained in their particular Microchip Data Sheet.
- Microchip believes that its family of products is one of the most secure families of its kind on the market today. when used in the intended manner and under normal conditions.
- There are dishonest and possibly illegal methods used to breach the code protection feature. All of these methods, to our knowledge, require using the Microchip products in a manner outside the operating specifications contained in Microchip's Data Sheets. Most likely, the person doing so is engaged in theft of intellectual property.
- Microchip is willing to work with the customer who is concerned about the integrity of their code.
- Neither Microchip nor any other semiconductor manufacturer can guarantee the security of their code. Code protection does not mean that we are guaranteeing the product as "unbreakable."

Code protection is constantly evolving. We at Microchip are committed to continuously improving the code protection features of our products. Attempts to break Microchip's code protection feature may be a violation of the Digital Millennium Copyright Act. If such acts allow unauthorized access to your software or other copyrighted work, you may have a right to sue for relief under that Act.

# Legal Notice

Information contained in this publication regarding device applications and the like is provided only for your convenience and may be superseded by updates. It is your responsibility to ensure that your application meets with

White Paper DS00003442A-page 4 your specifications. MICROCHIP MAKES NO REPRESENTATIONS OR WARRANTIES OF ANY KIND WHETHER EXPRESS OR IMPLIED, WRITTEN OR ORAL, STATUTORY OR OTHERWISE, RELATED TO THE INFORMATION, INCLUDING BUT NOT LIMITED TO ITS CONDITION, QUALITY, PERFORMANCE, MERCHANTABILITY OR FITNESS FOR PURPOSE. Microchip disclaims all liability arising from this information and its use. Use of Microchip devices in life support and/or safety applications is entirely at the buyer's risk, and the buyer agrees to defend, indemnify and hold harmless Microchip from any and all damages, claims, suits, or expenses resulting from such use. No licenses are conveyed, implicitly or otherwise, under any Microchip intellectual property rights unless otherwise stated.

#### **Trademarks**

The Microchip name and logo, the Microchip logo, Adaptec, AnyRate, AVR, AVR logo, AVR Freaks, BesTime, BitCloud, chipKIT, chipKIT logo, CryptoMemory, CryptoRF, dsPIC, FlashFlex, flexPWR, HELDO, IGLOO, JukeBlox, KeeLoq, Kleer, LANCheck, LinkMD, maXStylus, maXTouch, MediaLB, megaAVR, Microsemi, Microsemi logo, MOST, MOST logo, MPLAB, OptoLyzer, PackeTime, PIC, picoPower, PICSTART, PIC32 logo, PolarFire, Prochip Designer, QTouch, SAM-BA, SenGenuity, SpyNIC, SST, SST Logo, SuperFlash, Symmetricom, SyncServer, Tachyon, TempTrackr, TimeSource, tinyAVR, UNI/O, Vectron, and XMEGA are registered trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

APT, ClockWorks, The Embedded Control Solutions Company, EtherSynch, FlashTec, Hyper Speed Control, HyperLight Load, IntelliMOS, Libero, motorBench, mTouch, Powermite 3, Precision Edge, ProASIC, ProASIC Plus, ProASIC Plus logo, Quiet-Wire, SmartFusion, SyncWorld, Temux, TimeCesium, TimeHub, TimePictra, TimeProvider, Vite, WinPath, and ZL are registered trademarks of Microchip Technology Incorporated in the U.S.A.

Adjacent Key Suppression, AKS, Analog-for-the-Digital Age, Any Capacitor, AnyIn, AnyOut, BlueSky, BodyCom, CodeGuard, CryptoAuthentication, CryptoAutomotive, CryptoCompanion, CryptoController, dsPICDEM, dsPICDEM.net, Dynamic Average Matching, DAM, ECAN, EtherGREEN, In-Circuit Serial Programming, ICSP, INICnet, Inter-Chip Connectivity, JitterBlocker, KleerNet, KleerNet logo, memBrain, Mindi, MiWi, MPASM, MPF, MPLAB Certified logo, MPLIB, MPLINK, MultiTRAK, NetDetach, Omniscient Code Generation, PICDEM, PICDEM.net, PICkit, PICtail, PowerSmart, PureSilicon, QMatrix, REAL ICE, Ripple Blocker, SAM-ICE, Serial Quad I/O, SMART-I.S., SQI, SuperSwitcher, SuperSwitcher II, Total Endurance, TSHARC, USBCheck, VariSense, ViewSpan, WiperLock, Wireless DNA, and ZENA are trademarks of Microchip Technology Incorporated in the U.S.A. and other countries.

SQTP is a service mark of Microchip Technology Incorporated in the U.S.A.

The Adaptec logo, Frequency on Demand, Silicon Storage Technology, and Symmcom are registered trademarks of Microchip Technology Inc. in other countries.

GestIC is a registered trademark of Microchip Technology Germany II GmbH & Co. KG, a subsidiary of Microchip Technology Inc., in other countries.

All other trademarks mentioned herein are property of their respective companies.

© 2020, Microchip Technology Incorporated, Printed in the U.S.A., All Rights Reserved.

ISBN: 978-1-5224-5997-2

# **Quality Management System**

For information regarding Microchip's Quality Management Systems, please visit http://www.microchip.com/quality.



# **Worldwide Sales and Service**

AMERICAS	ASIA/PACIFIC	ASIA/PACIFIC	EUROPE
Corporate Office	Australia - Sydney	India - Bangalore	Austria - Wels
2355 West Chandler Blvd.	Tel: 61-2-9868-6733	Tel: 91-80-3090-4444	Tel: 43-7242-2244-39
Chandler, AZ 85224-6199	China - Beijing	India - New Delhi	Fax: 43-7242-2244-393
Tel: 480-792-7200	Tel: 86-10-8569-7000	Tel: 91-11-4160-8631	Denmark - Copenhagen
Fax: 480-792-7277	China - Chengdu	India - Pune	Tel: 45-4450-2828
Technical Support:	Tel: 86-28-8665-5511	Tel: 91-20-4121-0141	Fax: 45-4485-2829
http://www.microchip.com/support	China - Chongqing	Japan - Osaka	Finland - Espoo
Web Address:	Tel: 86-23-8980-9588	Tel: 81-6-6152-7160	Tel: 358-9-4520-820
http://www.microchip.com	China - Dongguan	Japan - Tokyo	France - Paris
Atlanta	Tel: 86-769-8702-9880	Tel: 81-3-6880- 3770	Tel: 33-1-69-53-63-20
Duluth, GA	China - Guangzhou	Korea - Daegu	Fax: 33-1-69-30-90-79
Tel: 678-957-9614	Tel: 86-20-8755-8029	Tel: 82-53-744-4301	Germany - Garching
Fax: 678-957-1455	China - Hangzhou	Korea - Seoul	Tel: 49-8931-9700
Austin, TX	Tel: 86-571-8792-8115	Tel: 82-2-554-7200	Germany - Haan
Tel: 512-257-3370	China - Hong Kong SAR	Malaysia - Kuala Lumpur	Tel: 49-2129-3766400
Boston	Tel: 852-2943-5100	Tel: 60-3-7651-7906	Germany - Heilbronn
Westborough, MA	China - Nanjing	Malaysia - Penang	Tel: 49-7131-72400
Tel: 774-760-0087	Tel: 86-25-8473-2460	Tel: 60-4-227-8870	Germany - Karlsruhe
Fax: 774-760-0088	China - Qingdao	Philippines - Manila	Tel: 49-721-625370
Chicago	Tel: 86-532-8502-7355	Tel: 63-2-634-9065	Germany - Munich
Itasca, IL	China - Shanghai	Singapore	Tel: 49-89-627-144-0
Tel: 630-285-0071	Tel: 86-21-3326-8000	Tel: 65-6334-8870	Fax: 49-89-627-144-44
Fax: 630-285-0075	China - Shenyang	Taiwan - Hsin Chu	Germany - Rosenheim
Dallas	Tel: 86-24-2334-2829	Tel: 886-3-577-8366	Tel: 49-8031-354-560
Addison, TX	China - Shenzhen	Taiwan - Kaohsiung	Israel - Ra'anana
Tel: 972-818-7423	Tel: 86-755-8864-2200	Tel: 886-7-213-7830	Tel: 972-9-744-7705
Fax: 972-818-2924	China - Suzhou	Taiwan - Taipei	Italy - Milan
Detroit	Tel: 86-186-6233-1526	Tel: 886-2-2508-8600	Tel: 39-0331-742611
Novi, MI	China - Wuhan	Thailand - Bangkok	Fax: 39-0331-466781
Tel: 248-848-4000	Tel: 86-27-5980-5300	Tel: 66-2-694-1351	Italy - Padova
Houston, TX	China - Xian	Vietnam - Ho Chi Minh	Tel: 39-049-7625286
Tel: 281-894-5983	Tel: 86-29-8833-7252	Tel: 84-28-5448-2100	Netherlands - Drunen
Indianapolis	China - Xiamen		Tel: 31-416-690399
Noblesville, IN	Tel: 86-592-2388138		Fax: 31-416-690340
Tel: 317-773-8323	China - Zhuhai		Norway - Trondheim
Fax: 317-773-5453	Tel: 86-756-3210040		Tel: 47-72884388
Tel: 317-536-2380			Poland - Warsaw
Los Angeles			Tel: 48-22-3325737
Mission Viejo, CA			Romania - Bucharest
Tel: 949-462-9523			Tel: 40-21-407-87-50
Fax: 949-462-9608			Spain - Madrid
Tel: 951-273-7800			Tel: 34-91-708-08-90
Raleigh, NC			Fax: 34-91-708-08-91
Tel: 919-844-7510			Sweden - Gothenberg
New York, NY			Tel: 46-31-704-60-40
Tel: 631-435-6000			Sweden - Stockholm
San Jose, CA			Tel: 46-8-5090-4654
Tel: 408-735-9110			UK - Wokingham
Tel: 408-436-4270			Tel: 44-118-921-5800
Canada - Toronto			Fax: 44-118-921-5820
Tel: 905-695-1980			
Fax: 905-695-2078			