

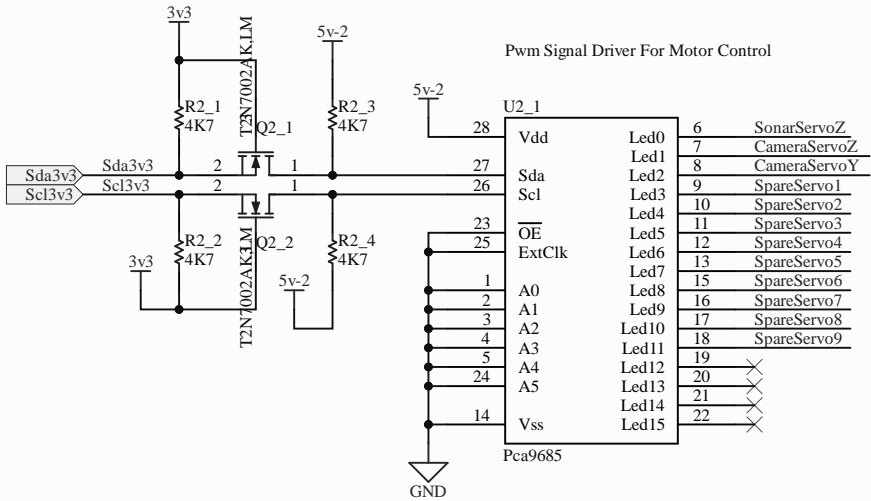
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Size A4	Number	Revision
Date:	4-06-2021	Sheet of
File:	C:\Users\...\main_control.SchDoc	Drawn By:

A

B

C

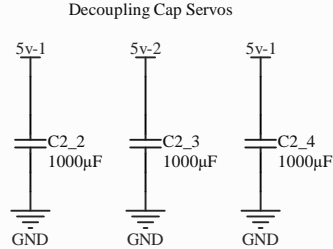
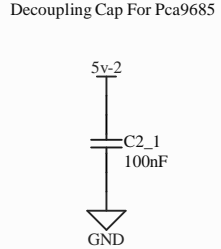
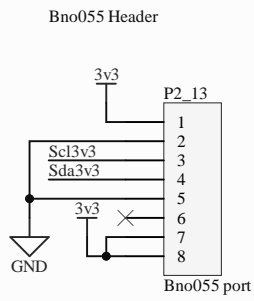
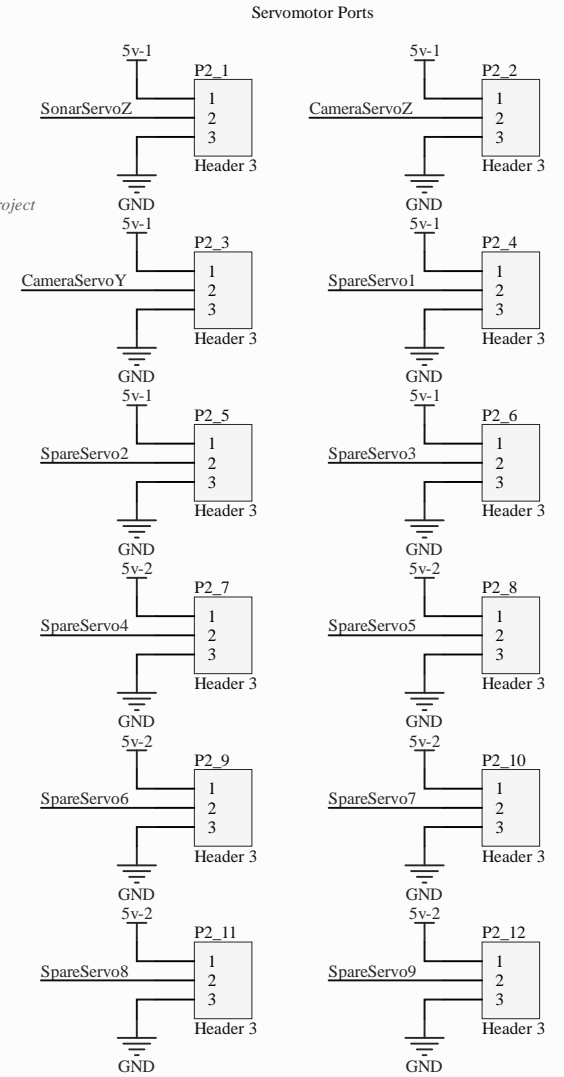
D



P2\_[1..3] are used by servos

The other ports are there only to route the remaining pwm signals

They may or may not be used in the project



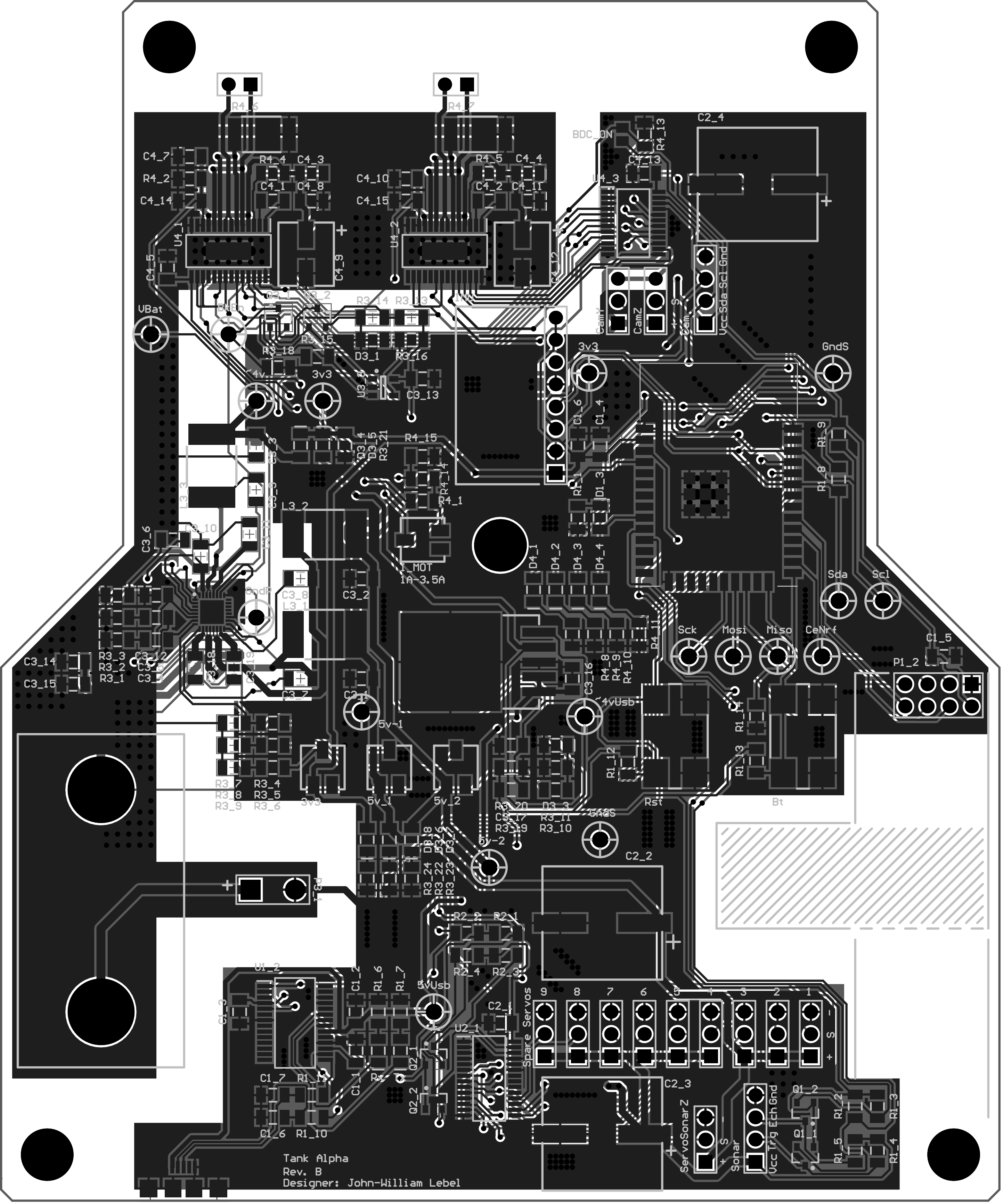
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Size	Number	Revision
A4		
Date:	4-06-2021	Sheet of
File:	C:\Users\...\servo_driver_and_imu_device	Drawn By:

A

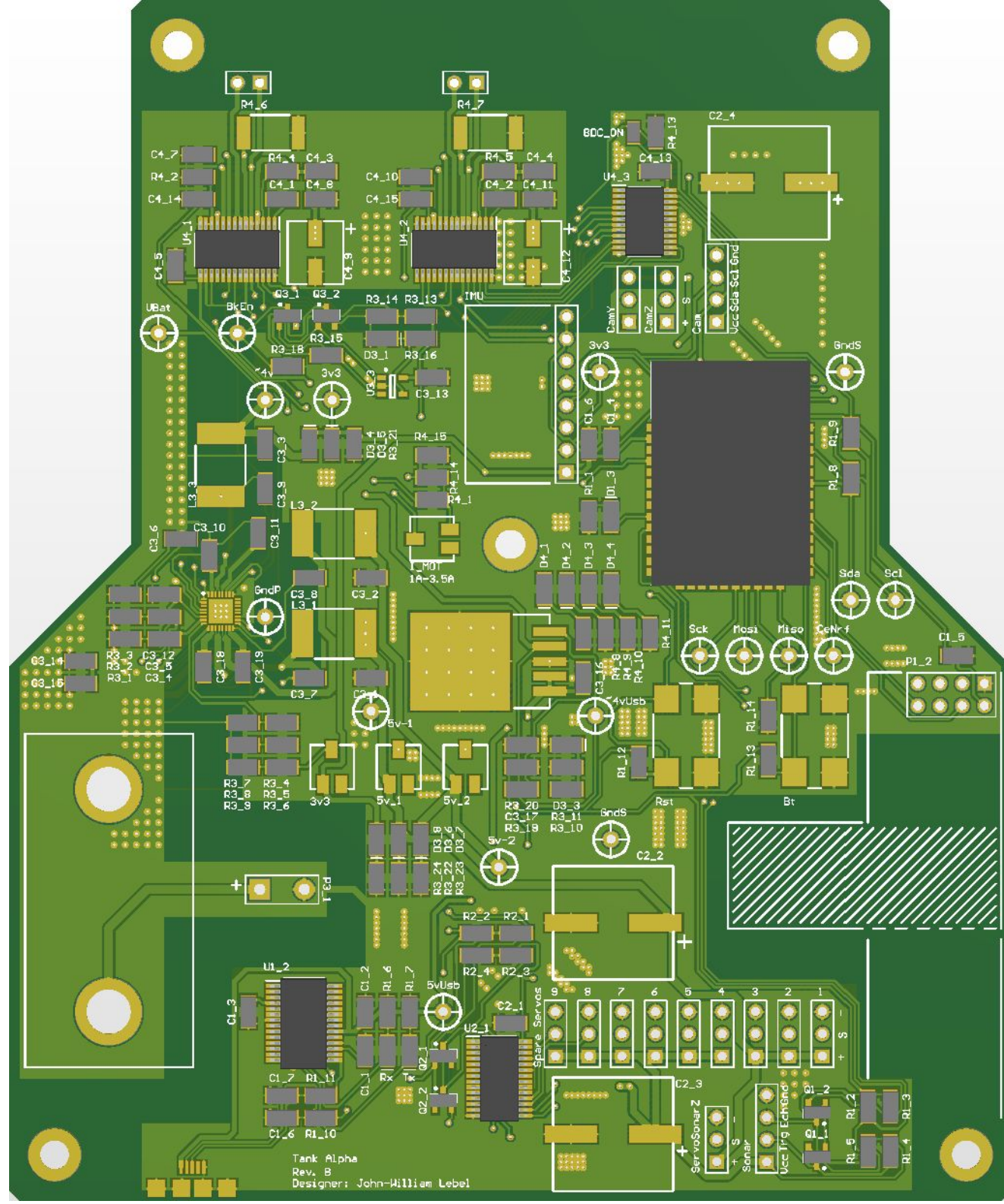
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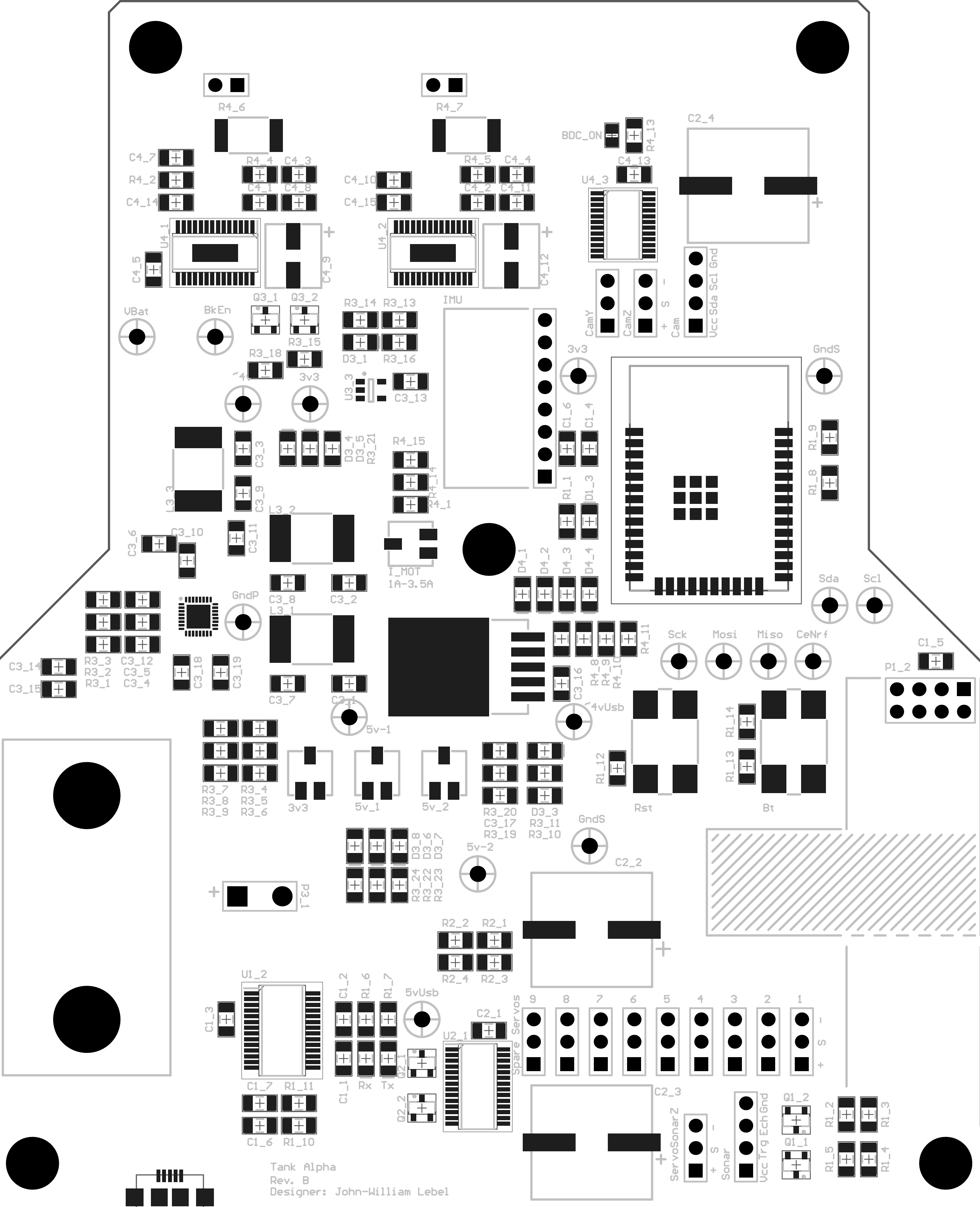
C

D

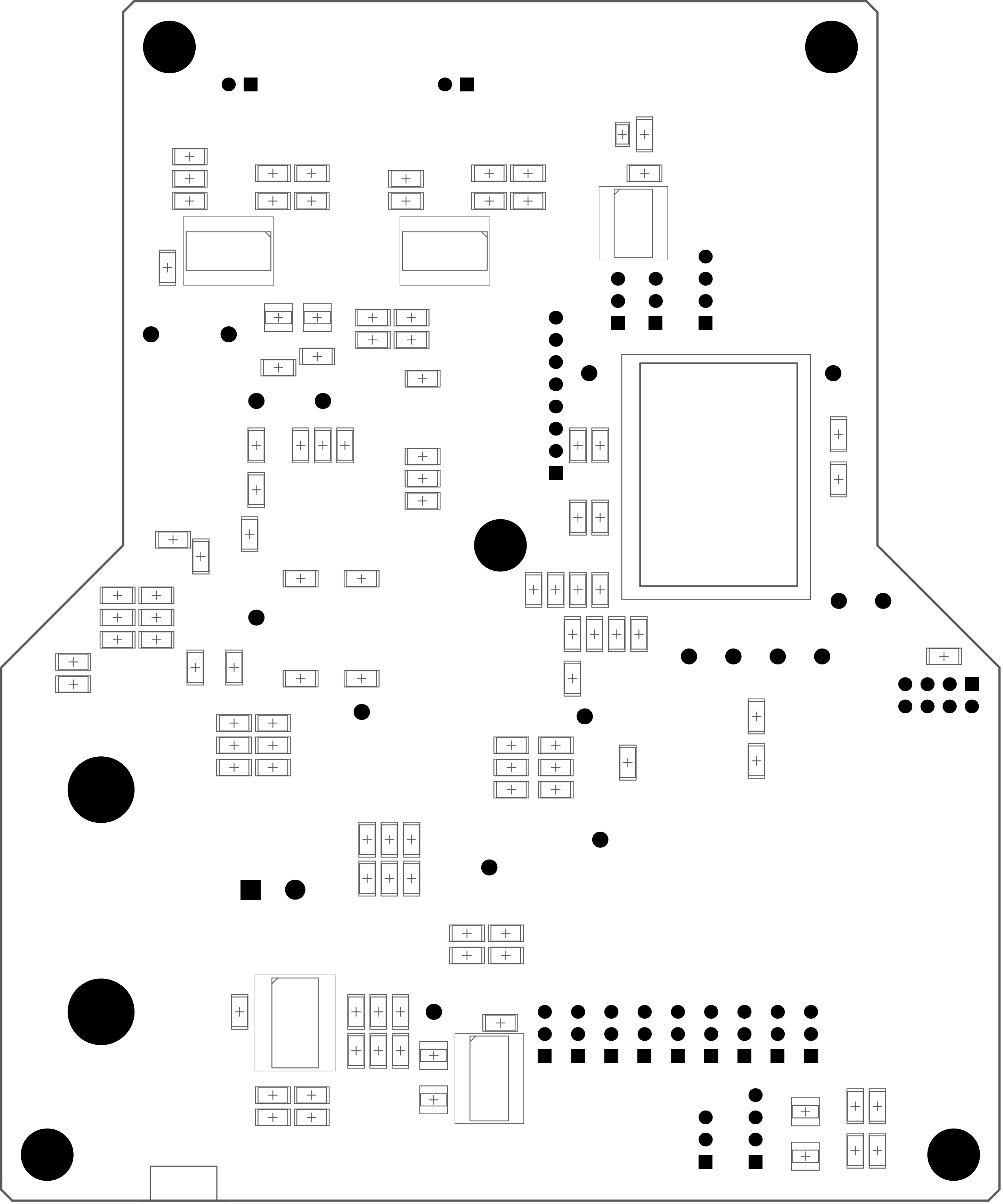


Tank Alpha  
Rev. B  
Designer: John-William Lebel





Tank Alpha  
Rev. B  
Designer: John-William Lebel





## Design Rules Verification Report

Filename : C:\Users\John\Desktop\altium12\altium12\PCB\_Alpha.PcbDoc

Warnings 0  
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.254mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ( All )	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.127mm) (Max=1.016mm) (Preferred=0.254mm) (All)	0
Power Plane Connect Rule(Relief Connect )(Expansion=0.508mm) (Conductor Width=0.254mm) (Air Gap=0.254mm)	0
Hole Size Constraint (Min=0.025mm) (Max=5.08mm) (All)	0
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Height Constraint (Min=0mm) (Max=25.4mm) (Preferred=12.7mm) (All)	0
Total	0

# Electrical Rules Check Report

Class	Document	Message
		Successful Compile for PCB_Hydra.PrjPcb