John Gangemi VLSI Design Automation Summer 2015

Homework 3

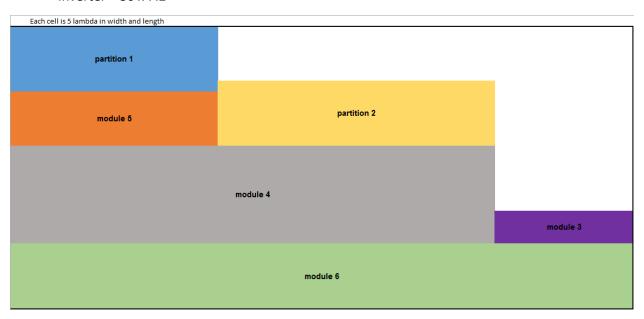
1)

Partition 1 (30 x 26.4 => 30 x 30):

- $3 \text{ input AND} = 30 \times 19.2$
- Inverter = 30 x 7.2

Partition 2 (30 x 38.4 => 30 x 40):

- 3 input XOR = 30 x 31.2 {XOR2 cell size, No XOR3 in standard cell library}
- Inverter = 30 x 7.2

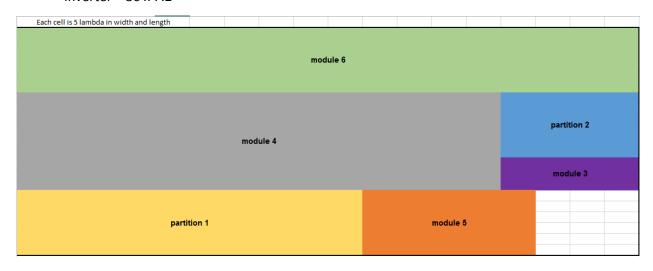


Partition 1 (30 x 47.1 => 30 x 50):

- 2 input XOR = 30 x 31.2
- 2 input OR = 30 x 15.9

Partition 2 (30 x 16.8 => 30 x 20):

- 2 input NAND = 30×9.6
- Inverter = 30×7.2

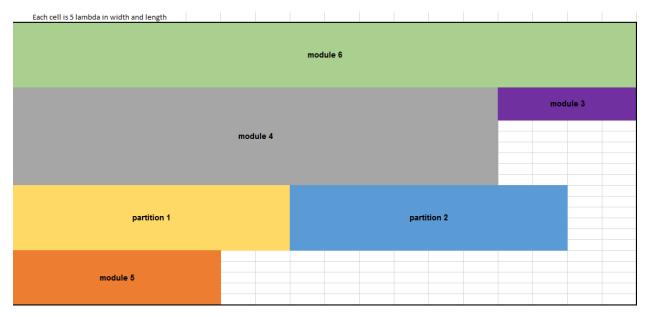


Partition 1 (30 x 37.5 => 30 x 40):

- 2 input NAND = 30×9.6
- 2 input NAND = 30×9.6
- 2 input NOR = 30×8.7
- $2 \text{ input NAND} = 30 \times 9.6$

Partition 2 (30 x 37.5 => 30 x 40):

- $2 \text{ input NAND} = 30 \times 9.6$
- 2 input NAND = 30×9.6
- 2 input NOR = 30 x 8.7
- $2 \text{ input NAND} = 30 \times 9.6$

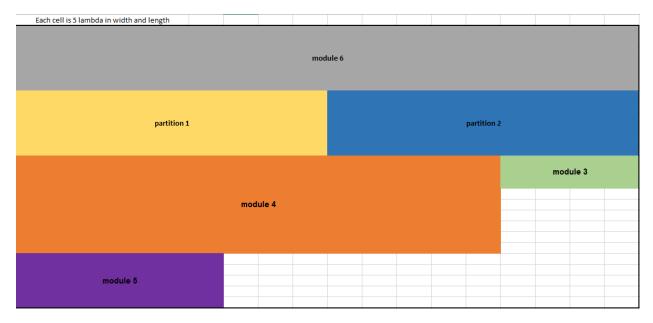


Partition 1 (30 x 48 => 30 x 50):

- Inverter = 30×7.2
- Inverter = 30×7.2
- 2 input NAND = 30×9.6
- $2 \text{ input NAND} = 30 \times 9.6$
- Inverter = 30×7.2
- Inverter = 30 x 7.2

Partition 2 (30 x 42.3 => 30 x 45):

- Inverter = 30×7.2
- $2 \text{ input NAND} = 30 \times 9.6$
- 2 input NOR = 30×8.7
- 2 input NAND = 30×9.6
- Inverter = 30×7.2



Partition 1 (30 x 73.5 => 30 x 75):

- Inverter = 30×7.2
- Inverter = 30 x 7.2
- Inverter = 30 x 7.2
- Inverter = 30 x 7.2
- Inverter = 30×7.2
- 2 input NAND = 30×9.6
- 2 input NAND = 30×9.6
- 2 input NAND = 30×9.6
- 2 input NOR = 30×8.7

Partition 2 (30 x 213.6 => 30 x 215):

- DFF = 30 x 72
- TFF = 30×88.8
- BUFFER = 30 x 12
- Inverter = 30×7.2
- Inverter = 30×7.2
- $2 \text{ input AND} = 30 \times 16.8$
- 2 input NAND = 30×9.6

Each cell is 5 lambda in width and length		
	module 6	module 5
partition 2		
	module 4	
	partition 1	
module 3		