$CIS4930.002 \ ext{VLSI Design Automation} \ ext{Summer C} - 2015$

Homework 2

Due: Saturday, June 6th, 2015 Submit PDF to Canvas by 11:59 PM

All Problems are worth 20pts unless otherwise specified

- (1) Perform Partitioning for Fig. 2.22 (pg. 86).
- (2) Perform Partitioning for Fig. 2.23 (pg. 86).
- (3) Perform Partitioning for Fig. 2.24 (pg. 87).
- (4) Perform Partitioning for Fig. 1 (Attached).
- (5) Perform Partitioning for Fig. 2 (Attached).
- (6) Perform Partitioning for Fig. 3 (Attached, Optional, 10pts EC)
- (7) Perform Partitioning for Fig. 4 (Attached, Optional, 10pts EC)

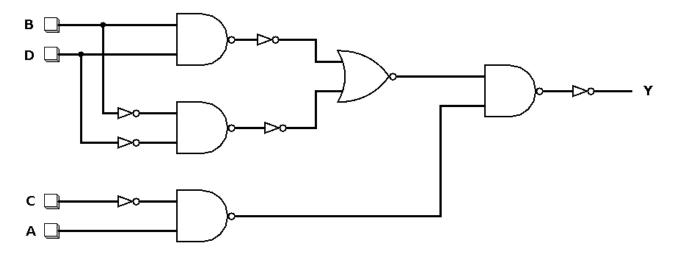


Figure 1: Circuit for Problem 4

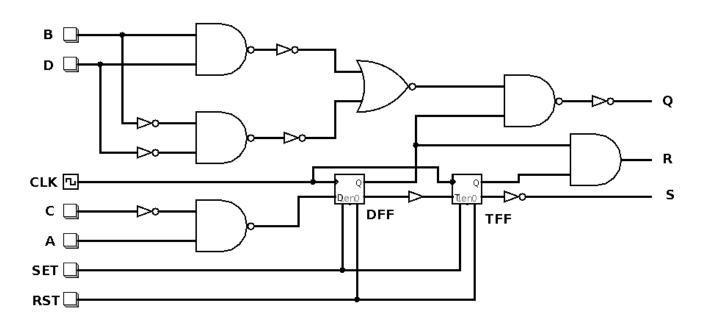


Figure 2: Circuit for Problem 5

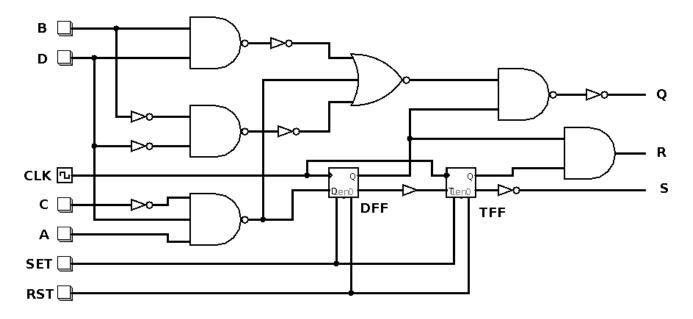


Figure 3: Circuit for Problem 6

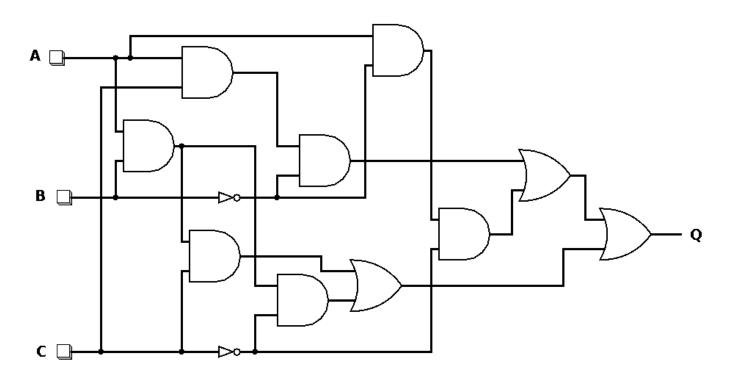


Figure 4: Circuit for Problem 7