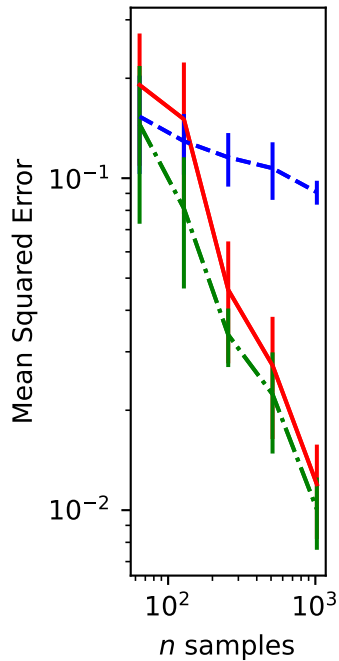
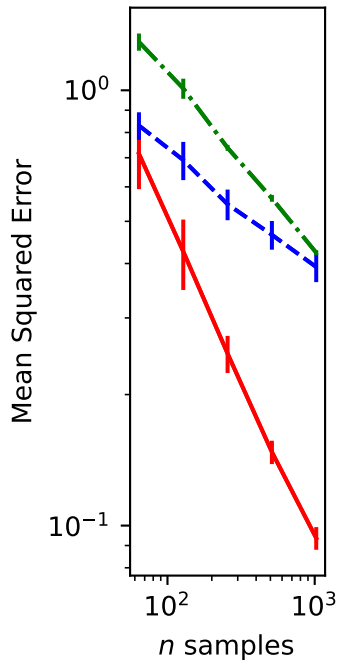


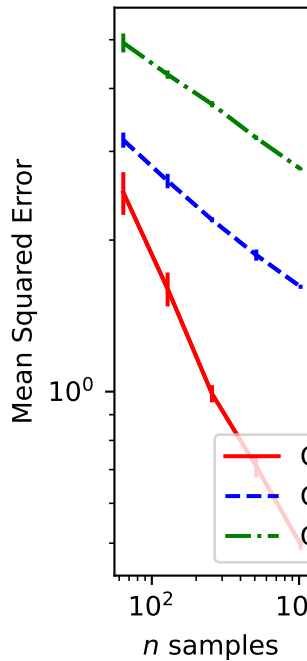
d=2



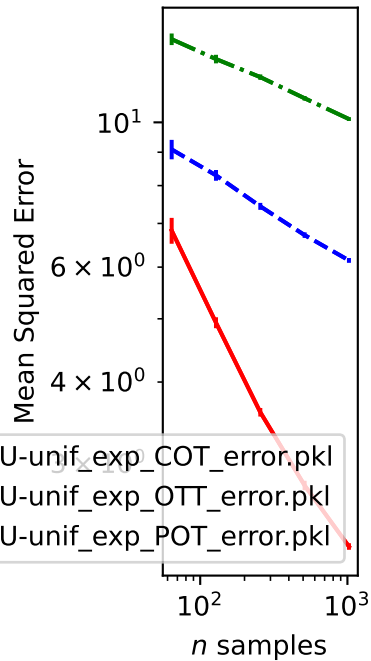
d=5



d=10



d=20



— CPU-unif\_exp\_COT\_error.pkl  
- - CPU-unif\_exp\_OTT\_error.pkl  
- . CPU-unif\_exp\_POT\_error.pkl