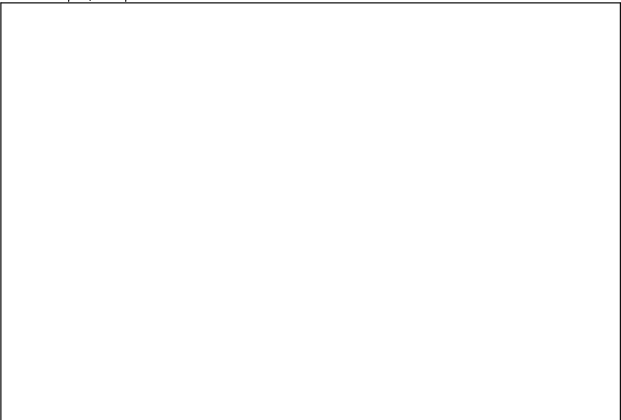


Sheet: Input/Output



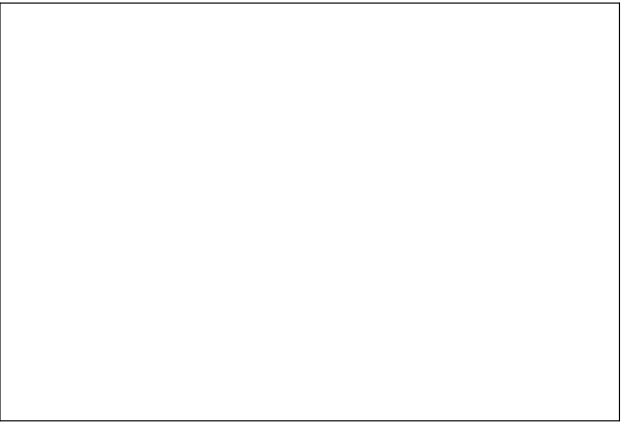
File: IO.sch

Sheet: SPRAM

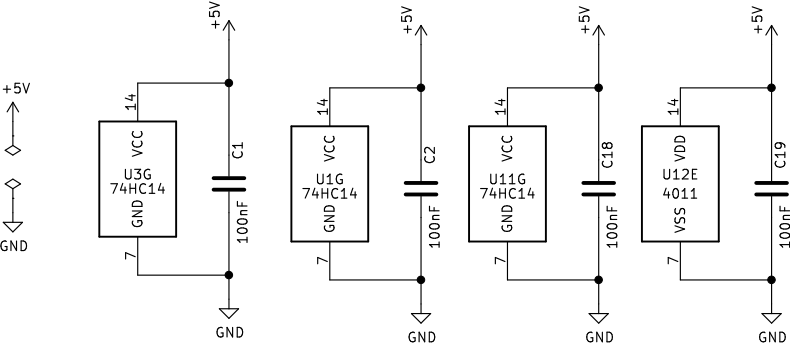


File: SPRAM.sch

Sheet: CPU



File: CPU.sch



REV.C

Nicola Cimmino

Sheet: /

File: plc14500.sch

Title: PLC14500 – Nano

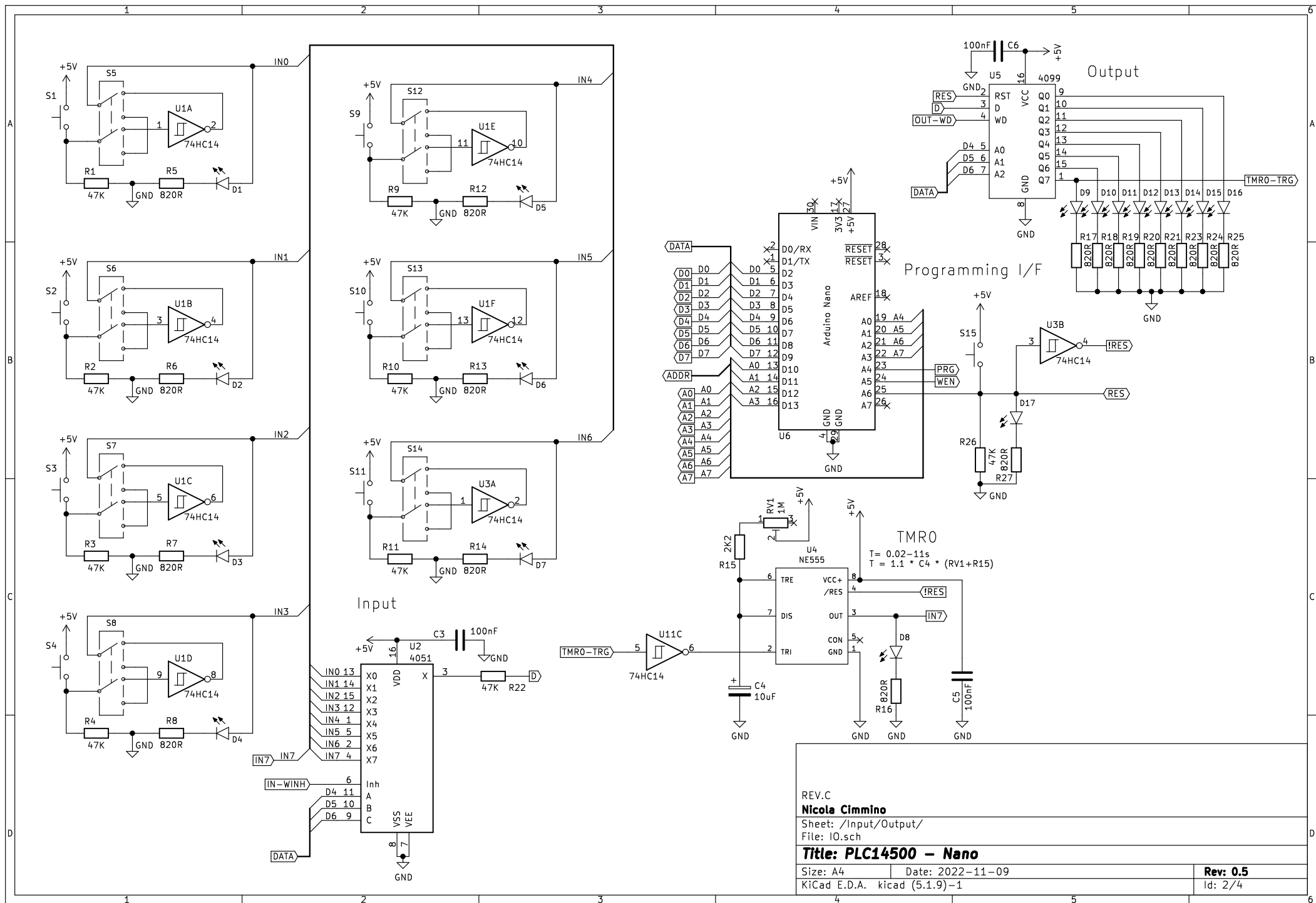
Size: A4

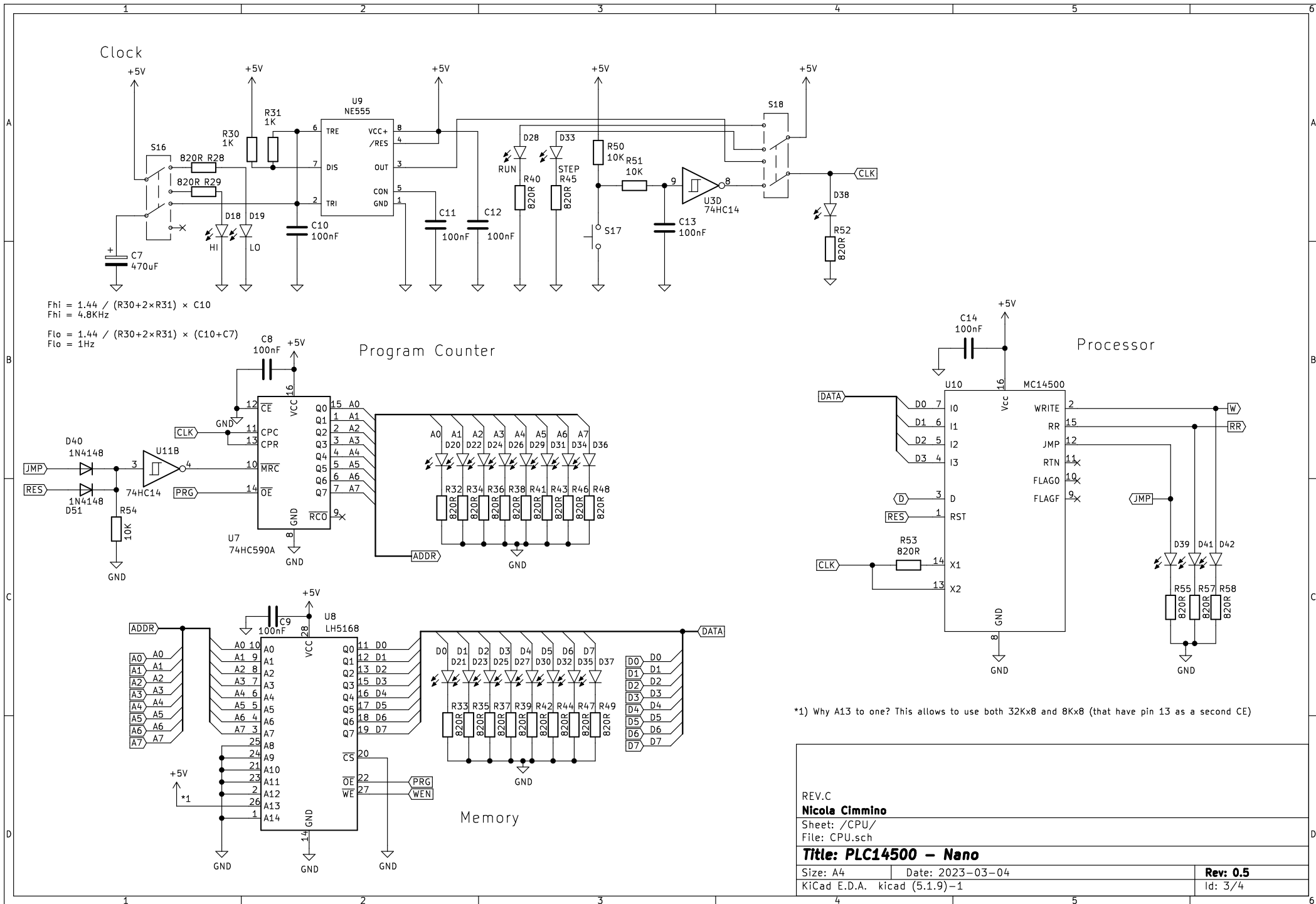
Date: 2022-11-09

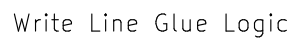
Rev: 0.5

KiCad E.D.A. kicad (5.1.9)-1

Id: 1/4







*1) Prior to REV.C this was a generic SPR and connected to Q7 of U13

V	D7	OUT-VD	SRAM-VD	IN-WINH	SRAM-WINH	
0	0	1	1	1	0	Read Scratchpad RAM
0	1	1	1	0	1	Read Inputs
1	0	1	0	1	1	Write Scratchpad RAM
1	1	0	1	1	1	Write Outputs

Rev: 0.5
Id: 4/4