

The schematic diagram shows the SI5351 clock generator circuit. The SI5351 chip has pins SCL, SDA, GND, VIN, and VOUT. SCL is connected to SCL\_B P2 (Orange). SDA is connected to SDA\_B P23 (Yellow). GND is connected to GND. VIN is connected to a 5V regulator (REG.) which is powered by 9.2V TO 5V and has a GND connection. VOUT is connected to CLK0 and CLK1. CLK0 is connected to DDS TO RADIO WFO IN. CLK1 is connected to DDS TO RADIO CAR IN. Pin 21 is connected to Pin 20.

