

Cambridge International Examinations

Cambridge International Advanced Subsidiary and Advanced Level

COMPUTER SCIENCE 9608/31

Paper 3 Written Paper May/June 2017

MARK SCHEME
Maximum Mark: 75

Published

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Question	Answer	Marks
1(a)(i)	DECLARE Book : LibraryBookRecord	1
1(a)(ii)	Book.Title ← "Dune"	1
1(b)	TYPE LibraryBookRecord DECLARE ISBN : INTEGER DECLARE Title : STRING DECLARE Genre : (Fiction, Non-Fiction) 1 DECLARE NumberOfLoans : 1 99 1 ENDTYPE mark for correct declaration and first two fields (note: only if attempt at modification) 1	3
1(c)(i)	6715	1
1(c)(ii)	8216	1
1(c)(iii)	88	1
1(c)(iv)	FALSE	1
1(d)(i)	Temp2 ← 22	1
1(d)(ii)	IntPointer ← @Temp1	1
1(d)(iii)	IntPointer^ ← Temp2	1

© UCLES 2017 Page 2 of 7

Question	Answer					
2(a)(i)	Worm					
2(a)(ii)	Phishing			1		
2(a)(iii)	Malicious software into a file of data	e that replicates by inserting a copy of itself (1) (1)		2		
2(b)	Regular virus sca Operating system Attachments/susp	Example: No <u>up-to-date</u> anti-virus (or equivalent) software Regular virus scans not performed Operating system not up-to-date Attachments/suspicious links clicked on 1 mark for any valid vulnerability				
2(c)(i)	public	public				
2(c)(ii)	Bob sends his <u>digital certificate</u> Digital certificate contains Bob's public key Successful decryption of certificate using CA's public key provides legitimacy 1 mark for any valid point – max 2					
2(c)(iii)	The person performing the action	What that person does		4		
	Anna	Requests Bob's public key.				
	Bob	Sends Anna his public key.	1			
	Anna	Encrypts email with Bob's public key.	1			
	Anna	Sends the email to Bob.				
	Bob	Decrypts email. Using his private key.	1 1			

© UCLES 2017 Page 3 of 7

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Question	Answer							Marks			
3(a)	X = A.(B̄ + (E B.C B̄ + B.C A.	3 . C))								1 1 1	3
3(b)	Α	В	С		,	Workir	ng Spa	ce	Х		2
	0	0	0						0		
	0	0	1						0		
	0	1	0						0		
	0	1	1						0		
	1	0	0						1		
	1	0	1						1		
	1	1	0						0		
	1	1	1						1		
	1 mark first fo	our entri	es, 1	mark	for the	last fo	our entr	ies			
3(c)(i)											1
						A	λB				
			_		00	01	11	10			
			С	0	0	0	0	1			
			C	1	0	0	1	1			
3(c)(ii)											2
						A	В				
					00	01	11	10]		
			,	0	0	0	0	1	\		
			С	1	0	0	1	1			
3(c)(iii)	X = A.B + A.					<u> </u>			4		2
(6)()	1 1										_
3(d)	$X = A.(\overline{\underline{B}} + (E))$										2
	X = A.(B + C) X = A.B + A.C)				4 /	don - :	land :		1	_
	Y = A'R + Y'	C				1 (uepend c	ient ma outcom	ork – must be co e from previous	line)	

© UCLES 2017 Page 4 of 7

Question	Answer				
4(a)	Example: Speed of access Just used as a look-up file No need for any serial or sequential processing 1 mark for any valid point				
4(b)(i)	CustomerID	RecordKey		1	
	802139	2139			
	700004	4			
	689998	89998			
	102139	2139			
4(b)(ii)	Minimum value: Maximum value:		1 1	2	
4(b)(iii)	RecordKey Success ← // Find p REPEAT IF rec THE ELS	Custome -FALSE osition for ord at posi N Insert new Success ← T E IF RecordKe THEN Recor ELSE Recor ENDIF Cess = TRUE	ey = 99999 cdKey ← 0 cdKey ← RecordKey + 1	4	
4(c)(i)		PINs are trans	PIN cannot be used mitted and compared	Max 2	
4(c)(ii)	6. PIN is ch	enters PIN PIN is enci ID is hashe record is l ecked agair	rypted	3	

© UCLES 2017 Page 5 of 7

Question	Answer	Marks
5(a)(i)	Packet: Both web page and web page request are split into packets Each packet is sent individually from device to device	
5(a)(ii)	Router: Transmit packets Contain connections to many other routers When packets arrive at router, router decides where next to send packet 1 mark for any valid point	Max 2
5(a)(iii)	TCP/IP: Is the protocol Rules for communication between web server and browser	
5(b)(i)	Two from: Picture and sound not synchronised Interruptions // video not continuous Can be degraded by other competing traffic	
5(b)(ii)	Dedicated communications channel between the two communicating devices a Established prior to start of communication // removal of links at end of communication	
5(b)(iii)	In packet switching, packets can take different routes and may not arrive in order Will arrive in order (only one route) As packets can take many different routes / share paths with others can be delayed Dedicated circuit has full bandwidth No loss of synch 1 mark for any valid point	Max 3

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Question	Answer	Marks
6(a)(i)	Control system	1
6(a)(ii)	Use of actuators means that the system is controlling	1
6(b)	System wastes processor time checking for values that are not changing Some sensor input needs to be acted upon immediately 1	2
6(c)(i)	Interrupts need to be disabled so that the process of dealing with an interrupt is itself not interrupted	1
6(c)(ii)	After handling the interrupt interrupts need to be enabled so that further interrupts can be dealt with	1
6(c)(iii)	Content of registers 1 Placed on stack 1	2
6(c)(iv)	Changing sensor value dealt with as soon as it happens 1 Processor needs to check sensor only when an interrupt occurs 1	2
6(c)(v)	AND #B0000001000000000 // AND #&0200 // AND #512 Op code	2

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