## **Trigger Routing Unit**

The trigger routing unit (TRU) provides simple sequence control of distributed modules without the penalties assocaiated with core intervention

Base Address: 32'h1000\_0000

Size(bytes): 0x2020

## **Registers List**

Offset	Identifier	Name
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2000	TRU_SSRN	Slave Select Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2004	TRU_MTR	Master Trigger Register

Offset	Identifier	Name
32'h0000_2004	TRU_MTR	Master Trigger Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2010	TRU_ERRADDR	Error Address Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2014	TRU_STAT	Status Information Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register

Offset	Identifier	Name
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_2018	TRU_GCTL	Global Control Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register
32'h0000_201c	TRU_RXDATA	Receive Data Register