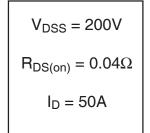
International Rectifier

IRFP260NPbF

HEXFET® Power MOSFET

- Advanced Process Technology
- Dynamic dv/dt Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Ease of Paralleling
- Simple Drive Requirements
- Lead-Free

G



Description

Fifth Generation HEXFETs from International Rectifier utilize advanced processing techniques to achieve extremely low on-resistance per silicon area. This benefit, combined with the fast switching speed and ruggedized device design that HEXFET Power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in a wide variety of applications.

The TO-247 package is preferred for commercial-industrial applications where higher power levels preclude the use of TO-220 devices. The TO-247 is similar but superior to the earlier TO-218 package because of its isolated mounting hole.



Absolute Maximum Ratings

	Parameter	Max.	Units
I _D @ T _C = 25°C	Continuous Drain Current, V _{GS} @ 10V	50	
I _D @ T _C = 100°C	Continuous Drain Current, V _{GS} @ 10V	35	Α
I _{DM}	Pulsed Drain Current ①	200	
P _D @T _C = 25°C	Power Dissipation	300	W
	Linear Derating Factor	2.0	W/°C
V_{GS}	Gate-to-Source Voltage	±20	V
E _{AS}	Single Pulse Avalanche Energy ^②	560	mJ
I _{AR}	Avalanche Current①	50	Α
E _{AR}	Repetitive Avalanche Energy①	30	mJ
dv/dt	Peak Diode Recovery dv/dt ③	10	V/ns
TJ	Operating Junction and	-55 to +175	
T _{STG}	Storage Temperature Range		°C
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)	
	Mounting torque, 6-32 or M3 srew	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Тур.	Max.	Units
R _{θJC}	Junction-to-Case		0.50	
R _{θCS}	Case-to-Sink, Flat, Greased Surface	0.24		°C/W
$R_{\theta JA}$	Junction-to-Ambient		40	



Electrical Characteristics @ $T_J = 25$ °C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions	
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	200			V	$V_{GS} = 0V, I_D = 250\mu A$	
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		0.26		V/°C	Reference to 25°C, I _D = 1mA	
R _{DS(on)}	Static Drain-to-Source On-Resistance			0.04	Ω	V _{GS} = 10V, I _D = 28A ④	
V _{GS(th)}	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
g _{fs}	Forward Transconductance	27			S	V _{DS} = 50V, I _D = 28A ⊕	
1	Drain-to-Source Leakage Current			25	μA	V _{DS} = 200V, V _{GS} = 0V	
I _{DSS}	Brain to Godice Leakage Guiterit			250	μΑ	$V_{DS} = 160V, V_{GS} = 0V, T_{J} = 150^{\circ}C$	
1	Gate-to-Source Forward Leakage			100	nA	V _{GS} = 20V	
I _{GSS}	Gate-to-Source Reverse Leakage			-100	l IIA	V _{GS} = -20V	
Qg	Total Gate Charge			234		I _D = 28A	
Q _{gs}	Gate-to-Source Charge			38	nC	V _{DS} = 160V	
Q _{gd}	Gate-to-Drain ("Miller") Charge			110		V _{GS} = 10V ④	
t _{d(on)}	Turn-On Delay Time		17			V _{DD} = 100V	
t _r	Rise Time		60			$I_D = 28A$	
t _{d(off)}	Turn-Off Delay Time		55		ns	$R_G = 1.8\Omega$	
t _f	Fall Time		48			V _{GS} = 10V ④	
	Internal Drain Inductance		5.0			Between lead,	
L _D	memai Drain Inductance		5.0				6mm (0.25in.)
L _S	Internal Source Inductance		13		nH	from package	
						and center of die contact	
C _{iss}	Input Capacitance		4057			V _{GS} = 0V	
C _{oss}	Output Capacitance		603		pF	$V_{DS} = 25V$	
C _{rss}	Reverse Transfer Capacitance		161			f = 1.0MHz	

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions					
Is	Continuous Source Current	50	- 50	50 A	MOSFET symbol						
	(Body Diode)				showing the						
I _{SM}	Pulsed Source Current		200		000	200	200	200	200	/\	integral reverse
	(Body Diode)①			- 200	200	p-n junction diode.					
V_{SD}	Diode Forward Voltage			1.3	V	$T_J = 25^{\circ}C$, $I_S = 28A$, $V_{GS} = 0V$ ④					
t _{rr}	Reverse Recovery Time		268	402	ns	$T_J = 25^{\circ}C, I_F = 28A$					
Q _{rr}	Reverse Recovery Charge		1.9	2.8	μС	di/dt = 100A/µs ④					
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L _S +L _D)									

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature.
- $\begin{tabular}{ll} \hline @ Starting $T_J=25^\circ$C, $L=1.5mH$\\ $R_G=25\Omega, I_{AS}=28A.$ \end{tabular}$
- $\label{eq:loss} \begin{array}{l} \text{ } \exists \ \ I_{SD} \leq 28A, \ di/dt \leq 486A/\mu s, \ V_{DD} \leq V_{(BR)DSS}, \\ T_{J} \leq 175^{\circ}C \end{array}$
- 4 Pulse width $\leq 400 \mu s$; duty cycle $\leq 2\%$.

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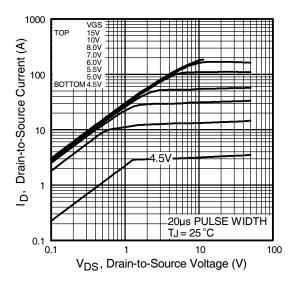


Fig 1. Typical Output Characteristics

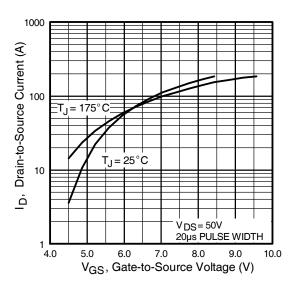


Fig 3. Typical Transfer Characteristics

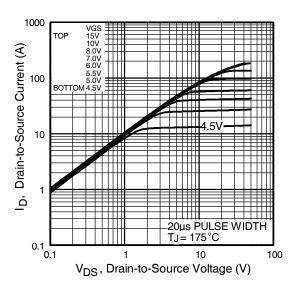


Fig 2. Typical Output Characteristics

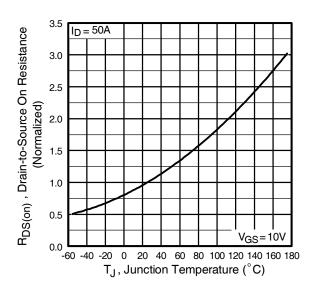


Fig 4. Normalized On-Resistance Vs. Temperature

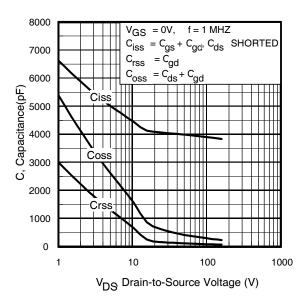
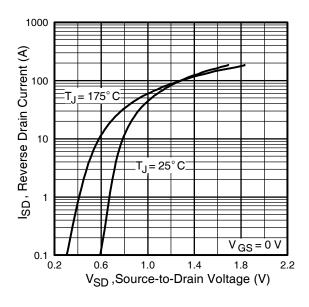


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage



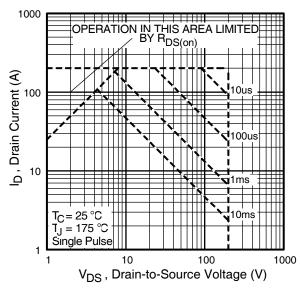
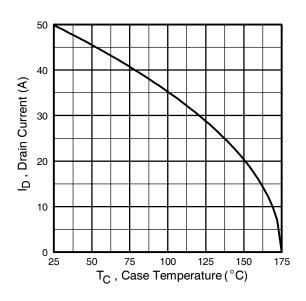


Fig 7. Typical Source-Drain Diode Forward Voltage

Fig 8. Maximum Safe Operating Area

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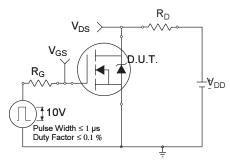


Fig 10a. Switching Time Test Circuit

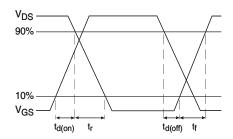


Fig 9. Maximum Drain Current Vs.
Case Temperature

Fig 10b. Switching Time Waveforms

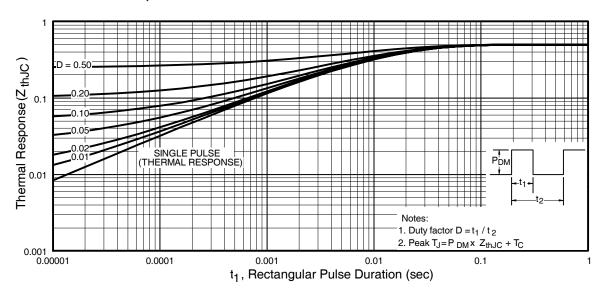


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

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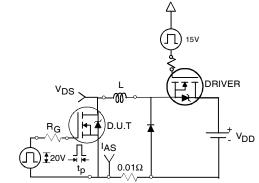


Fig 12a. Unclamped Inductive Test Circuit

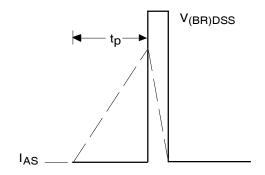


Fig 12b. Unclamped Inductive Waveforms

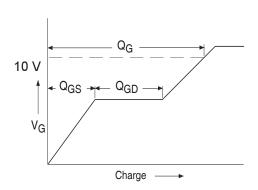


Fig 13a. Basic Gate Charge Waveform

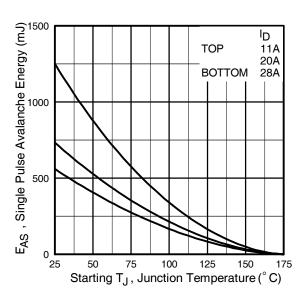


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

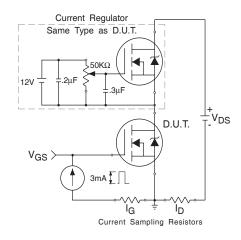
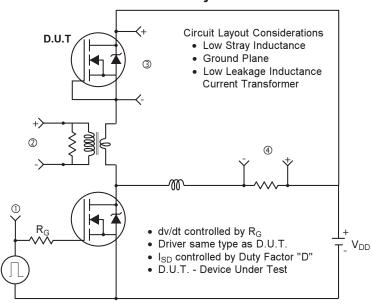
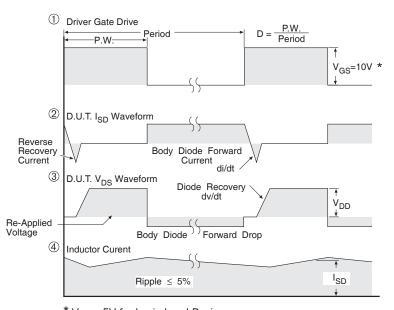


Fig 13b. Gate Charge Test Circuit

Peak Diode Recovery dv/dt Test Circuit





* V_{GS} = 5V for Logic Level Devices

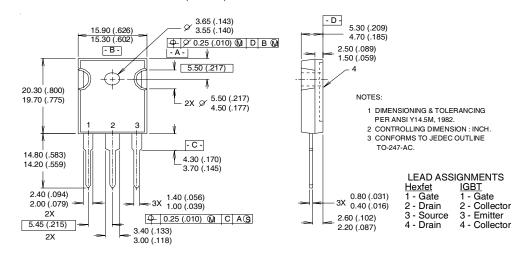
Fig 14. For N-Channel HEXFETS

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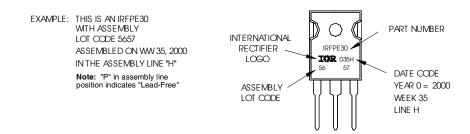
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TO-247AC Package Outline

Dimensions are shown in millimeters (inches)



TO-247AC Part Marking Information



Notes:

- 1. For an Automotive Qualified version of this part please see http://www.irf.com/product-info/auto/
- 2. For the most current drawing please refer to IR website at http://www.irf.com/package/

Data and specifications subject to change without notice.



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