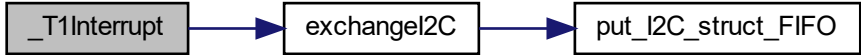


_T1Interrupt



```
graph LR; A[_T1Interrupt] --> B[exchangeI2C]; B --> C[put_I2C_struct_FIFO];
```

The diagram illustrates a three-step process flow. It begins with a gray rectangular box labeled '_T1Interrupt'. A dark blue arrow points from this box to a white rectangular box labeled 'exchangeI2C'. Another dark blue arrow points from the 'exchangeI2C' box to a final white rectangular box labeled 'put_I2C_struct_FIFO'. All boxes have a black border.

exchangeI2C

put_I2C_struct_FIFO