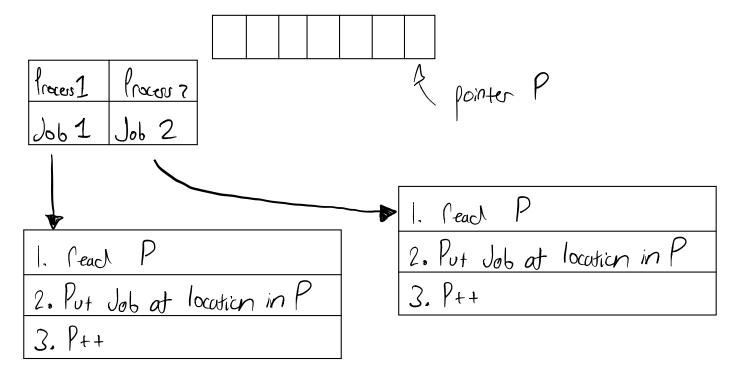
9/17/18

Day 1

· Events

Mutual exclusion - Chapter 5

Suppose we have a print spooter (queue for print)



Note if a timeout occurs after reading P on ever occurs where one will write over the other this happens when revolver ore shared

Critical section the arm where we do not want to edit memory. Program won't have infinite loops in critical sections.

How do we avoid this?

Process 1	Procus 2
white true	white true
\	\
while (turn == 0);	while (turn == 1);
{ (Critical code)	{ (Critical code >
turn = 1;	turn = 0:
}	}
}	}

// is this mutually exclusive.?

We cannot assume that both processes are taking the sume time, so we do have Mutual exclusion but we have to take term through Strict attention.

here both processes could grat the flag of the some time if they small at the lane time

	Process 1	Procus 2
۱.	flag[0] - tre	flag[1] = tre
	while flag [1] do	while flag [1] do
	(Critical Section)	(Critical Section)
	flag [0] = Jase	flag [1] = fase
		; J

If ofter line 1 a time out occur we can deadlock

· hace cardition - when things happen they may or may not work.

hard to recreate.

$$\int \frac{1}{e^{x^2}} = e^{y} \quad y = -2x \, dx$$

$$\frac{dy}{-2x} = \frac{dx}{-2x}$$

$$\lim_{x \to \infty} \frac{e^{-x^2}}{-2x} = \frac{1}{-2xe^{x^2}}$$

Grading Critain Paper

· Unitied

· to the point

· Logical

· Correct in grammar

· appropriate to audience

Dead lock

· For the next test we need to be ready to room

pstedo code determy to code is metally
excluse, cover on European

defire TRUE

locer of	Process 1
whire true	White true
{ Huy [0] = +(ve	{ Huy [1] = +(ve
while (flug[1])	while (flug[o])
{ if (turn=1)	{ if (turn=0)
{ Huy[o]= False	{ Huy[1]= Falm
White (term ==1)°	White (tern==0)°
fly [O] = true	flag [1] = true
}	}
}	}
<pre><critical sect=""></critical></pre>	<pre><critical sect=""></critical></pre>
turn = 1	turn = 0
flug [0] = Jak	flug[1] = take
J:	
}	}

// this method works

// Dekker's Algorithm

Next paper

- their methods in distributed environments

- process near to communicate to be
able to control across to resource.

- It: token my

Po flag [0] = tore turn = 1 white (flags/2) If turn == 1) // busy wait only wron both (C. S.) // true can we proceed flag [0] = farc : Petruni Algorithm

TODO: he is gary to rearrage on of the

Hw 1 - Suffue colvanos #7, Chapter 5 pg. 249 "Works for any number of processes

```
· Harducre Solution
     - disable interopts
          · Not a good idea
     - Special instructions - Machine instruction
          · instructions connot be interrupted
  Exampre
         Test and Set instruction psuedo Coche
         boolean test set (inti)
         { if (i==0)
{ (=1
           seturn true;
           esc
         { [eturn truse; }
        int Golt;
        while tre
        { whire (!tuted (bolt)); // busy wait Cock
          Critical section
          601+ = 0;
       3
```

Exchange instruction - Intel

Psuedo coore

Void exchange (int legister, int Memory)

{ int temp
 tem = Memory;
 Memory = legister;
 (register = temp;
}

// Hw how would this work or a lock

HW