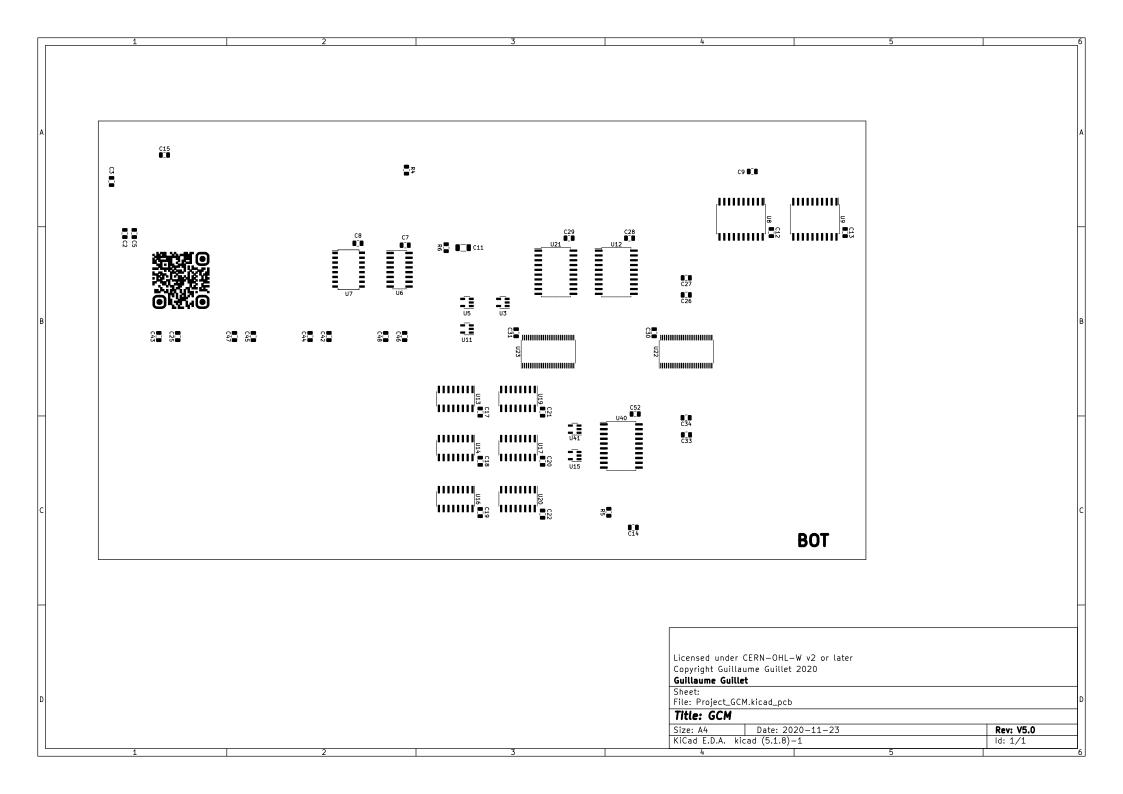
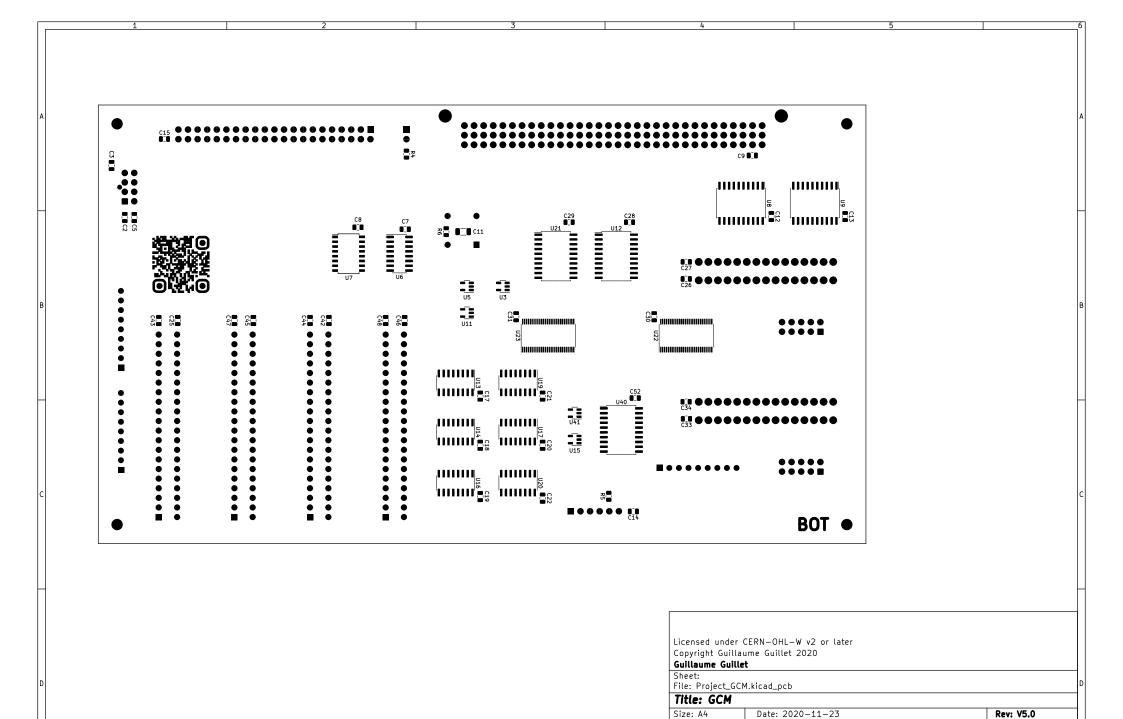


Debug buses GND 5V MOM1\_SPS1 U35 0-0 C39 C35 U28 ... C16 U26 C24 U27 U37 C50 U31 C37 U36 0 0 U34 C40 MM1 slot 1 U25 U30 C4 • Р3 P2 Р1 P0 MM1 slot 1 address extension RNZ • U10 0.0 ••••••• ••••• • • MM1 slot 2 U42 1 • C54 U43 ■ ... •••• • • • RN3 • lacktrian• MM1 slot 2 address extension • • •••• ě • • •••••**=** • 111 EEU • • SPI0 ě • • • GCM -**V5.0** • • TOP CS SCLK MOSI Licensed under CERN-OHL-W v2 or later Copyright Guillaume Guillet 2020 Guillaume Guillet Sheet: File: Project\_GCM.kicad\_pcb Title: GCM Size: A4 Date: 2020-11-23 Rev: V5.0 KiCad E.D.A. kicad (5.1.8)-1 ld: 1/1





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