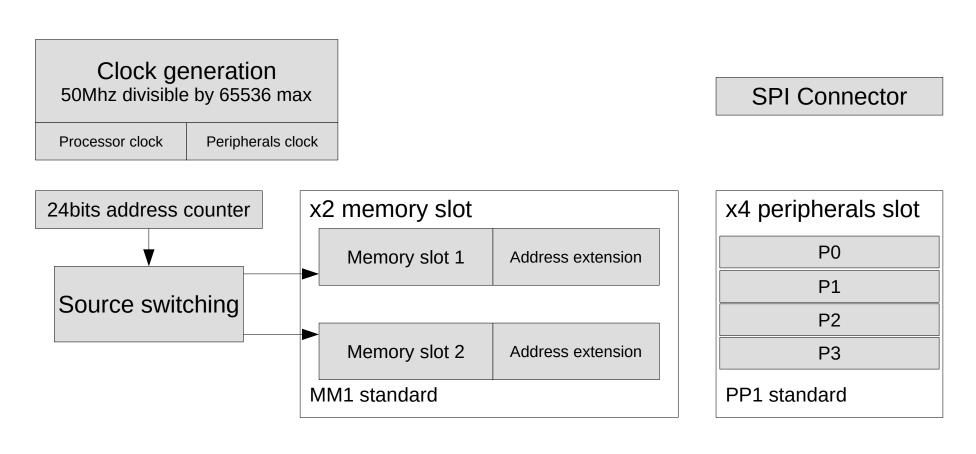
Block diagram – GCM_V5 – Revision 2

Main connector
MOM1_SPS1
standard

Peripheral communication
4 peripherals
2 writable bus
2 readable bus

Processor clock
SPI

24Bits address/jump bus
~16MByte



Features depend of the processor