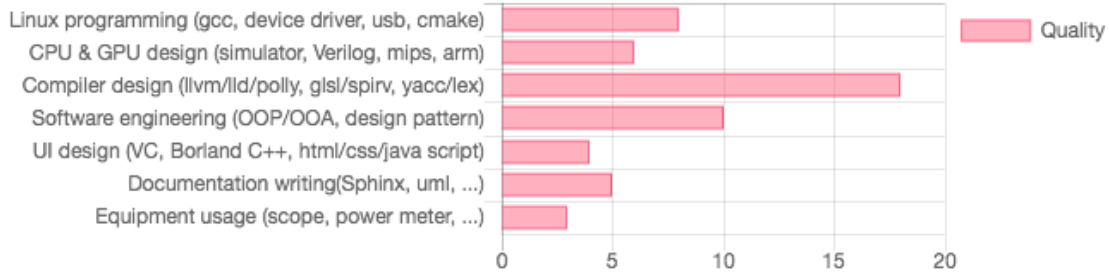




Chung-Shu Chen 陳鍾樞

QUALIFICATION

Compiler developer with good experience in llvm cpu and gpu backend, lld linker, npu/onnx, c++, OpenGL/gsl and simulator for 8 years, over 20 years experience in c/c++ programming and research in parallel processing for master degree.

RESUME**SKILLS****MY OPEN SOURCE PROJECT**

I am proud of my work is accepted by LLVM documentation, appears at <http://llvm.org/docs/tutorial/#external-tutorials>

Tutorial: Create an LLVM Backend compiler <http://jonathan2251.github.io/lbd/index.html>

Tutorial: Create an LLVM Backend Toolchain <http://jonathan2251.github.io/lbt/index.html>

The concept of GPU compiler <http://jonathan2251.github.io/lbd/gpu.html>

EDUCATION

1997-1999 Master, June 1999, National Taiwan Normal University (國立台灣師範大學), Taipei, Major: Information Science.

1991-1994 B.S., June 1994, National Taiwan Technology University of Science and Technology (國立台灣科技大學), Taipei, Major: Industry Engineer.

LICENSE

Taiwan National Computer Engineer license, 1995 高考資訊技師及格.

EXPERIENCE**THESIS OF MASTER DEGREE**

[The Researches of Column Sort and Related Problems](#) [at library](#)

[Publish Conference: 陳鍾樞, 林順喜, 2000, 行排列法簡化步驟之研究, 2000網際網路與分散式系統研討會, 台南成功大學, 台灣, 中華民國, Vol.2, 頁85-93。](#)

PROPOSAL OF PHD STUDY

[The Researches of Sorting Network and Related Algorithm](#)

Research report

[林順喜, 陳鍾樞, 賴有得, 1999, The Design and Research of Lower-Cost Sorting Networks, NSC88-2213-E003-002, Sponsored by Ministry of Science and Technology.](#)

[More detailed resume](#)