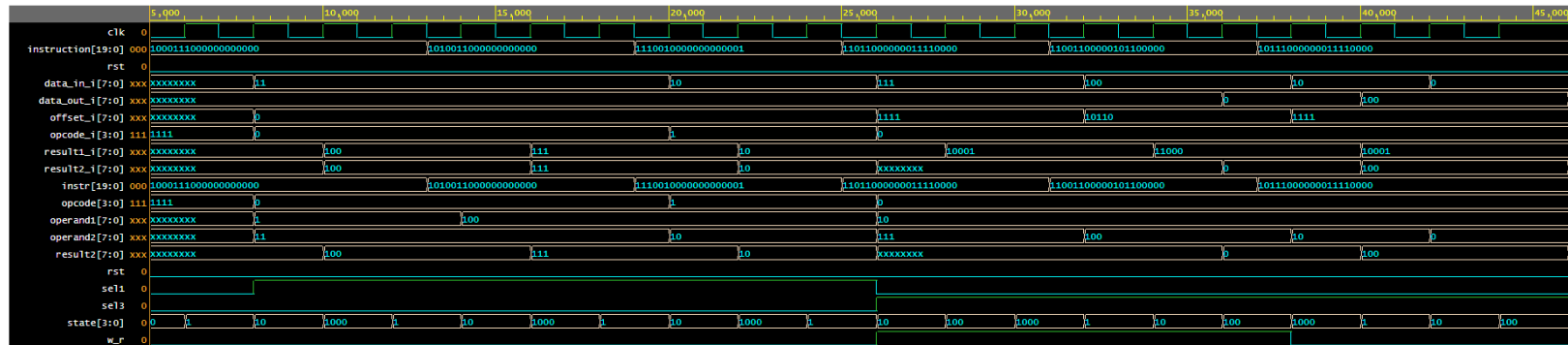# Prac 6: Simple CPU

Done By Jonathan Campbell and Akhil Jacob

EM Waveform generated by the code

```
end else if (instruction[19:18] == 2'b11) begin //storeR
    // My Code Start
    operand1 <= regfile[instruction[15:14]]; //X2
    operand2 <= regfile[instruction[17:16]]; //z
    offset <= instruction[11:4];
    opcode <= instruction[3:0];
    sel1 <= 0; //pass data_out
    sel3 <= 1; //pass offset
    w_r <= 1;
    // My Code Stop
end
```

Code added to CU to get the CPU to work

EDA Playground Link: https://www.edaplayground.com/x/8vTB

GITHUB link https://github.com/Jonathan5320/EEE3096S.git