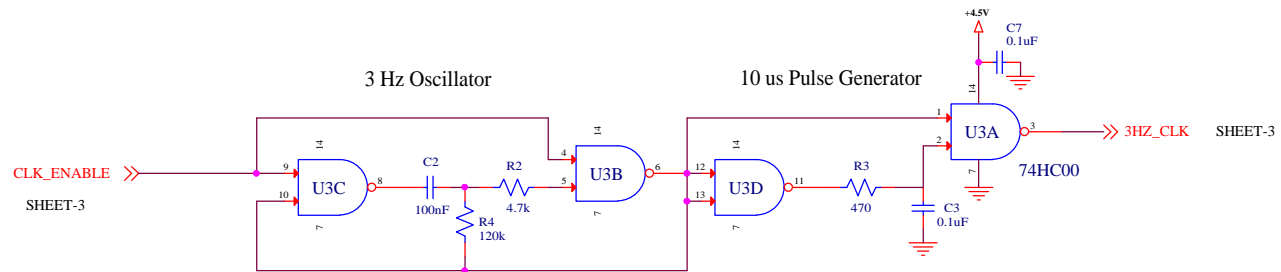
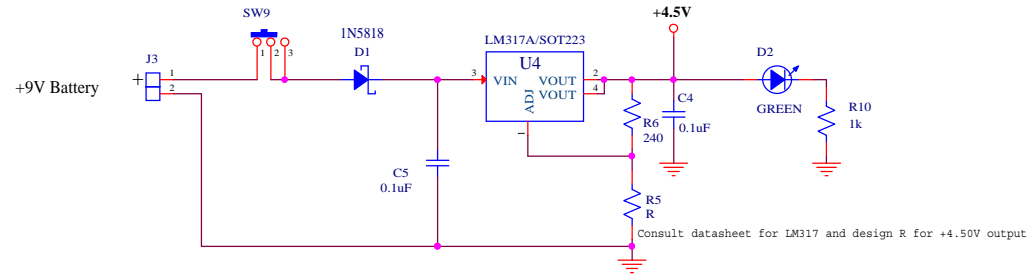


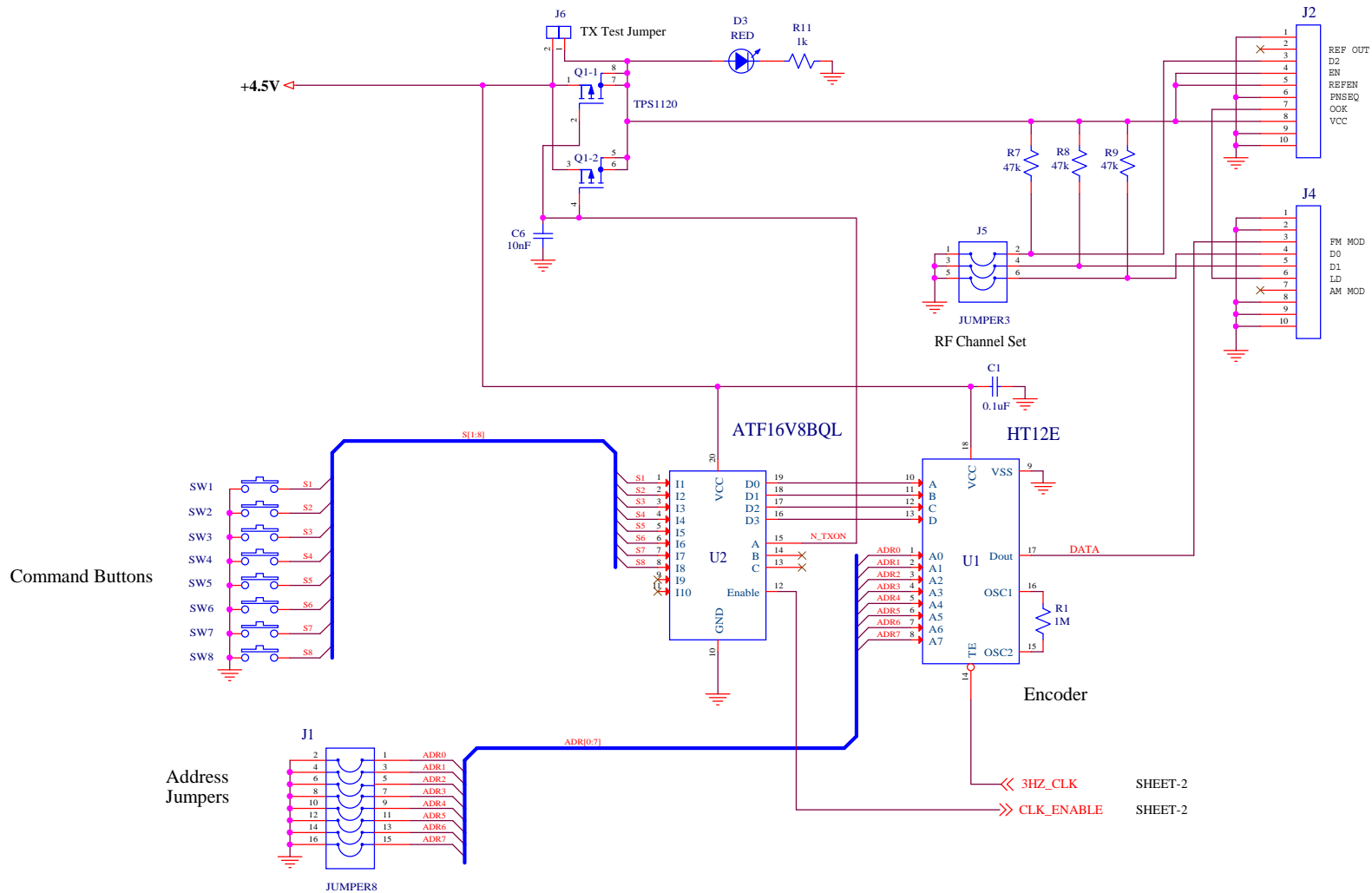
Project Revision	Date	Modifications
1.0	Aug 2009	Creation of Tutorial Project for CMPE174, F-2009 using Allegro Design Entry CIS (Cadence SPB 16.2).
1.1	Aug 2012	Updated U4 for correct VOUT labels for pins 2 and 4. These pins should both be passive to allow for duplicated names. Allegor will only flag a warning.
1.2	Oct 08 2016	Updated sheet-3, U1 pin 8 is no correctly defined as an input. It was an output.



CMPE 174, F2009 UCSC		Engr.: S.C. Petersen, Instructor	
Project Title Tutorial Handset Design		Page Title Revisions	
Size A	Project File Handset.opj	Project Rev	Page Rev 0.0
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EE 174, F2015 UCSC		Engr.: S.C. Petersen, Instructor	
Project Title Tutorial Handset Project		Page Title Power and Oscillator	
Size A	Project File Handset.opj	Project Rev 1.2	Page Rev 2.0 (16.6)
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Daughterboard for 915MHz transmitter

EE174, F2016 UCSC		Engr.: S.C. Petersen, Instructor	
Project Title Tutorial Handset Design		Page Title Wireless Transmitter	
Size A	Project File Handset.opj	Project Rev 1.2	Page Rev 0.0
Date: Saturday, October 08, 2016		Sheet 3 of 3	

