F = page fault

B = process blocked

R = process ready

Pa(b) = processed page b of process a

tim	e	0	1	2 3	4	5	6	7	8	9	10	11	12 13	14	15	16	17	18	19	20 2	1 22	2 23	24	25	26	27	28	29	30	31	32	33 3	4 35	36	37	38	39	40	41	42	43	44	45	46	47 4	18 4	19 50
	CPU events						P1(1)	P1F							P1(2)	P1F					P1(	3) <b>P1F</b>					Р	1(4)	P1F						P1(5)	)											
CDLL								P2(5)	P2F							P2(4)	P2F					P2(3	) P2F	=				Р	2(2) F	P2F						P2(1)	)										
CPU eV	ents	P3F							P3(1)	P3(1)	P3(1)	P:	3(1) P3(1	1) P3(1	.)		P3(1)								П												П										
		P4F										P4(1) F	4F					P4(2)	P4F					P4(3)	P4(2)	P4(1)	P4F					P4	(4) P4(	4) P(4)													
		P1B					P1R	P1B					P1F	2		P1B					P1	R P1B						P1R	P1B					P1R													
Duo o o o o o to t		P2B					P2R		P2B					P2R	2		P2B					P2R	P2E	3				Р	2R P	2B					P2R												
Process stat	us events	P3B					P3R																																								
		P4B					P4R					F	4B					P4R	P4B					P4R		P	4B					P4	IR.														
	P1																																														
Blocked	P2																																														
Indicator	Р3														ļ													_																			
	P4																																														

## Summary

PID	Turna	round time	Faults	Fault times
P1		38	5	0, 7, 16, 23, 30
P2		39	5	0, 8, 17, 24, 31
Р3		18	1	0
P4		37	4	0, 12, 19, 28
	avr:	33		