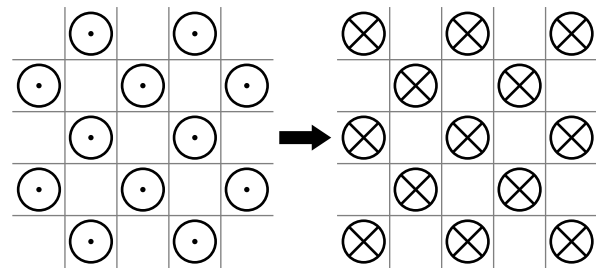


Input cycle A  
(bit 0)

First  
sub-step

Second  
sub-step



Input cycle B  
(bit 1)

First  
sub-step

Second  
sub-step

