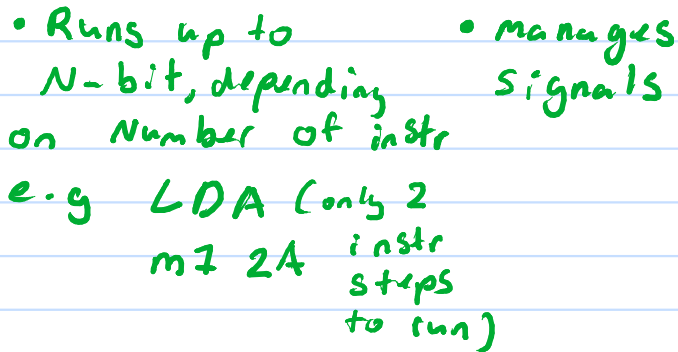


- Should be able to display prog memory, which is all controlled by the control unit.

## Fetch



machine

Decode - (instruction code into opcode)

e.g) LDA 10

"

M, 2A 001

W W W

- opCode

• If a Instruction n in the "IR" has an opcode - "machine code instr that specifies what will be run" of 3 operations.

- Then the "PC" in "CU" will run from 0-2 (i.e x3)

4 values will be stored to their particular modules, i.e (RAM)

example - load 3 led rows

$\therefore$  each row contains 1 instruction i.e.

"LDA 10"  $\Rightarrow$  0 0 0 0 ...

- Run Decode & execute to get

∴ (x3) Fetch → Decode → execute

IDA 10  $\rightarrow$  m., 2A  $\rightarrow$