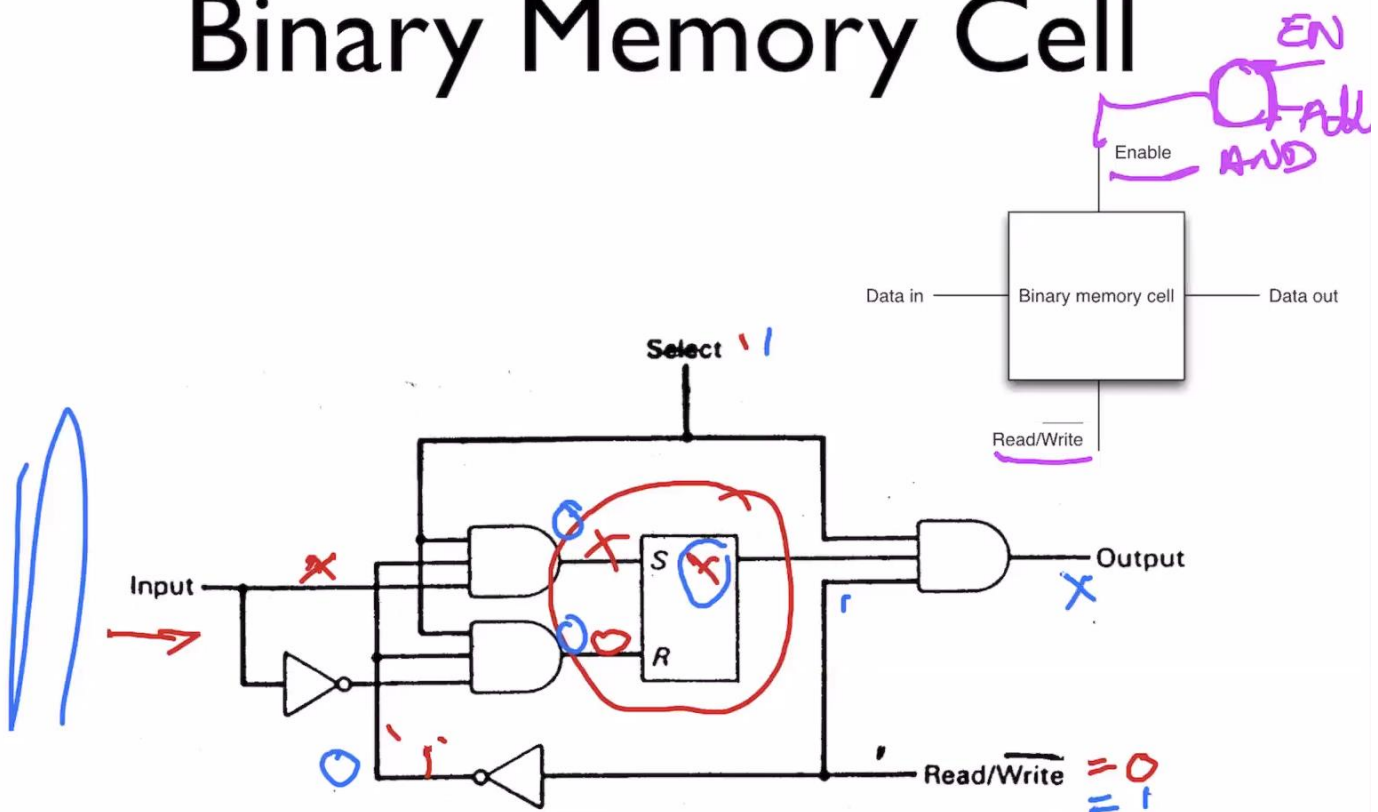
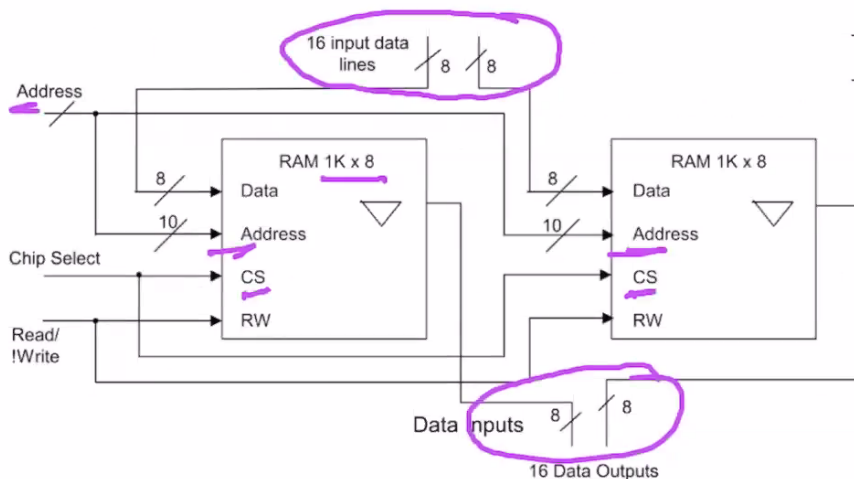


Binary Memory Cell



Memory Interfacing & Expansion

- Double word size:



Block diagram of a 1K x 8 RAM Chip

- Pin count gets high quickly.

two of 1K x 8
 → 1K x 16
 → 2K x 8