Analogue to digital Conversion ? quantisation Unitern quantisation (dividing signal into (2 ") equal interes) (Amplifude Ana) N-bit Uniform Quantisation -divides voltage into 2N agnal internals with quantization step

Rapges on Axis A = (Vmax-Vmin) quantization step Voltage interval [kA, (k+1)A], k=0,1,000,2~-1 - Discrete voitage = (K+(K+1)) A - encode input voltage using N-hits - Max quantisation error amplitude = 1/2 3-bit Uniform Quantiser $\gamma^3 = 8$ 15/16 5/16 3/16 0011 116 00°N+2 7/9 1 (FSY 0 49 2/9 Analogue Input Since N=3 an 2-4 = 1)5% = 1 = 5%

Reliable may amplitude exceerror

111

010

Quantisation Ellor - Almost unavoidable in a ADC conversion X-max error amplitude 12/2 = 2 - (N+1) Quantisation noise power = 12 · Flash and Sigma-Delta quantisers · Flash Quantiser - Wiltage ladder of MD 20 5: multanions intervals - use 2N comparaters to find the quantisation interval. Vin is a single stop -fist simple (could require a lange number of comparators) (diagram 4 for more inton on diagram)

· Successive - Approximation Quantisec (exemple) 3-bit Uniform quantisation

Vinat = 1 V, Vin = 0 V o 1 = 1/8

Analogue input = 0.425 V currently 000 1. control unit starts with a bin code 1000 =4 => 100

DAC outputs 0.5 V (= 4xA)> 0.425 V

first bit = 0 4xA = 4xI = 6.5 first bit = 0 2.010 = 2= 2 x 1 = 0.25 L 0.425 ", Second bit = 1 3. 0101 = 3x = 0.375 × 0.425 ! third bit=1 in control unit outputs the quantisation result of OII

· Steps views I. get all relevant information \$2. Start from 0000 and add a 1 to every bit as to find the value of that step. (bin col)4=7000 = Bin code found

(bin col)4=7000 = active/current bin step 12= 1100 10=1010 Then in every loop if (bin code X) < Analogue input else 3. end bit = 0 output quantisation result

if N3 73=8< spills midd