

4-Bit Multiplier

by D. Panin

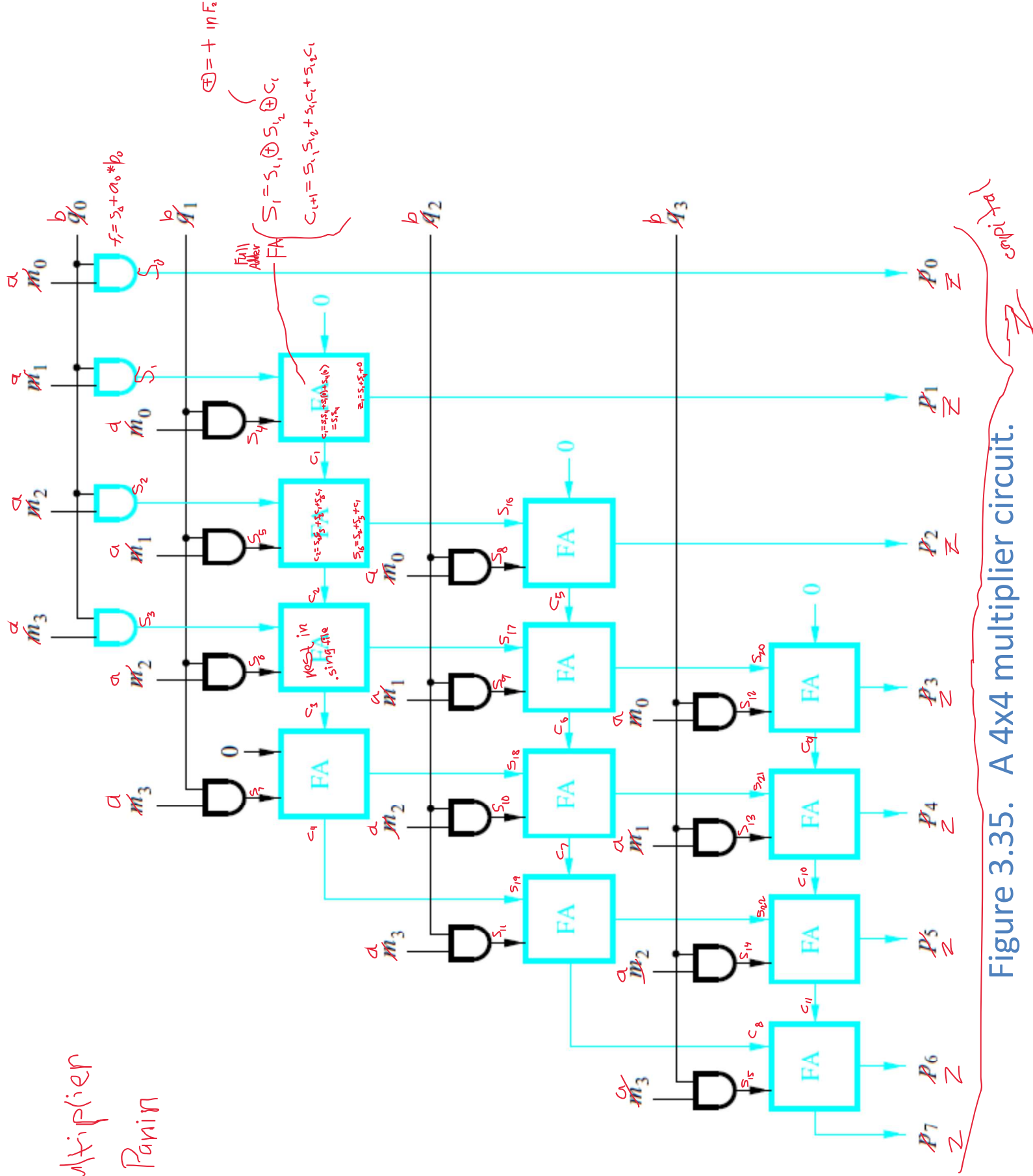


Figure 3.35. A 4x4 multiplier circuit.