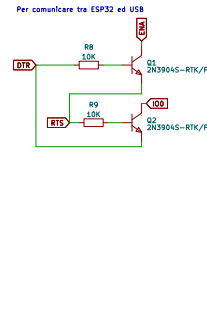
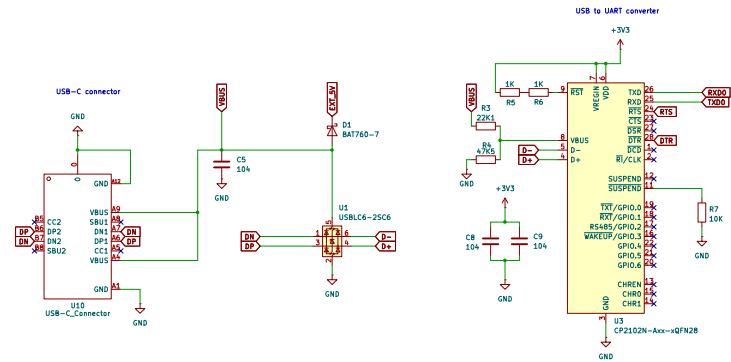
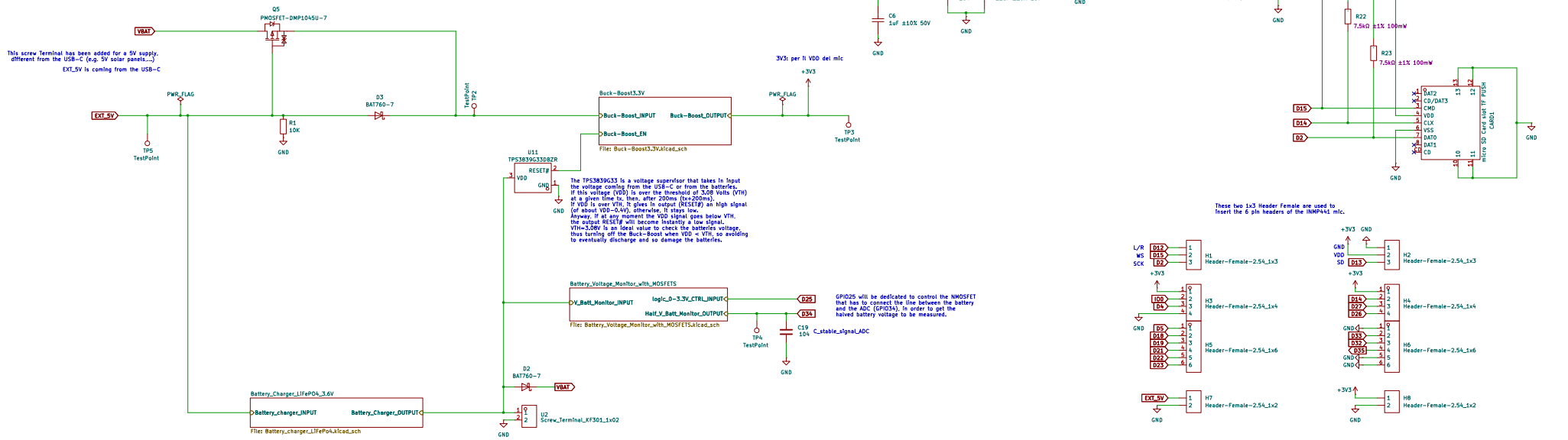


This screw Terminal has been added for a 5V supply, different from the USB-C (e.g. 5V solar panels...) EXT\_5V is coming from the USB-C



**Analog to Digital Converter (ADC)**

The ESP32 has 18 x 12 bits ADC Input channels (while the ESP8266 only has 1x 10 bits ADC). These are the GPIOs that can be used as ADC and respective channels:

GPIO	Channel	GPIO	Channel
AD1CL_CH0 (GPIO 39)	AD1CL_CH0 (GPIO 39)	AD1CL_CH1 (GPIO 39)	AD1CL_CH1 (GPIO 39)
AD1CL_CH2 (GPIO 39)	AD1CL_CH2 (GPIO 39)	AD1CL_CH3 (GPIO 39)	AD1CL_CH3 (GPIO 39)
AD1CL_CH4 (GPIO 39)	AD1CL_CH4 (GPIO 39)	AD1CL_CH5 (GPIO 39)	AD1CL_CH5 (GPIO 39)
AD1CL_CH6 (GPIO 39)	AD1CL_CH6 (GPIO 39)	AD1CL_CH7 (GPIO 39)	AD1CL_CH7 (GPIO 39)
AD1CL_CH8 (GPIO 39)	AD1CL_CH8 (GPIO 39)	AD1CL_CH9 (GPIO 39)	AD1CL_CH9 (GPIO 39)
AD1CL_CH10 (GPIO 39)	AD1CL_CH10 (GPIO 39)	AD1CL_CH11 (GPIO 39)	AD1CL_CH11 (GPIO 39)
AD1CL_CH12 (GPIO 39)	AD1CL_CH12 (GPIO 39)	AD1CL_CH13 (GPIO 39)	AD1CL_CH13 (GPIO 39)
AD1CL_CH14 (GPIO 39)	AD1CL_CH14 (GPIO 39)	AD1CL_CH15 (GPIO 39)	AD1CL_CH15 (GPIO 39)
AD1CL_CH16 (GPIO 39)	AD1CL_CH16 (GPIO 39)	AD1CL_CH17 (GPIO 39)	AD1CL_CH17 (GPIO 39)
AD1CL_CH18 (GPIO 39)	AD1CL_CH18 (GPIO 39)	AD1CL_CH19 (GPIO 39)	AD1CL_CH19 (GPIO 39)

Note: ADC3 pins cannot be used when Wi-Fi is used. So, if you're using Wi-Fi and you're having trouble getting the value from an ADC2 GPIO, you may consider using an ADC1 GPIO instead. That should solve your problem.

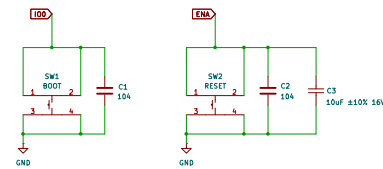
The ESP32 ADC pins don't have a linear behavior. You'll probably won't be able to distinguish between 0 and 0.1V, or between 3.2 and 3.3V. You need to keep that in mind when using the ADC pins. It is better to scale the input signal to the ADC to Voltages to a range [0.7V; 1.9V].

**Input only pins**

GPIOs 3 to 39 are GPIOs - Input only pins. These pins don't have internal pull-up or pull-down resistors. They can't be used as outputs, so use these pins only as inputs:

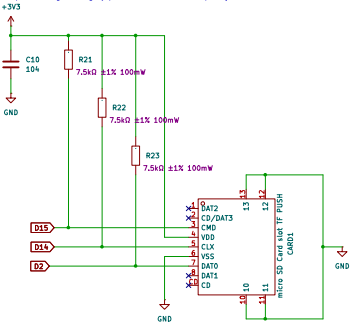
- GPIO 3 (SENSOR VP)
- GPIO 36 (SENSOR VP)
- GPIO 39 (SENSOR VP)

Pulsanti di BOOT e RESET.

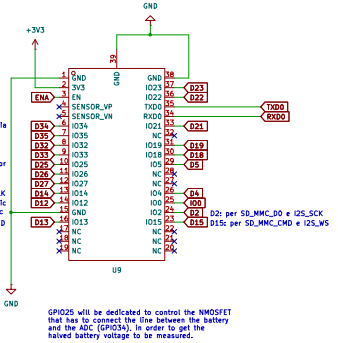
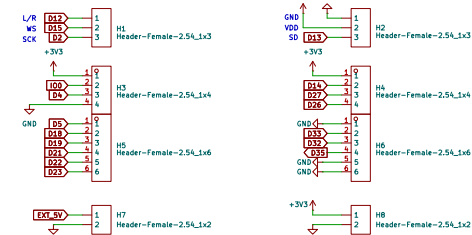


The ESP32 goes in BOOT when IO0 goes low (IO0 connected directly to GND). and in RESET when ENA goes low (ENA connected directly to GND). The switches have the pins 1 and 2 directly connected, and also the pins 3 and 4, when the button is not being pressed, the pair 1-2 is disconnected from the pair 3-4. Viceversa, when the button is being pressed, 1 connects to 3, while 2 connects to 4. So, when pressed, we have the BOOT and RESET behaviour desired. In reality we just need to use one pair of pins to connect IO0/ENA to GND when the button is being pressed. For example we can use just 1 and 3, or just 2 and 4 (leaving the not used floating). But in our case, even if we use all the pairs, the behaviour is still the same (the direct connection between IO0/ENA to GND, but with two pins instead of one). The capacitors are used in order to filter the debouncing effect that occurs when we press the button.

The three pull-up resistors (smd, 0603 package) should be of values between: 4.7K (faster rise times, supports higher speeds, less prone to signal integrity problems, consumes slightly more power) and 10K (slower, more prone to signal integrity problems, consumes less power)

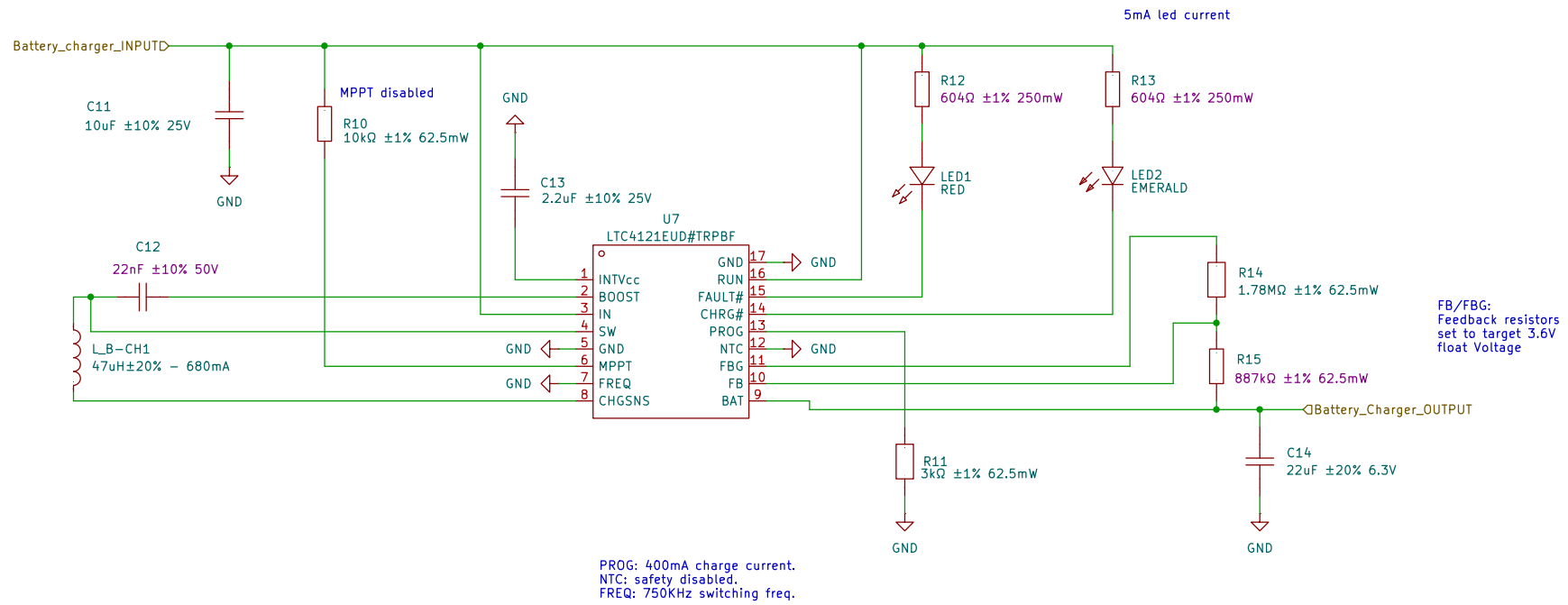


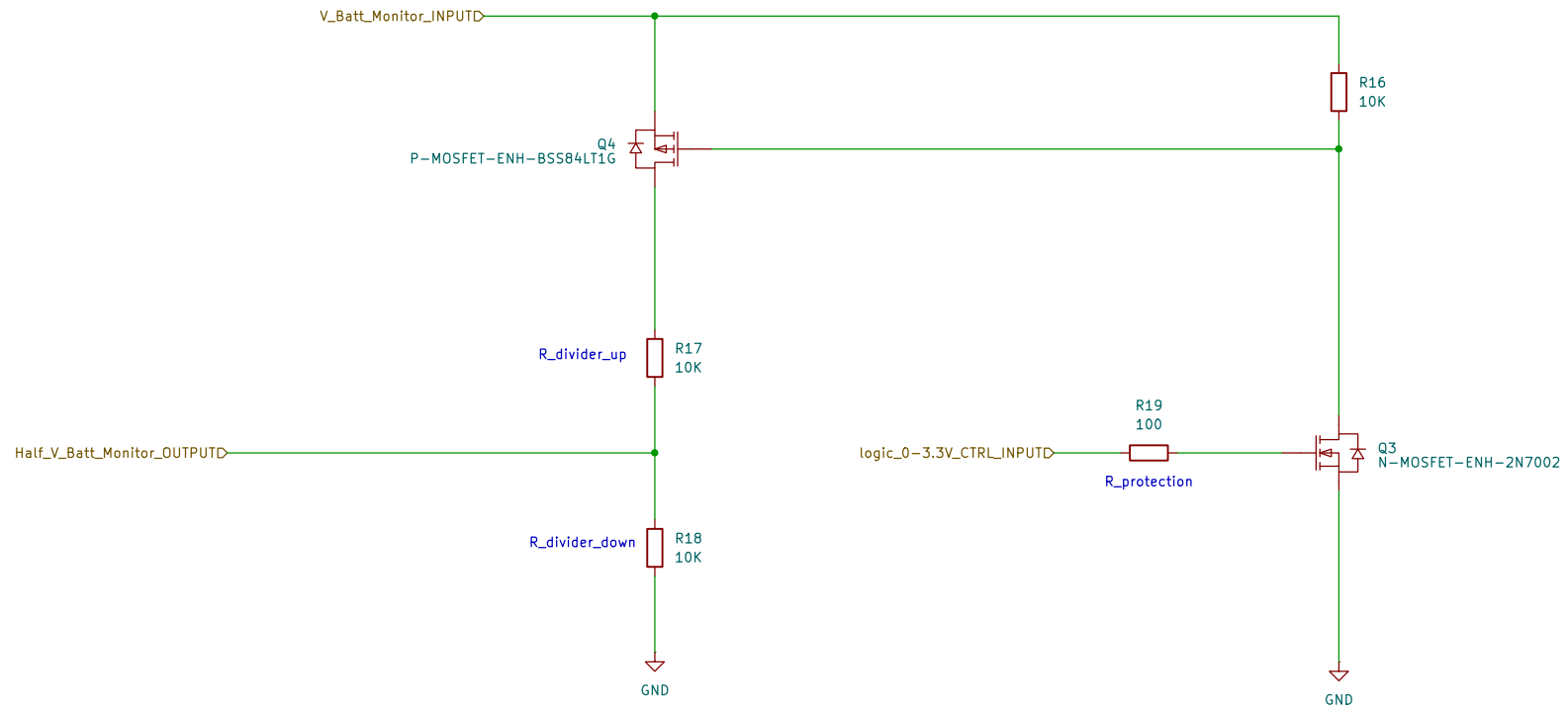
These two 1x3 Header Female are used to Insert the 6 pin headers of the IMMPass mic.



GPIO25 will be dedicated to control the NMOSFET that has to connect the line between the battery and the ADC (GPIO24). In order to get the halved battery voltage to be measured.

For this schematic, see LTC4121EUD datasheet,  
at pag.26, Figure 10. Design Example 3, SLA Charging with LTC4121





This Buck-Boost (TPS63001DRCR) is an efficient converter (up to 96%), and can convert either the 5V coming from the USB, or eventually the 3-3.6V coming from the batteries, to fixed 3.3V necessary for the esp32.

