

## **EE 5327 – VLSI DESIGN LAB**

### **Guidelines for Project Report I**

1. Your project report header should consist of the following:

**EE5327 VLSI Design Project, Report I**

**Submitted by: <your name>, Email ID: <your email>**

2. In Report I, please complete the initial synthesis of your design. You don't need to run APR for Report I. The clock frequency should be around 800MHz.
3. Please include an introduction of your project at the beginning of your report.
4. Then you can describe the architecture of your design, such as main design modules, data flow, pipeline, etc.
5. Please specify test benches, such as the precision, etc. Please attach key waveforms to verify the functionality.
6. In the results part, please include such a table:

<b>Metrics</b>	<b>Unit</b>	<b>Reference Design</b>	<b>Your Design</b>
Gate count			
Clock frequency	MHz		800
Area	$\mu\text{m}^2$		

7. Please also include the reference toward the end of your report.

#### **Presentation and Submission:**

1. Please use Times New Roman (font size 12) for your write-up and Courier New (font size 12) fonts for your code listings.
2. Please only include codes when necessary. Please ensure that your code is appropriately commented and indented to make the report easier to read.
3. Please include the screenshots of report\_design and report\_area.
4. For waveforms and schematics, please use either a black or a white background when you are obtaining the screenshots. Add appropriate captions to all waveforms and schematics.
5. Please upload only a PDF version of your report, using the following file naming convention: EE5327\_Project\_Report I\_<Name>.pdf