Team: - Jordan Cottle - Michael Russell - Harshil Suthar Notes: - Mathematical operations only performed on floating point values - DataTypes: - Floating point values - Register Addresses - Memory Addresses -Memory: - 1024 Addressable memory locations (32 bits each) - 16 Addressable registers (32 bits each) -Speeds: - Clock cycle: 100ns - Single ALU operation: 200ns - Register Read/Write: 100n - Memory Read/Write: 300ns - Condition Codes: Z N V E (CAPS = Used) - Z: Zero flag - set when operation results in a 0 - N: Negative flag - set when operation results in a negative value - V: Overflow flag - set when operation results in an overflowed operation - E: Error flag - set when an operations results in an error - Conventions: - Rd signifies a destination register - Ri, Rj signify source registers - # precedes immediate values - Encoding format: - Instructions encoded with 5 bits - Registers addressed with 5 bits - Branching labels use all remaining bits Instruction Mnemonic and Operands Operation Cycles in ALU ______ Set Rd, #FPvalue Rd ← FPvalue - description: Takes the immediate floating-point value and sets it into the Register specified by Rd - syntax: SET Rd, #FPValue - opcode: 00000 - condition codes used: Z N _ _ Load Rd, Ri $Rd \leftarrow M[Ri]$ Load - description: Takes a value from the specified address in memory (stored in register Ri) and stores it in register specified by Rd

 $M[Rd] \leftarrow Ri$

- description: Stores a value located in register Ri into the memory adressed by

- syntax: LOAD Rd, Ri

- syntax: STORE Rd, Ri

- condition codes used: Z N _ _

the value in register Rd

- condition codes used: Z N $_$ $_$

Store Rd, Ri

- opcode: 00001

- opcode: 00010

Store

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Move
            Move Rd, Ri
                                     Rd ← Ri
                                                                   1
  - description: Moves a value from register Ri into register Rd
  - syntax: MOVE Rd, Ri
  - opcode: 00011
  - condition codes used: Z N _ _
                              Rd ← Ri + Rj
Add
             Fadd Rd, Ri, Rj
  - description: Adds the floating point values in registers Ri and Rj together and
     stores the result in Rd
  - syntax: ADD Rd, Ri Rj
  - opcode: 00100
  - condition codes used: Z N V _
            Fsub Rd, Ri, Rj
                                     Rd ← Ri - Rj
Subtract
  - description: Subtracts the floating point value in register Rj from the value
     in Ri and stores it in register Rd
  - syntax: SUB Rd, Ri Rj
  - opcode: 00101
  - condition codes used: Z N V
                                     Rd ← -Ri
             Fneg Rd, Ri
  - description: Multiplies value in register Ri by -1 and stores result in
     register Rd
  - syntax: NEG Rd, Ri
  - opcode: 00110
  - condition codes used: Z N
Multiply
          Fmul Rd, Ri, Rj ← Ri * Rj
  - description: Multiplies value in registers Ri and Rj together and stores result
     in register Rd
  - syntax: MUL Rd, Ri Rj
  - opcode: 00111
  - condition codes used: Z N V
Divide
             Fdiv Rd, Ri, Rj
                                     ← Ri / Rj
  - description: Divides value in Ri by value in Rj and stores result in register
  - syntax: DIV Rd, Ri Rj
  - opcode: 01000
  - condition codes used: Z N V E
             Floor Rd, Ri
                                     Rd ← roundDown(Ri)
Floor
  - description: Calculates the floor of the value in register Ri by rounding it
     down to the nearest integer value and stores it in register Rd
  - syntax: FLOOR Rd Ri
  - opcode: 01001
  - condition codes used: Z N _ _
                                     Rd ← roundUp(Ri)
Ceiling
             Ceil Rd, Ri
  - description: Calculates the ceiling of the value in register Ri by rounding it
     up to the nearest integer value and stores it in register Rd
  - syntax: CEIL Rd Ri
  - opcode: 01010
  - condition codes used: Z N _ _
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 $Rd \leftarrow round(Ri)$ Round Round Rd, Ri 1 - description: Rounds the value in register Ri to the neartest integer and stores the result in register Rd - syntax: ROUND Rd, Ri - opcode: 01011 - condition codes used: Z N _ _ Value Fabs Rd, Ri $Rd \leftarrow abs(Ri)$ 1 Absolute - description: Calculates the absolute value of the value in register Ri and stores the result in register Rd - syntax: ABS Rd, Ri - opcode: 01100 - condition codes used: Z N $Rd \leftarrow min(Ri, Rj)$ Minimum Min Rd, Ri, Rj - description: Finds the smaller of the values stored in registers Ri and Rj and stores the result in register Rd - syntax: MIN Rd, Ri Rj - opcode: 01101 - condition codes used: Z N _ _ Maximum Max Rd, Ri, Rj $Rd \leftarrow max(Ri, Rj)$ - description: Finds the larger of the values stored in registers Ri and Rj and stores the result in register Rd - syntax: MAX Rd, Ri Rj - opcode: 01110 - condition codes used: Z N _ _ Pow Rd, Ri, #int value Rd ← Ri^integer value Power - description: Raises the value in register Ri to the power specified by the immediate integer value and stores the result in register Rd - syntax: POW Rd, Ri #int value - opcode: 01111 - condition codes used: Z N V Rd ← e^Ri Exponent Exp Rd, Ri - description: Calculates the value of e raised to the value stored in register Ri and stores the result in register Rd - syntax: EEXP Rd, Ri - opcode: 10000 - condition codes used: _ _ V _ $Rd \leftarrow sqrt(Ri)$ Square Root Sqrt Rd, Ri - description: Calculates the square root of the value stored in register Ri and stores the result in register Rd - syntax: SQRT Rd, Ri - opcode: 10001 - condition codes used: Z _ _ E

Branc	h (Uncond.) B Ri	PC ← M[R1]]	1	
_	description: Moves program by the value in register is syntax: GOTO Ri		stored value	in the memor	y addressed
_	_				
_	condition codes used:	_ E			
Branc	h Zero BZ Ri, LABEL	If (Ri ==	0) PC ← LABE	L (line)	3
_	- description: Moves to program counter to the specified label if the value i register ${\tt Ri}$ is ${\tt 0}$				
_	syntax: GOTOZ Ri LABEL				
	opcode: 11000				
_	condition codes used:	_ E			
Branc	h Negative BN Ri, LA	ABEL If (Ri <)	0) PC ← LABEI	(line) 3	
_	 description: Moves the program counter to the specified label if the value i register Ri is negative 				
_	syntax: GOTON Ri LABEL				
_	opcode: 11001				
_	condition codes used:	_ E			
No-op	Nop	No operati	ion	1	
- description: Does nothing for one cycle					
_	syntax: PASS				
_	opcode: 11111				
_	condition codes used: $_{-}$ -				
Halt	Halt	Stop Prog	ram	-	
_	description: Stops execut	ing of the progra	m		
_	syntax: STOP				
_	opcode: 10101				

- condition codes used: _ _ _ _