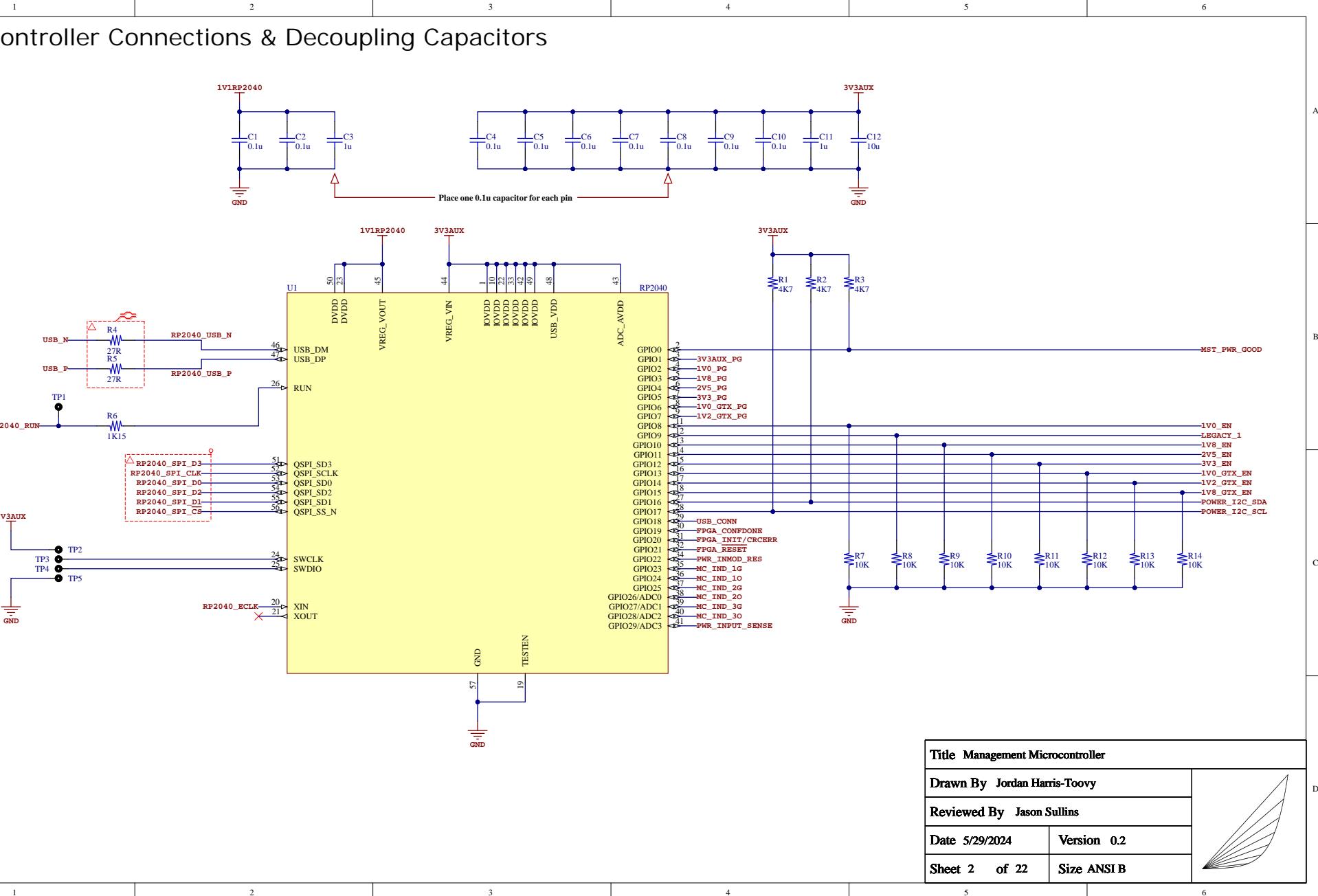
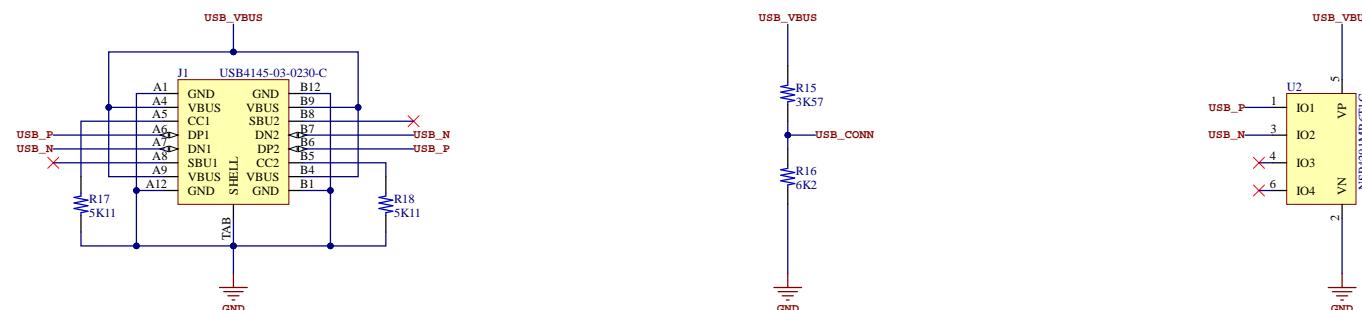


# Microcontroller Connections & Decoupling Capacitors

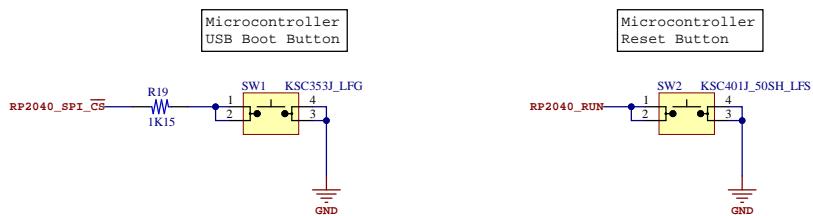


1 2 3 4 5 6

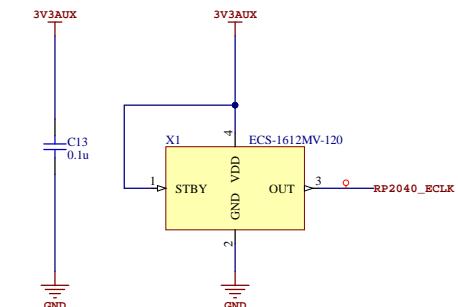
## Microcontroller USB Connection



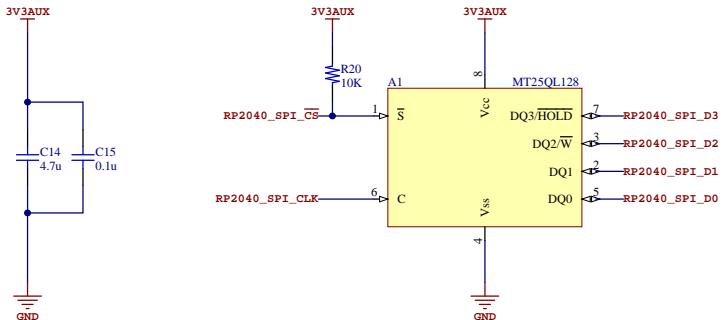
## Microcontroller Buttons



## Microcontroller Clock



## Microcontroller FLASH



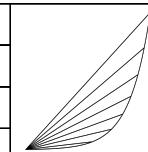
Title Management Microcontroller Support	
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Drawn By	Jordan Harris-Toovy
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Reviewed By	Jason Sullins
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Date 5/29/2024	Version 0.2
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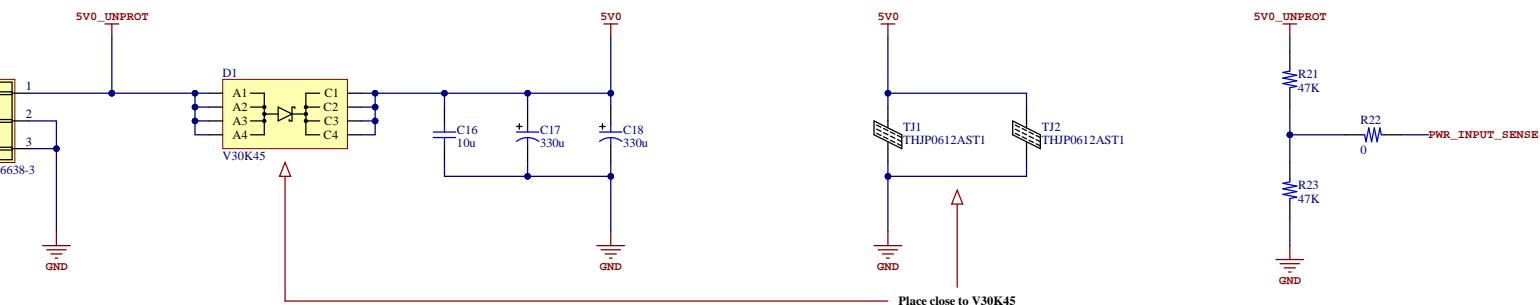
Sheet 3 of 22	Size ANSI B
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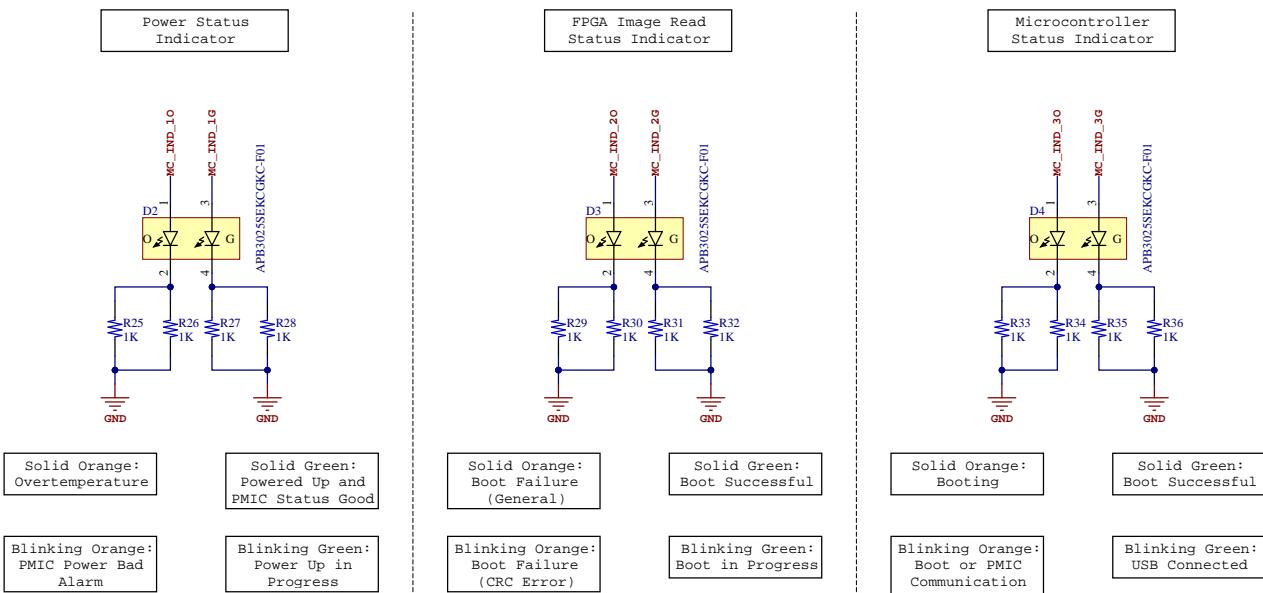
1 2 3 4 5 6

1 2 3 4 5 6

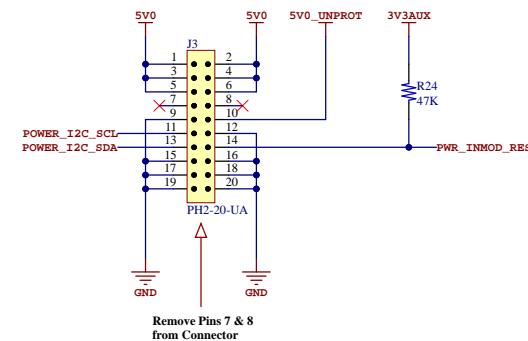
## Board 5V Power Input



## Indicator LEDs



## Power Input Module Connector (Optional)



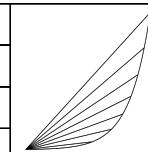
### Title Power Inputs and Indicators

Drawn By Jordan Harris-Toovy

Reviewed By Jason Sullins

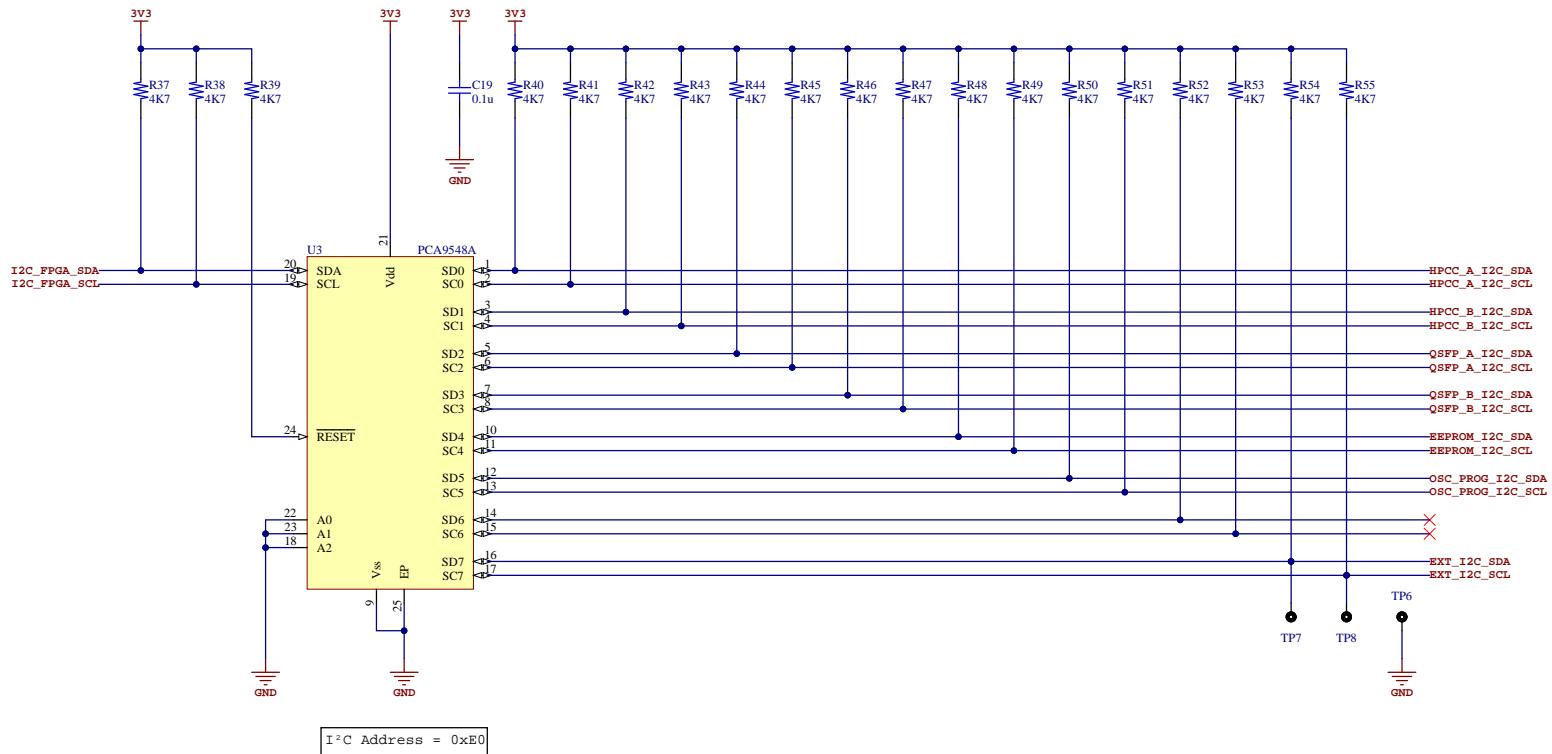
Date 5/29/2024 Version 0.2

Sheet 4 of 22 Size ANSI B

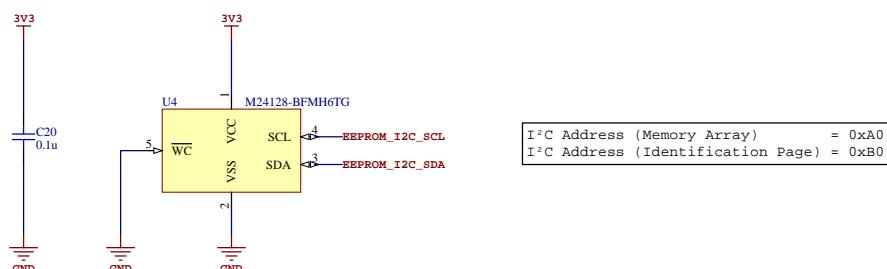


1 2 3 4 5 6

## IIC Switch



## IIC EEPROM



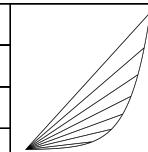
**Title** PC Switch & EEPROM

**Drawn By** Jordan Harris-Toovy

**Reviewed By** Jason Sullins

**Date** 5/29/2024    **Version** 0.2

**Sheet** 5 of 22    **Size** ANSI B



1

2

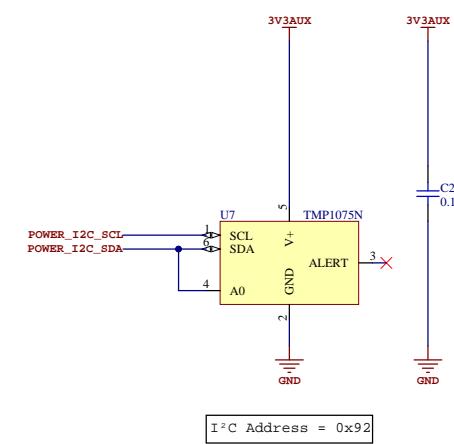
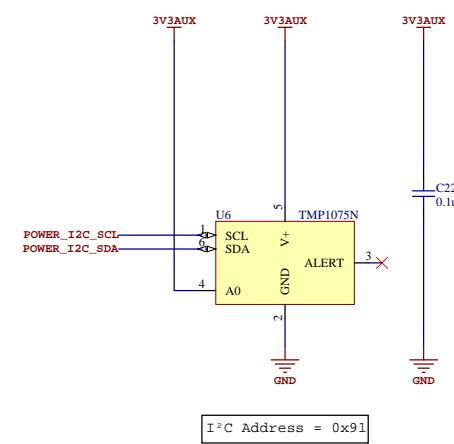
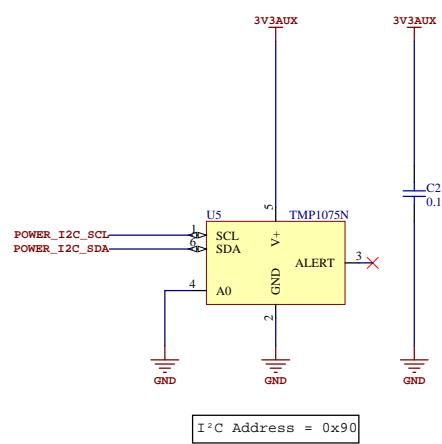
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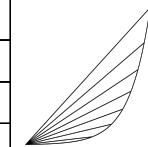
5

6

## Temperature Sensors



<b>Title</b> PMIC-Area Temperature Sensors	
<b>Drawn By</b> Jordan Harris-Toovy	
<b>Reviewed By</b> Jason Sullins	
<b>Date</b> 5/29/2024	<b>Version</b> 0.2
<b>Sheet</b> 6 of 22	
<b>Size</b> ANSI B	



1

2

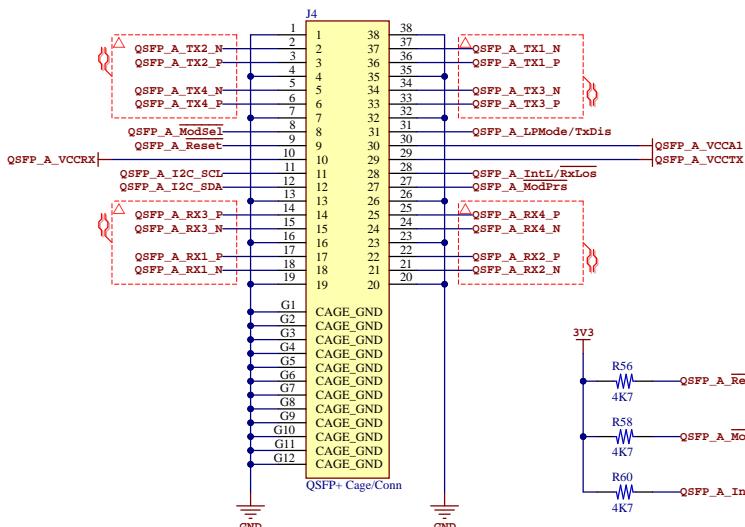
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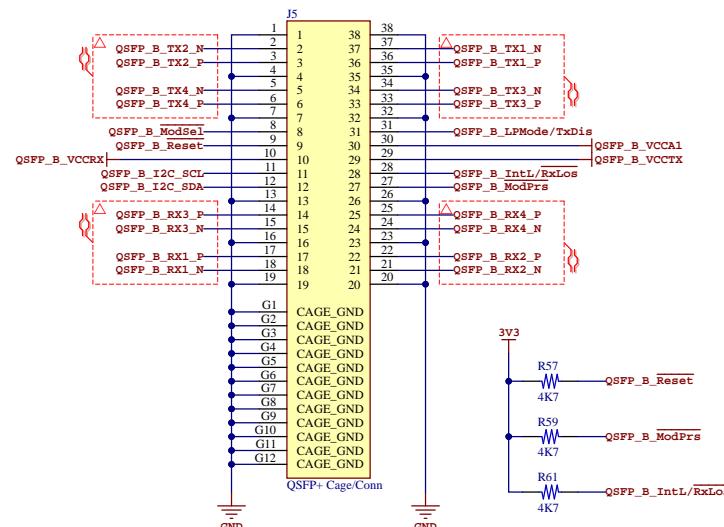
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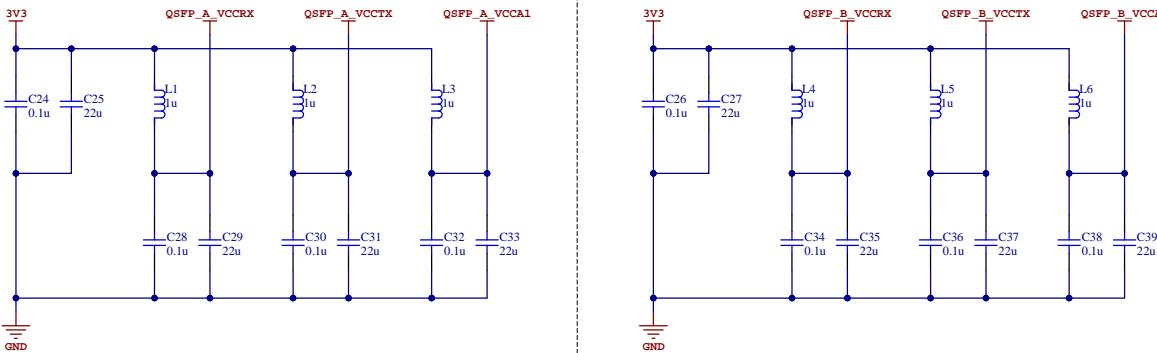
## QSFP+ A Cage & Connector



## QSFP+ B Cage & Connector



## QSFP+ Power Filters



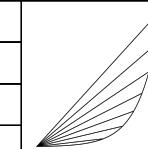
### Title QSFP+ Connectors & Filtering

Drawn By Jordan Harris-Toovy

Reviewed By Jason Sullins

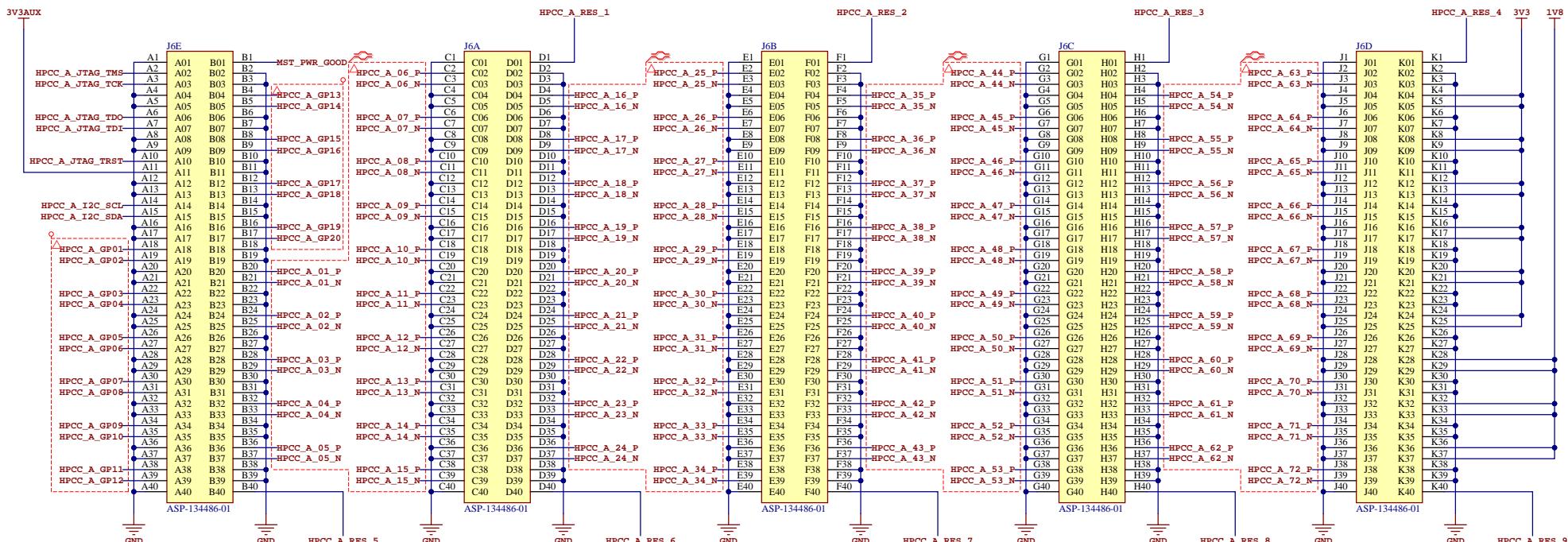
Date 5/29/2024 Version 0.2

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# HPCC A Connector

A



B

C

D

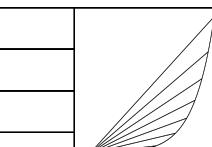
Title HPCC Connector A

Drawn By Jordan Harris-Toovy

Reviewed By Jason Sullins

Date 5/29/2024 Version 0.2

Sheet 8 of 22 Size ANSI B



1

2

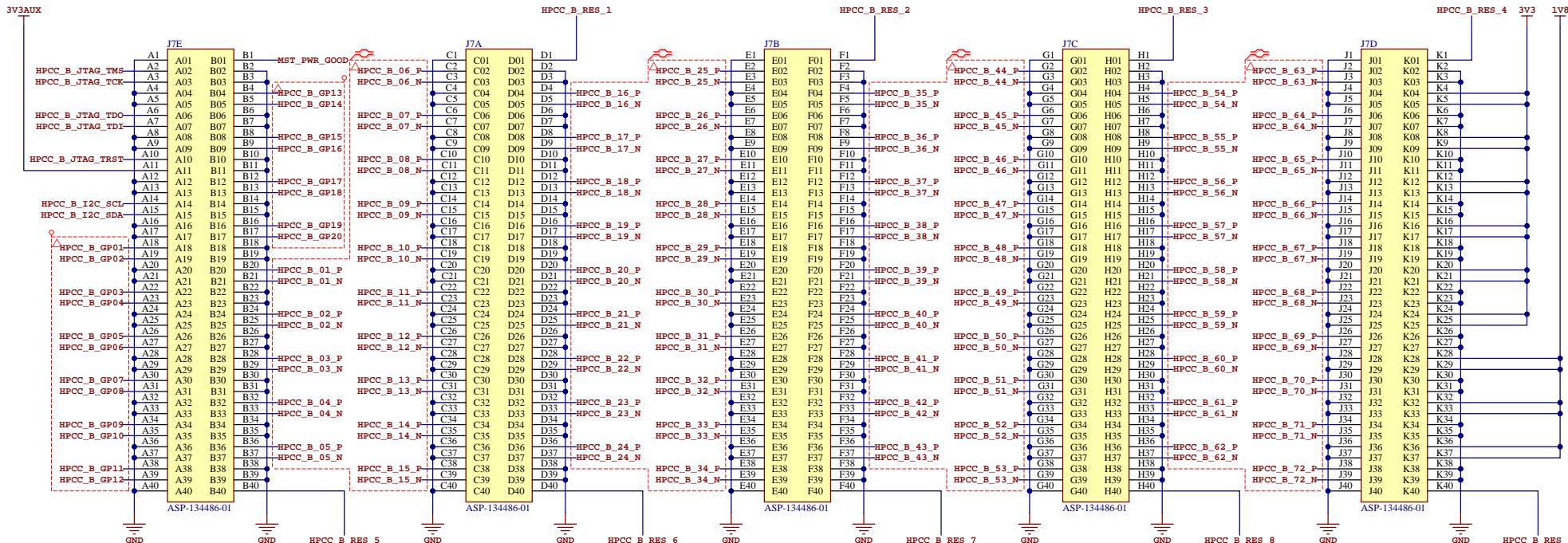
3

4

5

6

# HPCC B Connector



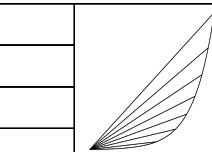
Title HPCC Connector B

Drawn By Jordan Harris-Toovy

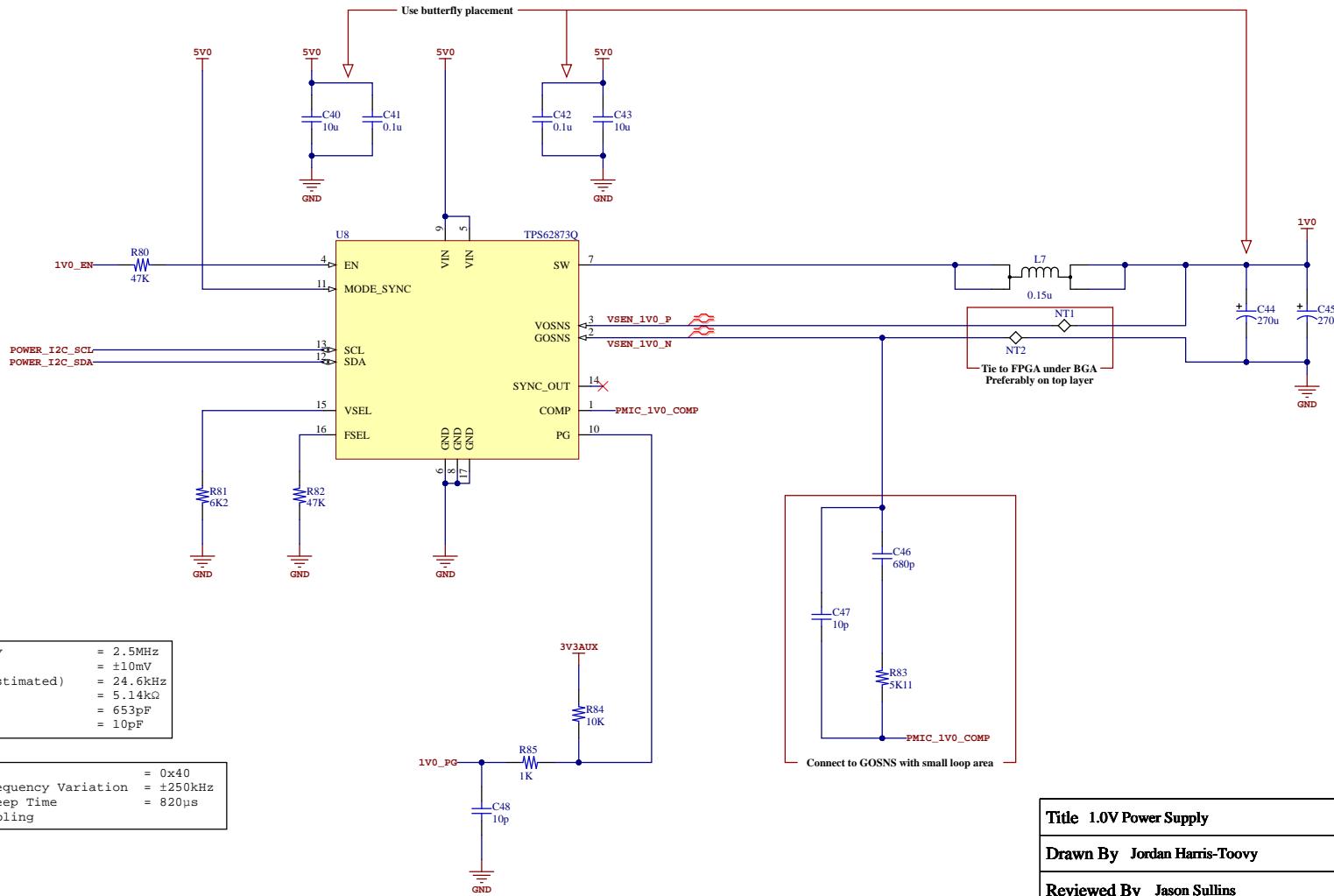
Reviewed By Jason Sullins

Date 5/29/2024 Version 0.2

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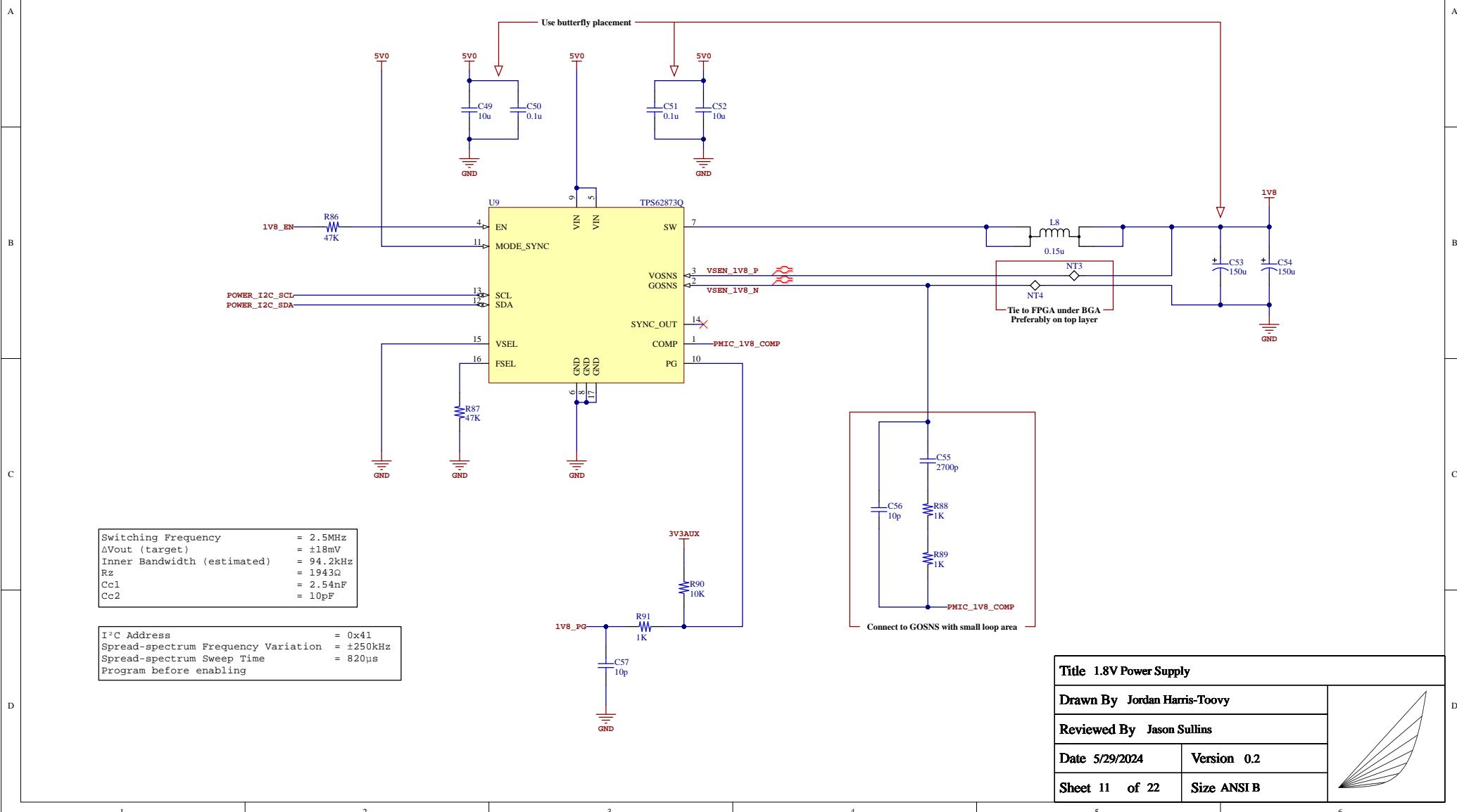


# FPGA 1.0V Power Supply

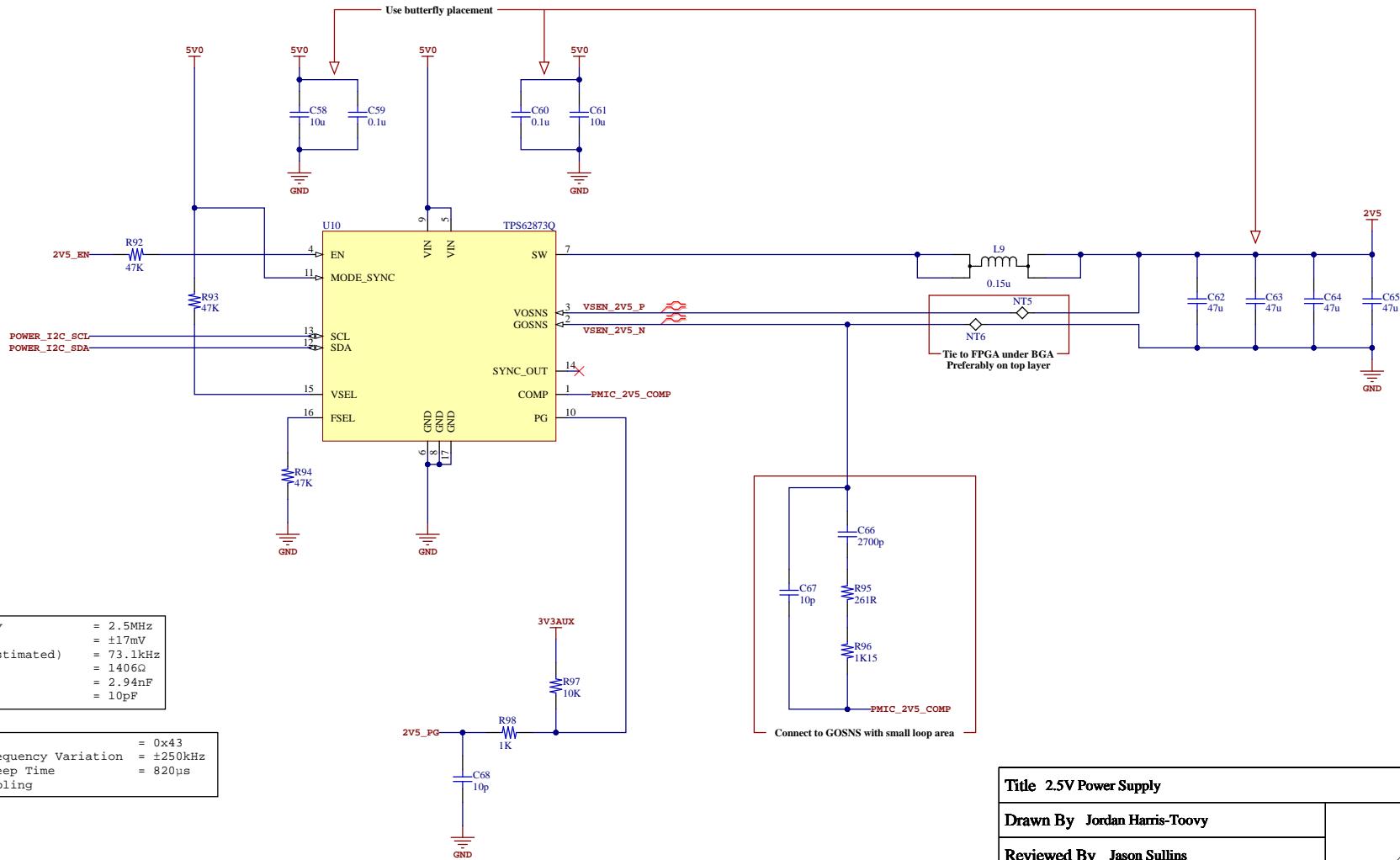


Title 1.0V Power Supply	
Drawn By Jordan Harris-Toovy	Reviewed By Jason Sullins
Date 5/29/2024	Version 0.2
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# FPGA 1.8V Power Supply

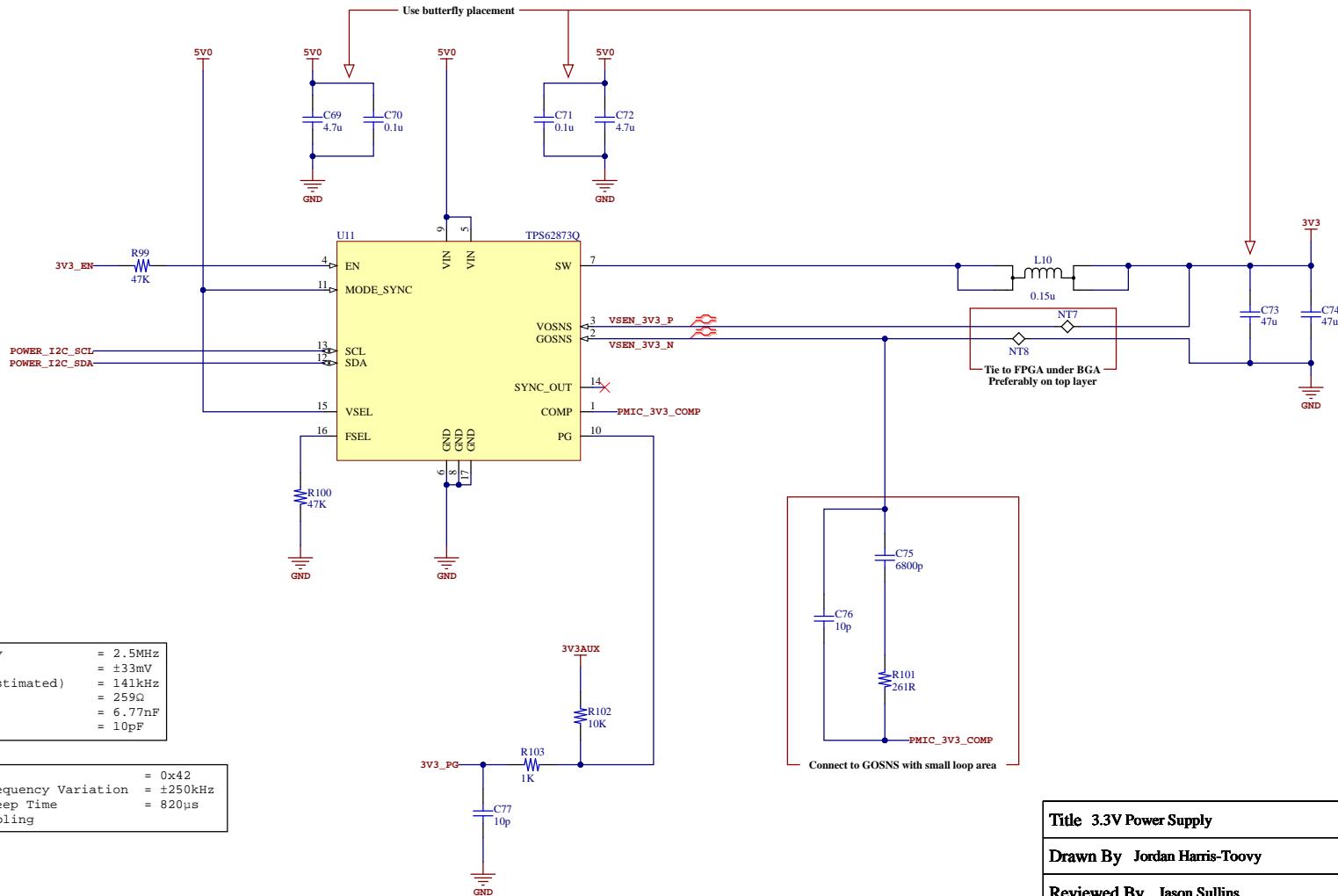


# FPGA 2.5V Power Supply



Title 2.5V Power Supply	
Drawn By Jordan Harris-Toovy	Reviewed By Jason Sullins
Date 5/29/2024	Version 0.2
Sheet 12 of 22	Size ANSI B

# FPGA 3.3V Power Supply



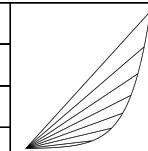
Title 3.3V Power Supply

Drawn By Jordan Harris-Toovy

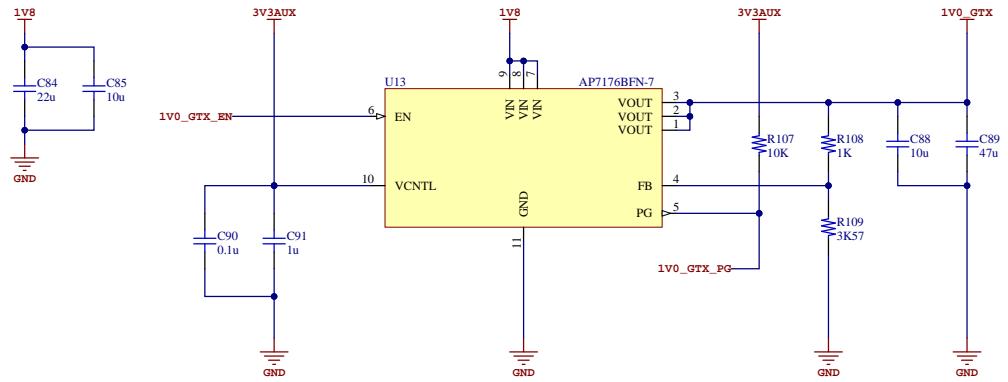
Reviewed By Jason Sullins

Date 5/29/2024 Version 0.2

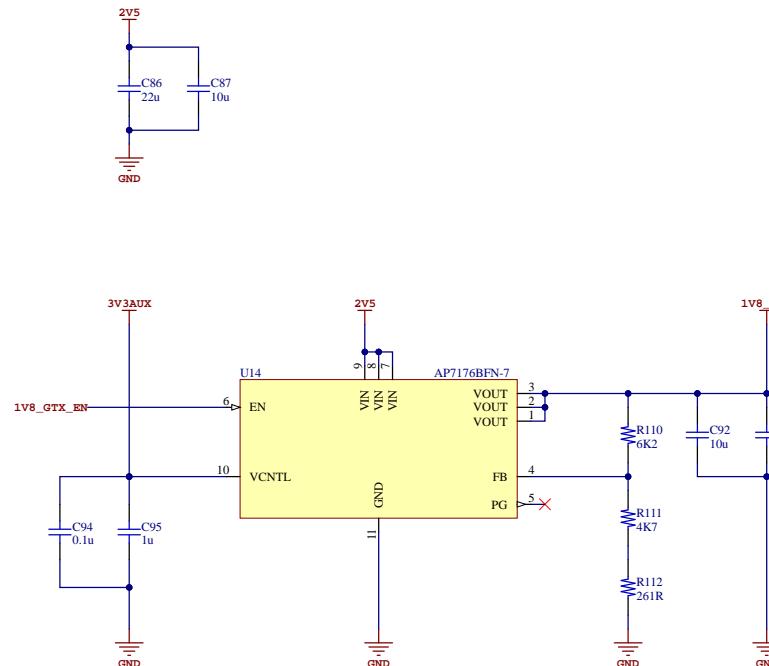
Sheet 13 of 22 Size ANSI B



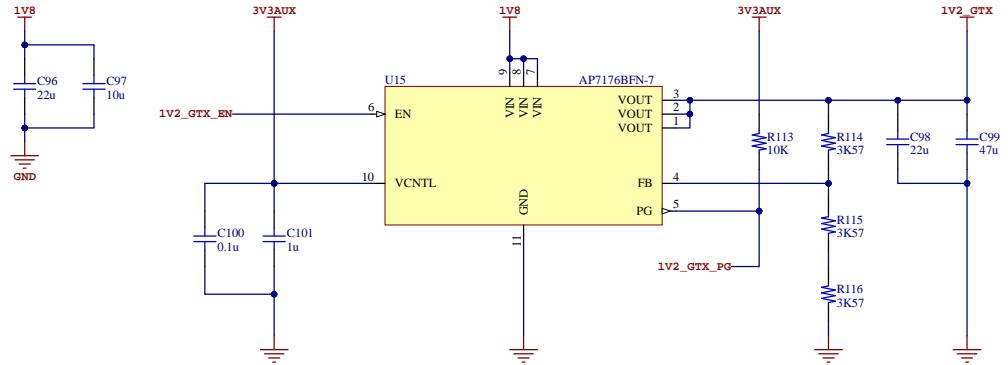
## FPGA GTX 1.0V Supply



## FPGA GTX 1.8V Supply



## FPGA GTX 1.2V Supply



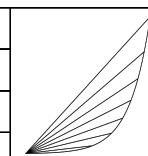
Title GTX Power Supplies

Drawn By Jordan Harris-Toovy

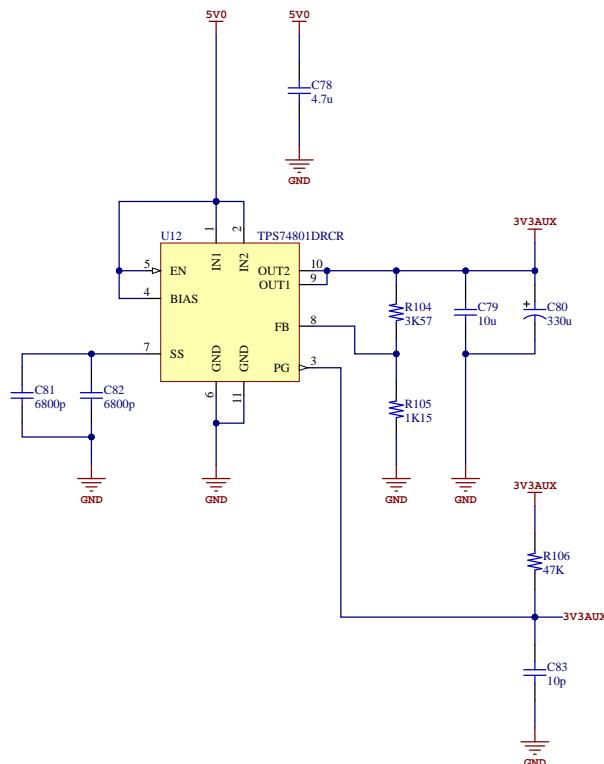
Reviewed By Jason Sullins

Date 5/29/2024 Version 0.2

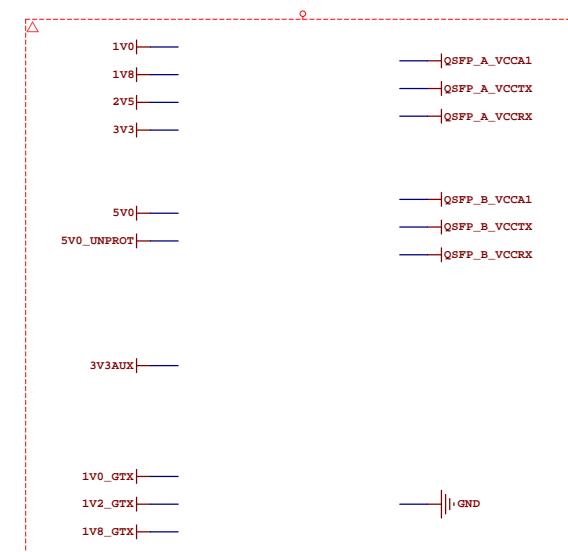
Sheet 14 of 22 Size ANSI B



### 3.3V Auxiliary Power Supply

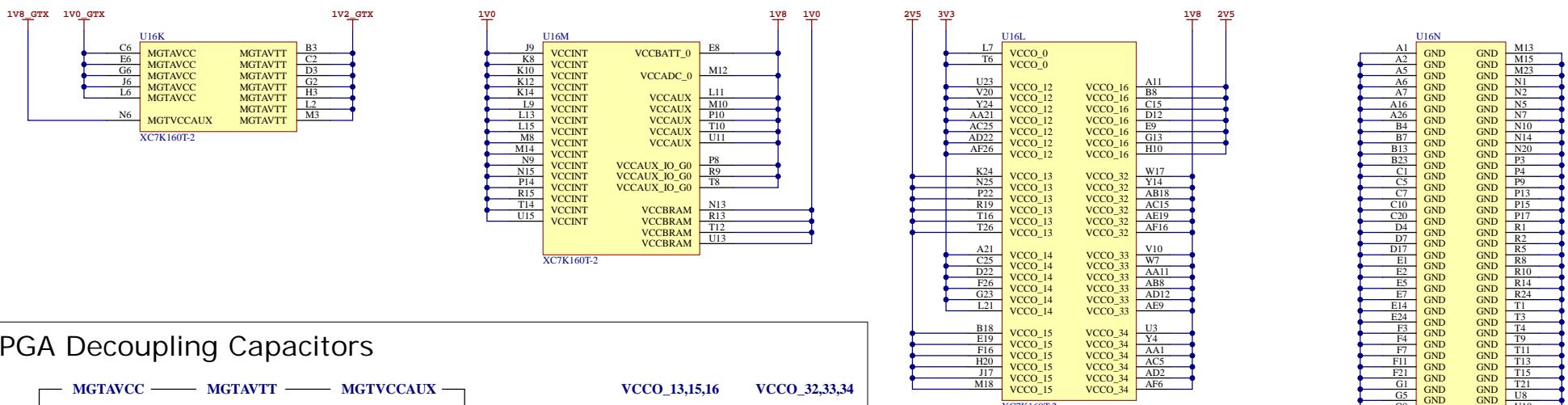


### Power Net Class Definitions

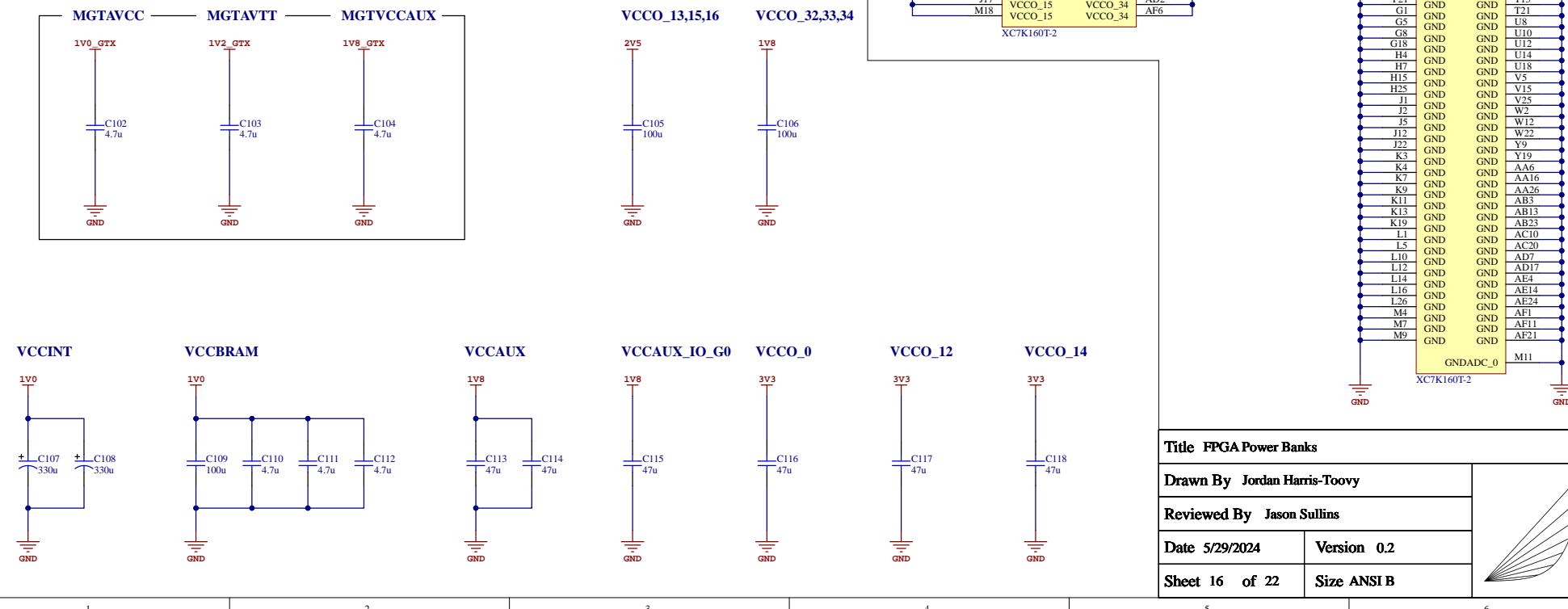


Title 3.3V Auxiliary Power Supply and Power Net Class Definitions	
Drawn By Jordan Harris-Toovy	Reviewed By Jason Sullins
Date 5/29/2024	Version 0.2
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# FPGA Power Connections



# FPGA Decoupling Capacitors



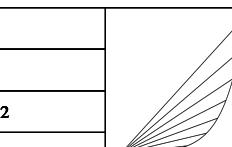
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Drawn By Jordan Harris-Toovy

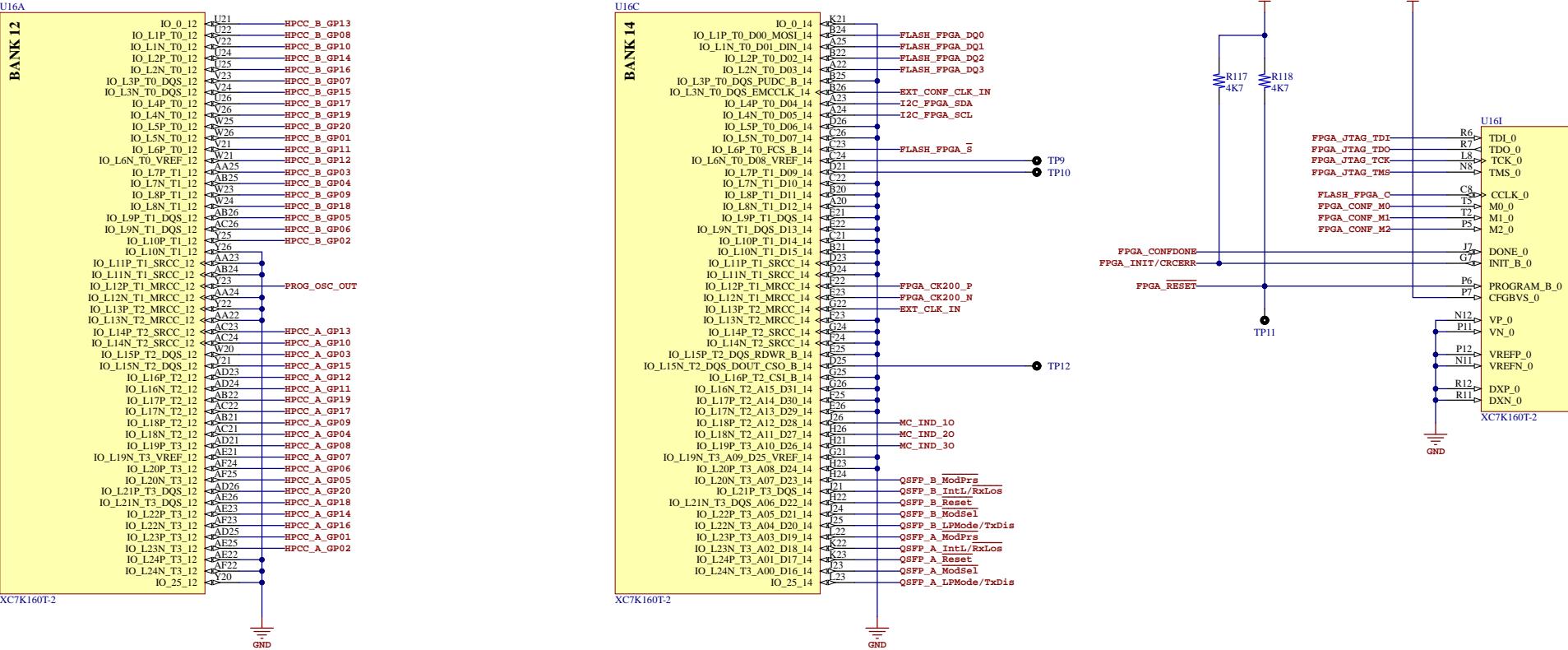
Reviewed By Jason Sullins

Date 5/29/2024 Version 0.2

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# FPGA Configuration Connections



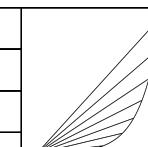
Title **FPGA Configuration Banks**

Drawn By **Jordan Harris-Toovy**

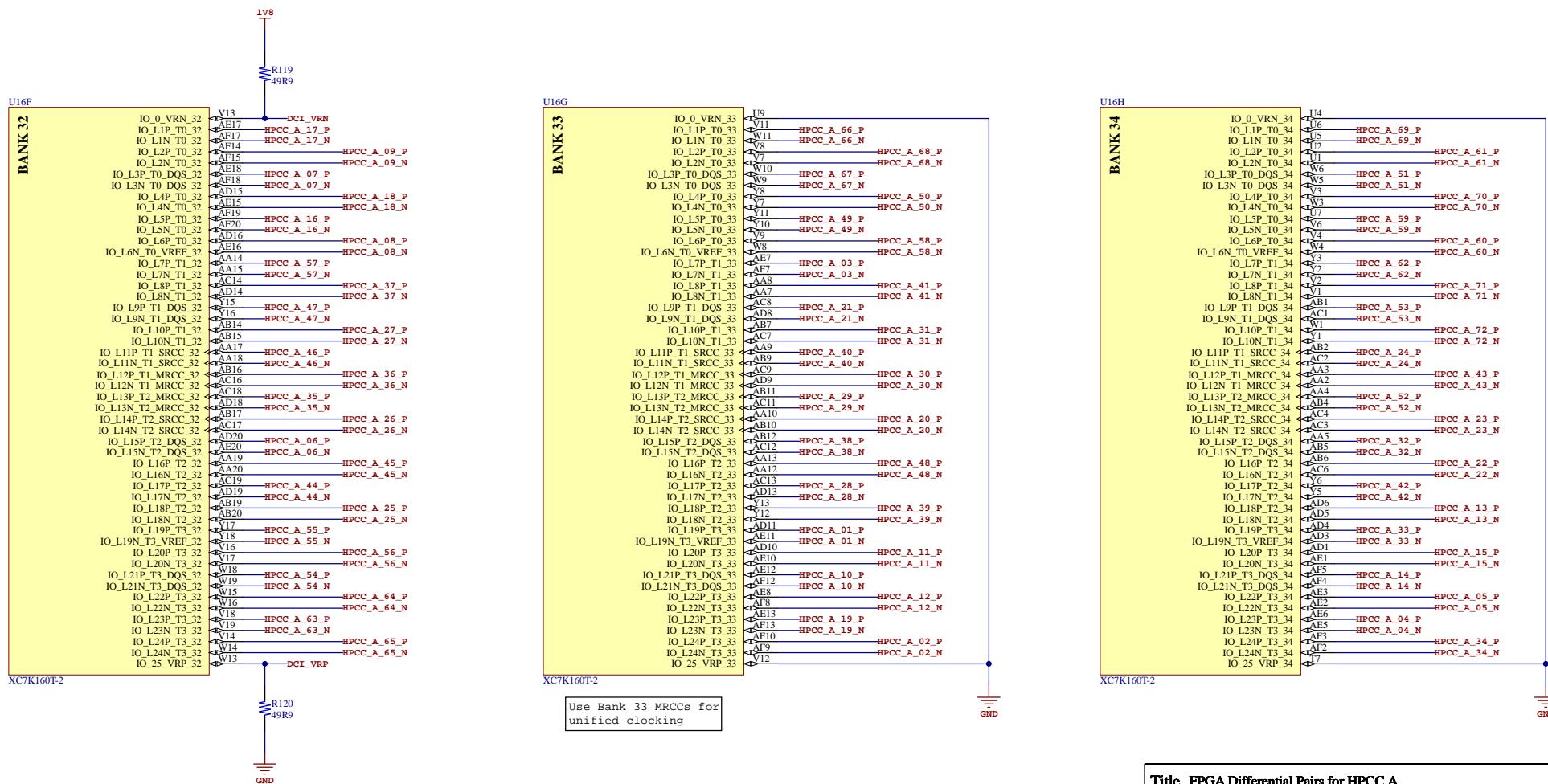
Reviewed By **Jason Sullins**

Date **5/29/2024** Version **0.2**

Sheet **17** of **22** Size **ANSI B**



# FPGA HPCC A Connections



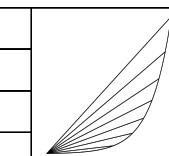
Title **FPGA Differential Pairs for HPCC A**

Drawn By **Jordan Harris-Toovy**

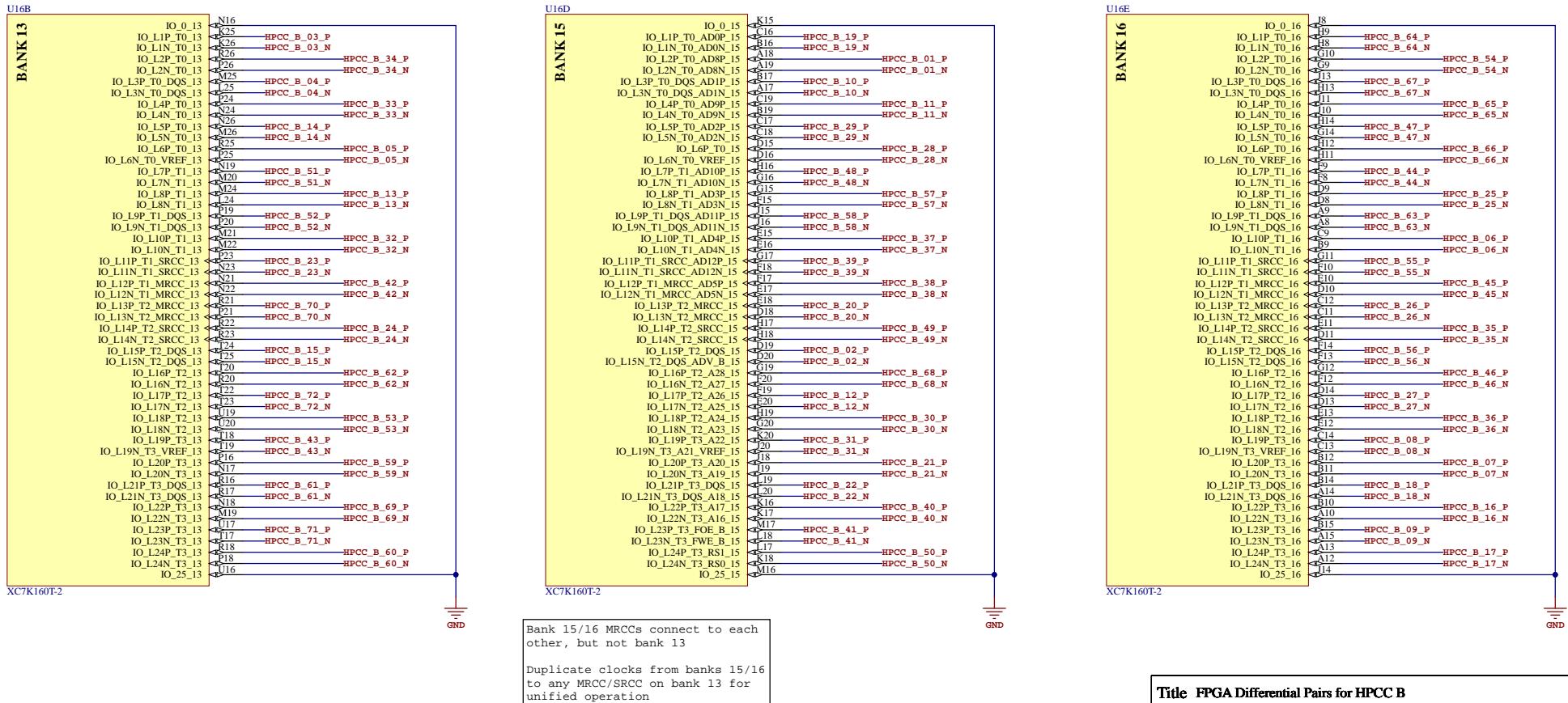
Reviewed By **Jason Sullins**

Date **5/29/2024** Version **0.2**

Sheet **18** of **22** Size **ANSI B**



# FPGA HPCC B Connections



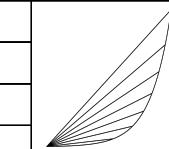
**Title** FPGA Differential Pairs for HPCC B

**Drawn By** Jordan Harris-Toovy

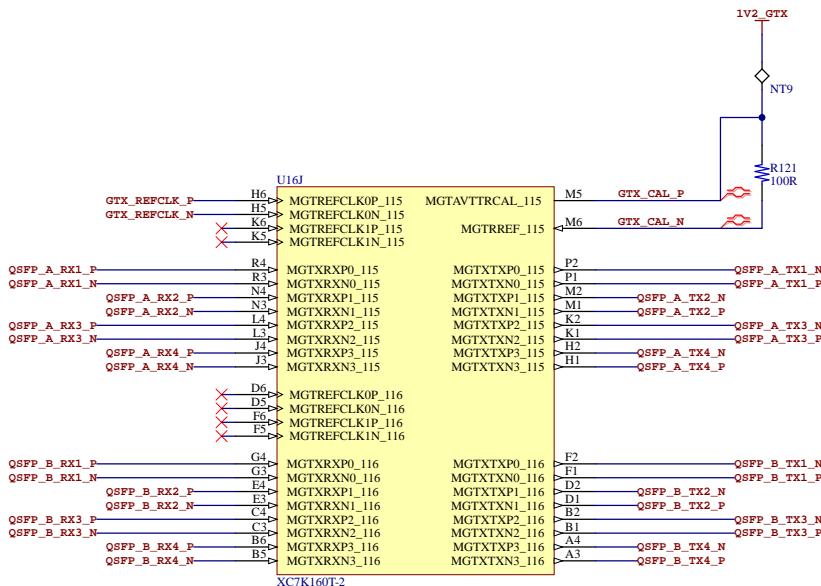
**Reviewed By** Jason Sullins

**Date** 5/29/2024    **Version** 0.2

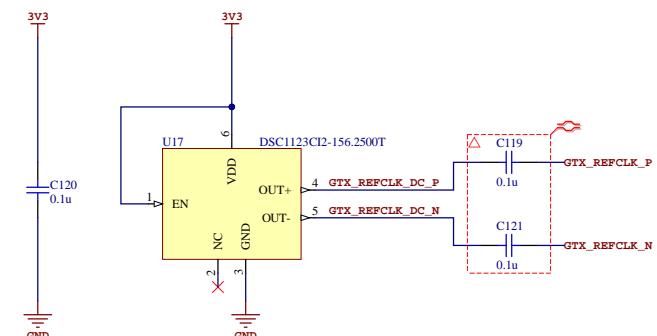
**Sheet** 19    **of** 22    **Size** ANSI B



## FPGA GTX Quads



## FPGA GTX Clock



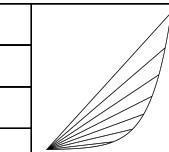
Title FPGA GTX Transceivers & SFP Clock

Drawn By Jordan Harris-Toovy

Reviewed By Jason Sullins

Date 5/29/2024 Version 0.2

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# FPGA FLASH

A

A

B

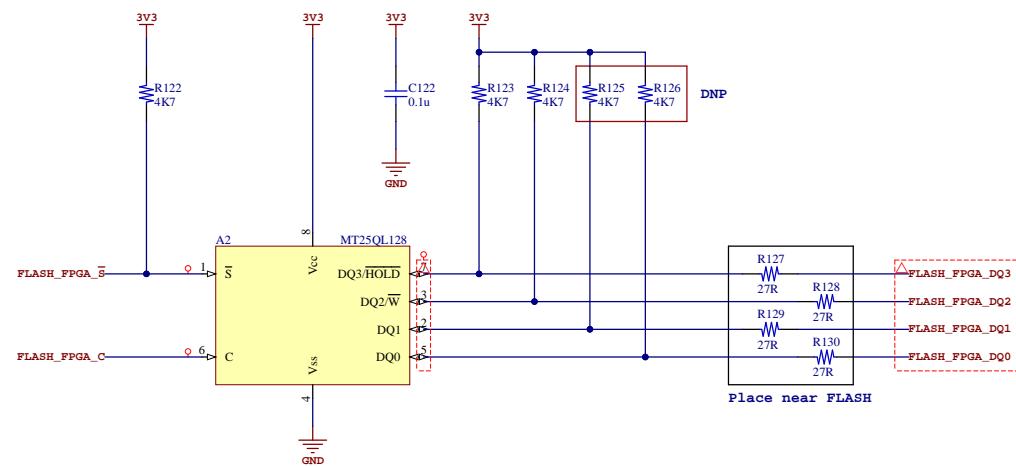
B

C

C

D

D



Title FPGA QSPI FLASH	
Drawn By Jordan Harris-Toovy	
Reviewed By Jason Sullins	
Date 5/29/2024	Version 0.2
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