JORDAN TAPIA

Electrical Engineering Senior Undergraduate

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EDUCATION

Bachelor of Science - Electrical Engineering
(ABET Certified)
University of California - Santa Cruz
SEPTEMBER 2015 - JUNE 2018 (expected)
GPA: 3.4

PROJECTS

Motion Control System for OLED Printing
NOVEMBER 2017 - JUNE 2018
- Feedback control system for pneumatically controlled cylinder that maintains smooth motion.
- Main duties: control algorithm design in C, system characterization using Matlab, circuit design for Arduino/actuator interface.

AM Receiver
JANUARY 2018
- Superheterodyne single conversion receiver tuned for 1460 AM.
- Designed using Cadence Schematic Capture and PCB Editor.
- Components used were a mix of through hole and surface mount.

64 bit CPU in Verilog
MARCH - JUNE 2017
- Designed component modules to implement a single stage pipeline CPU for RISC-V ISA.

EMPLOYMENT

Physics Tutor - *Santa Cruz, CA* - Cabrillo College
SEPTEMBER 2014 - JUNE 2015
- Effectively addressed student challenges concerning electromagnetism and Newtonian mechanics

Certified Pharmacy Technician - Walgreens Pharmacy
JAN 2009 - OCT 2017
- Team environment focused on meeting customer deadlines
- Third party insurance resolutions

CERTIFICATIONS/ASSOCIATIONS

Licensed Amateur Radio Operator
- General Level License (call sign KM6NCG)

Amateur Radio Club
- Sept 2017

LANGUAGES

C, C++, Python, and Verilog

SOFTWARE

Orcad PCB Editor and Schematic Capture, Matlab, PSPICE, Arduino IDE, Microsoft Office

SKILLS

Oscilloscope, DMM, Spectrum Analyzer, Smith Charts, SMT soldering, UNIX command line, Version control(git)

RELEVANT COURSEWORK

- Advanced Analog design
- Digital Signal Processing (DSP)
- Advanced Logic Design
- RF Hardware Design
- Feedback/Control Theory
- Analog and Digital Comm. Systems

PERSONAL ATTRIBUTES

- Hard-working attitude
- Focused determination
- Collaborative worker and enjoy team efforts