

JORDAN TAPIA

Electrical Engineering Student

Electrical engineering senior undergraduate student seeking full-time career

EDUCATION

Bachelor of Science - Electrical Engineering

University of California - Santa Cruz

SEPTEMBER 2015 - MAY 2018 (expected)

GPA: 3.42

PROJECTS

Designed and Simulated a CPU - UC Santa Cruz

Advanced Logic Design, CMPE 125

MARCH - JUNE 2017

- Implemented the 64 bit RISC V ISA in Verilog
- Design used single stage pipeline CPU
- Simulated using Verilator and C++ testbenches

FPGA Driven Video Game - UC Santa Cruz

Digital Logic Design, CMPE 100

MAY 2016

- Designed Pong variation on a Spartan 3E FPGA using Xilinx software and VGA monitor
- Implemented an 8-state Mealy machine for score keeping
- Interfaced with VGA output to generate video

EMPLOYMENT

Physics Tutor - Santa Cruz, CA

Cabrillo College Physics Learning Center

SEPTEMBER 2014 - JUNE 2015

- Effectively addressed student challenges concerning electromagnetism and Newtonian mechanics

Certified Pharmacy Technician - Santa Cruz, CA

Walgreens Pharmacy

JAN 2009 - OCT 2017

- Team environment focused on meeting customer deadlines
- Third party insurance resolutions
- PTCB certified

CERTIFICATIONS/ASSOCIATIONS

Licensed Amateur Radio Operator

- General Level License (call sign KM6NCG)

Amateur Radio Club

- Sept 2017

Santa Cruz, CA 95064

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(831) 821-0919

LANGUAGES

C, C++, Python, and Verilog

SOFTWARE

Allegro PCB Design, Matlab, PSPICE, Microsoft Office

HARDWARE

Spartan 3E FPGA, PIC 32-bit MCU, Arduino, Raspberry Pi

SKILLS

Oscilloscope and digital multimeter operation, UNIX command line interface, version control(git)

RELEVANT COURSEWORK

- Advanced Logic Design
- Analog CMOS design
- VLSI
- Digital Signal Processing
- Signals and systems/Analog and Digital Comm. Systems

PERSONAL ATTRIBUTES

- Hard-working attitude
- Focused determination
- Collaborative worker