

JORDAN TAPIA

Electrical Engineering Student

Electrical engineering undergraduate student seeking full-time internship

EDUCATION

Bachelor of Science - Electrical Engineering

[University of California - Santa Cruz](#)

SEPTEMBER 2015 - MAY 2018 (expected)

GPA: 3.43

PROJECTS

RISC V CPU

[Advanced Logic Design, CMPE 125](#)

MARCH - JUNE 2017

- Implemented a 64 bit RISC V ISA using single stage pipeline CPU in Verilog
- Design was simulated using Verilator

FPGA Driven Video Game - UC Santa Cruz

[Digital Logic Design, CMPE 100](#)

MAY 2016

- Designed Pong variation on a Spartan 3E FPGA using Xilinx software and VGA monitor
- Implemented an 8-state Mealy machine for score keeping
- Interfaced with VGA output to generate video

Battleship Game on PIC 32-bit Microcontroller

APRIL 2016

- Programmed state machines on MCU to implement the rules of the Hasbro Battleship game for 2 players

Feedback Amplifier - UC Santa Cruz

[Advanced Analog Circuit Design, EE 172](#)

NOVEMBER 2016

- Designed folded cascode operational amplifier using MOSFETs with common mode feedback

EMPLOYMENT

Physics Tutor - Santa Cruz, CA

[Cabrillo College Physics Learning Center](#)

SEPTEMBER 2014 - JUNE 2015

- Effectively addressed student challenges concerning electromagnetism and Newtonian mechanics

Certified Pharmacy Technician - Santa Cruz, CA

[Walgreens Pharmacy](#)

JAN 2009 - PRESENT

- Addressed patient concerns regarding medications
- Third party insurance resolutions
- Managed inventory of pharmaceuticals

413 Koshland Way
Santa Cruz, CA 95064
joetapia@ucsc.edu
(831) 821-0919

LANGUAGES

C, C++, Python, Verilog, MATLAB, JavaScript, HTML

SOFTWARE

PSPICE, Xilinx ISE, MPLabX, Microsoft Office

HARDWARE

Spartan 3E field programmable gate array, PIC 32-bit MCU, Arduino development board, Raspberry Pi.

SKILLS

FPGA Design, Analog circuit design, oscilloscope and digital multimeter operation, UNIX command line interface, version control.

RELEVANT COURSEWORK

- Advanced digital logic design
- Analog CMOS design
- VLSI
- Digital Signal Processing
- Signals and systems/Analog and Digital Comm. Systems

PERSONAL ATTRIBUTES

- Hard-working attitude
- Focused determination
- Collaborative worker