

# Circuit Theory and Electronics Fundamentals

Department of Electrical and Computer Engineering, Técnico, University of Lisbon

Example Laboratory Report

February 27, 2021

## Contents

<b>1</b>	<b>Introduction</b>	<b>1</b>
<b>2</b>	<b>Theoretical Analysis</b>	<b>1</b>
2.1	Mesh Analysis . . . . .	1
2.2	Nodal Analysis . . . . .	3
<b>3</b>	<b>Simulation Analysis</b>	<b>3</b>
3.1	Operating Point Analysis . . . . .	3
<b>4</b>	<b>Conclusion</b>	<b>4</b>

## 1 Introduction

The objective of this laboratory assignment is to study a circuit containing a various resistors, two voltage sources and two current sources. The circuit can be seen if Figure 1.

In Section 2, a theoretical analysis of the circuit is presented. In Section 3, the circuit is analysed by simulation, and the results are compared to the theoretical results obtained in Section 2. The conclusions of this study are outlined in Section 4.

## 2 Theoretical Analysis

In this section, the circuit shown in Figure 1 is analysed theoretically, first we approach the circuit using the mesh analysis, and later we analyse the circuit using the nodal analysis.

### 2.1 Mesh Analysis

As seen during theoretical lessons, we can use a mesh analysis to analyse the circuit. This method is built upon Kirchhoff's Voltage Law that states:

In a mesh, the sum of all voltages equals 0.

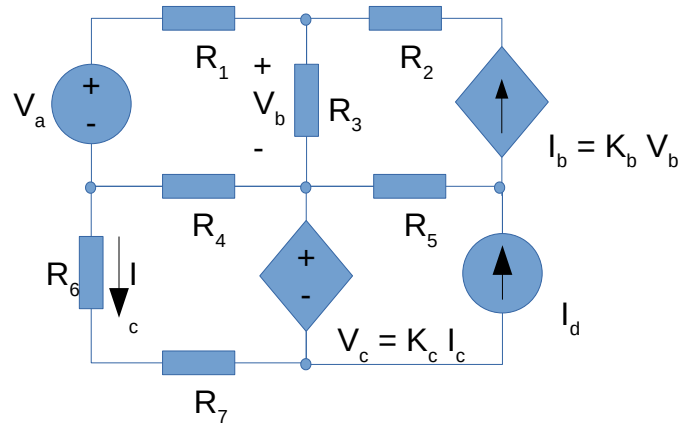


Figure 1: Circuit to be studied

$$\sum_{i=0}^n V_i = 0 \quad (1)$$

The method consists of identifying every mesh, labeling its current and choosing the currents direction. Then the KVL equations are written for each mesh, and we can solve the system of equations, solving consequently the circuit.

As seen in in figure 2, there are four meshes, each with currents  $I_a$ ,  $I_b$ ,  $I_c$ ,  $I_d$ , we will label each mesh as A, B, C, D respectively.

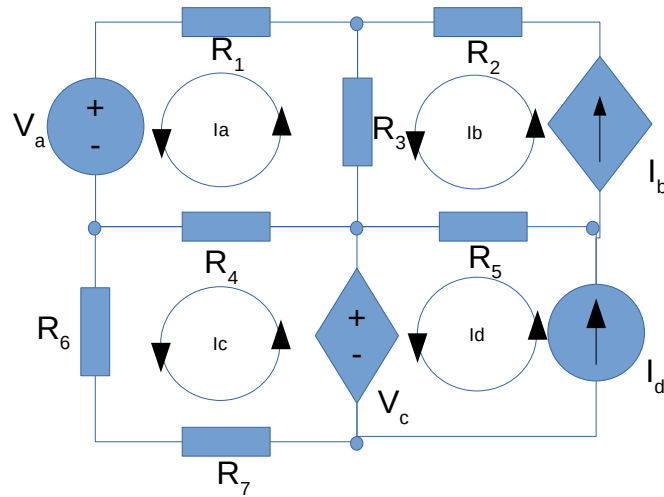


Figure 2: Current choosen for each mesh

The equations for each mesh are therefore:

$$\mathbf{E_A} : R_1 \cdot I_a + V_a + R_4 \cdot (I_a - I_c) + R_3 \cdot (I_a - I_b) = 0 \quad (2)$$

$$\mathbf{E_B} : R_2 \cdot I_b + R_3 \cdot (I_b - I_a) + R_5 \cdot (I_b - I_d) = 0 \quad (3)$$

$$\mathbf{E}_C : R_6 \cdot I_c + R_7 \cdot I_c - V_c + R_4 \cdot (I_c - I_a) = 0 \quad (4)$$

$$\mathbf{E}_D : V_c + R_5 \cdot (I_d - I_a) = 0 \quad (5)$$

Solving this system of equations, using the following values generated used the data generator python program using the number 93156:

R1	1.03919759193
R2	2.06836523173
R3	3.03375774261
R4	4.12779067183
R5	3.11985677803
R6	2.04513887844
R7	1.04289965713
Va	5.00439410964
Id	1.04536428769
Kb	7.25705461539
Kc	8.23640363075

Table 1: Data generated using number 93156

And the the following extra equations:

$$I_b = K_b \times V_b \quad (6)$$

where:

$$V_b = R_3 \times (I_b - I_a) \quad (7)$$

and

$$V_c = K_c \times I_c \quad (8)$$

We reach the following results, using octave:

## 2.2 Nodal Analysis

## 3 Simulation Analysis

### 3.1 Operating Point Analysis

Table 2 shows the simulated operating point results for the circuit under analysis. Compared to the theoretical analysis results, one notices the following differences: describe and explain the differences.

Lorem ipsum dolor sit amet, consectetur adipiscing elit. Ut purus elit, vestibulum ut, placerat ac, adipiscing vitae, felis. Curabitur dictum gravida mauris. Nam arcu libero, nonummy eget, consectetur id, vulputate a, magna. Donec vehicula augue eu neque. Pellentesque habitant morbi tristique senectus et netus et malesuada fames ac turpis egestas. Mauris ut leo. Cras viverra metus rhoncus sem. Nulla et lectus vestibulum urna fringilla ultrices. Phasellus eu tellus sit amet tortor gravida placerat. Integer sapien est, iaculis in, pretium quis, viverra ac, nunc. Praesent eget sem vel leo ultrices bibendum. Aenean faucibus. Morbi dolor nulla, malesuada eu, pulvinar at, mollis ac, nulla. Curabitur auctor semper nulla. Donec varius orci eget risus. Duis nibh mi, congue eu, accumsan eleifend, sagittis quis, diam. Duis eget orci sit amet orci dignissim rutrum.

Name	Value [A or V]
@cb[i]	0.000000e+00
@ce[i]	0.000000e+00
@q1[ib]	7.022567e-05
@q1[ic]	1.404513e-02
@q1[ie]	-1.41154e-02
@q1[is]	5.765392e-12
@rc[i]	1.411536e-02
@re[i]	1.411536e-02
@rf[i]	7.022567e-05
@rs[i]	0.000000e+00
v(1)	0.000000e+00
v(2)	0.000000e+00
base	2.254108e+00
coll	5.765392e+00
emit	1.411536e+00
vcc	1.000000e+01

Table 2: Operating point. A variable preceded by @ is of type *current* and expressed in Ampere; other variables are of type *voltage* and expressed in Volt.

## 4 Conclusion

In this laboratory assignment the objective of analysing an RC circuit has been achieved. Static, time and frequency analyses have been performed both theoretically using the Octave maths tool and by circuit simulation using the Ngspice tool. The simulation results matched the theoretical results precisely. The reason for this perfect match is the fact that this is a straightforward circuit containing only linear components, so the theoretical and simulation models cannot differ. For more complex components, the theoretical and simulation models could differ but this is not the case in this work.

Lorem ipsum dolor sit amet, consectetur adipiscing elit. Ut purus elit, vestibulum ut, placerat ac, adipiscing vitae, felis. Curabitur dictum gravida mauris. Nam arcu libero, nonummy eget, consectetur id, vulputate a, magna. Donec vehicula augue eu neque. Pellentesque habitant morbi tristique senectus et netus et malesuada fames ac turpis egestas. Mauris ut leo. Cras viverra metus rhoncus sem. Nulla et lectus vestibulum urna fringilla ultrices. Phasellus eu tellus sit amet tortor gravida placerat. Integer sapien est, iaculis in, pretium quis, viverra ac, nunc. Praesent eget sem vel leo ultrices bibendum. Aenean faucibus. Morbi dolor nulla, malesuada eu, pulvinar at, mollis ac, nulla. Curabitur auctor semper nulla. Donec varius orci eget risus. Duis nibh mi, congue eu, accumsan eleifend, sagittis quis, diam. Duis eget orci sit amet orci dignissim rutrum.