

# NTE74LS47 Integrated Circuit TTL - BCD-to-Seven-Segment Decoder/Driver with Open Collector Outputs

#### **Description:**

The NTE74LS47 is a BCD-to-Seven-Segment Decoder/Driver in a 16-Lead plastic DIP type package that features active-low outputs designed for driving common-anode VLEDs or incandescent indicators directly. This device has full ripple-blanking input/output controls and a lamp test input. Display patterns for BCD input count above 9 are unique symbols to authenticate input conditions.

The NTE74LS47 incorporates automatic leading and/or trailing-edge zero-blanking control (RBI and RBO). Lamp test (LT) may be performed at any time when the BI/RBO node is set at a high level. This device also contains an overriding blanking input (BI) which can be used to control the lamp intensity by pulsing or to inhibit the outputs. Inputs and outputs are entirely compatible for use with TTL logic outputs.

#### Features:

- Open–Collector Outputs Drive Indicators Directly
- Lamp Test Provision
- Leading/Trailing Zero Suppression

# Absolute Maximum Ratings: (Note 1)

Supply Voltage, V <sub>CC</sub>	7V
Input Voltage	7V
Peak Output Current (t <sub>w</sub> ≤ 1ms, Duty Cycle ≤ 10%)	200mA
Current Forced Into Any Output in the Off-State	1mA
Operating Temperature Range, T <sub>A</sub>	0°C to +70°C
Storage Temperature Range, T <sub>stg</sub>	-65°C to +150°C

Note 1. Unless otherwise specified, all voltages are referenced to GND.

## **Recommended Operating Conditions:**

Parameter	Symbol	Min	Тур	Max	Unit
Supply Voltage	V <sub>CC</sub>	4.75	5.0	5.25	V
Off-State Output Voltage (a through g)	V <sub>O(off)</sub>	-	_	15	V
On-State Output Current (a through g)	I <sub>O(on)</sub>	-	_	24	mA
High-Level Output Current (BI/RBO)	I <sub>OH</sub>	-	_	-50	μΑ
Low-Level Output Current (BI/RBO)	l <sub>OL</sub>	-	_	3.2	mA
Operating Temperature Range	T <sub>A</sub>	0	_	+70	°C

#### **Electrical Characteristics**: (Note 2, Note 3)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit		
High-Level Input Voltage	$V_{IH}$			2	_	_	V	
Low-Level Input Voltage	$V_{IL}$							
Input Clamp Voltage	V <sub>IK</sub>	$V_{CC} = MIN, I_I = -18mA$	V <sub>CC</sub> = MIN, I <sub>I</sub> = -18mA					
High Level Output Voltage BI/RBO	V <sub>OH</sub>	V <sub>CC</sub> = MIN, V <sub>IH</sub> = 2V, V <sub>IL</sub> = MAX, I <sub>OH</sub> =	2.4	4.2	_	V		
Low Level Output Voltage	$V_{OL}$	$V_{CC} = MIN, V_{IH} = 2V, V_{IL} = MAX$	$V_{IH} = 2V$ , $V_{IL} = MAX$ $I_{OL} = 1.6mA$				V	
BI/RBO	BI/RBO			_	0.35	0.5	V	
Off-State Output Current a through g	I <sub>O(off)</sub>	$V_{CC} = MAX, V_{IH} = 2V, V_{IL} = MAX, V_{O(off)}$	_		250	μΑ		
On-State Output Voltage	V <sub>O(on)</sub>	$V_{CC} = MIN, V_{IH} = 2V, V_{IL} = MAX$	I <sub>O(on)</sub> = 12mA	_	0.25	0.4	V	
a through g			I <sub>O(on)</sub> = 24mA	_	0.35	0.5	V	
Input Current	Ι <sub>Ι</sub>	$V_{CC} = MAX, V_I = 7V$	CC = MAX, V <sub>I</sub> = 7V				mA	
High Level Input Current	I <sub>IH</sub>	$V_{CC} = MAX, V_I = 2.7V$		-	_	20	μΑ	
Low Level Input Current Any Input except BI/RBO	I <sub>IL</sub>	$V_{CC} = MAX, V_I = 0.4V$		-	-	-0.4	mA	
BI/RBO				_	_	-1.2	mA	
Short-Circuit Output Current BI/RBO	los	V <sub>CC</sub> = MAX	-0.3	-	-2	mA		
Supply Current	I <sub>CC</sub>	V <sub>CC</sub> = MAX, Note 4		ı	7	13	mA	

- Note 2. .For conditions shown as MIN or MAX, use the appropriate value specified under "Recommended Operation Conditions".
- Note 3. All typical values are at  $V_{CC}$  = 5V,  $T_A$  = +25°C. Note 4.  $I_{CC}$  is measured with all outputs open and all inputs at 4.5V.

## <u>Switching Characteristics</u>: $(V_{CC} = 5V, T_A = +25^{\circ}C \text{ unless otherwise specified})$

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Turn-Off Time from A Input	t <sub>off</sub>	$R_L = 665\Omega, C_L = 15pF$	-	_	100	ns
Turn-On Time from A Input	t <sub>on</sub>		-	_	100	ns
Turn-Off Time from RBI Input	t <sub>off</sub>		-	_	100	ns
Turn-On Time from RBI Input	t <sub>on</sub>		_	_	100	ns

#### **Function Table:**

Decimal	Inputs						BI/RBO	Outputs							
or Function	LT	RBI	D	С	В	Α	(NOTE)	а	b	С	d	е	f	g	Notes
0	Н	Н	L	L	L	L	Н	ON	ON	ON	ON	ON	ON	OFF	
1	Н	Χ	L	L	L	Н	Н	OFF	ON	ON	OFF	OFF	OFF	OFF	
2	Н	Χ	L	L	Н	L	Н	ON	ON	OFF	ON	ON	OFF	ON	
3	Н	Х	L	L	Н	Н	Н	ON	ON	ON	ON	OFF	OFF	ON	
4	Н	Χ	L	Н	L	L	Н	OFF	ON	ON	OFF	OFF	ON	ON	
5	Н	Χ	L	Н	L	Н	Н	ON	OFF	ON	ON	OFF	ON	ON	
6	Н	Χ	L	Н	Н	L	Н	OFF	OFF	ON	ON	ON	ON	ON	
7	Н	Х	L	Н	Н	Н	Н	ON	ON	ON	OFF	OFF	OFF	OFF	1
8	Н	Χ	Н	L	L	L	Н	ON	ON	ON	ON	ON	ON	ON	'
9	Н	Χ	Н	L	L	Н	Н	ON	ON	ON	OFF	OFF	ON	ON	
10	Н	Χ	Н	L	Н	L	Н	OFF	OFF	OFF	ON	ON	OFF	ON	
11	Н	Х	Н	L	Н	Н	Н	OFF	OFF	ON	ON	OFF	OFF	ON	
12	Н	Χ	Н	Н	L	L	Н	OFF	ON	OFF	OFF	OFF	ON	ON	
13	Н	Χ	Н	Н	L	Н	Н	ON	OFF	OFF	ON	OFF	ON	ON	
14	Н	Χ	Н	Н	Н	L	Н	OFF	OFF	OFF	ON	ON	ON	ON	
15	Н	Х	Н	Н	Н	Н	Н	OFF	OFF	OFF	OFF	OFF	OFF	OFF	
BI	Χ	Χ	Χ	Χ	Χ	Χ	L	OFF	OFF	OFF	OFF	OFF	OFF	OFF	2
RBI	Н	L	L	L	L	L	L	OFF	OFF	OFF	OFF	OFF	OFF	OFF	3
LT	L	Х	Х	Х	Х	Х	Н	ON	ON	ON	ON	ON	ON	ON	4

H = HIGH Level

L = LOW Level

X = Irrelevant

**NOTE**: BI/RBO is wire-AND logic serving as blanking input (BI) and/or ripple-blanking output (RBO).

- Note 1. The blanking input (BI) must be open or held at a high logic level when output functions 0 through 15 are desired. The ripple–blanking input (RBI) must be open or high if blanking of a decimal zero is not desired.
- Note 2. When a low logic level is applied directly to the blanking input (BI), all segment outputs are off regardless of the level of any other input.
- Note 3. When ripple-blanking input (RBI) and inputs A, B, C, and D are at a low level with the lamp test input high, all segment outputs go off and the ripple-blanking output (RBO) goes to a low level (response condition).
- Note 4. When the blanking input/ripple blanking output (BI/RBO) is open or held high and a low is applied to the lamp–test input, all segment outputs are on.

### **Pin Connection Diagram** 16 V<sub>CC</sub> B 1 15 f C 2 **14** g LT 3 BI/RBO 4 **13** a **12** b RBI 5 11 c 10 d D 6 A 7 GND 8 9 е

