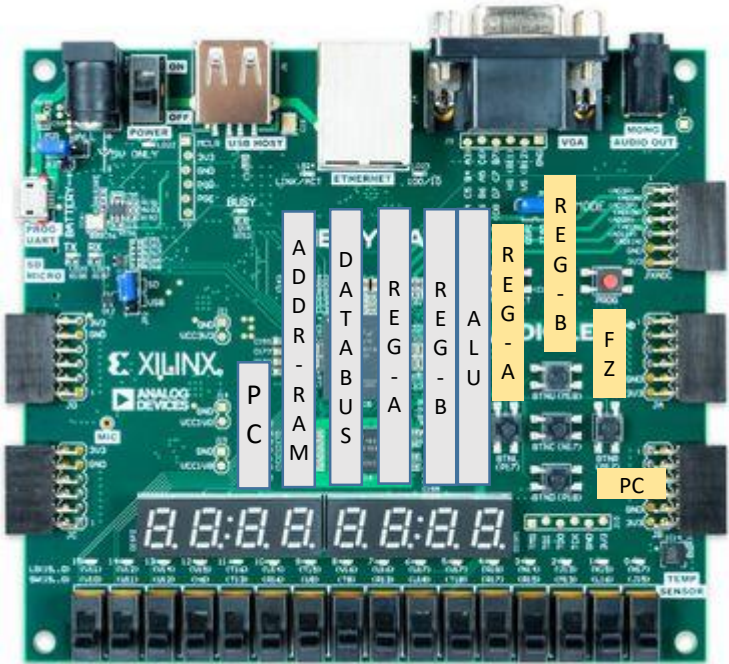


RAM

Contenido ROM

Dir.	Dato	Dir.	Instruction (0COP-OpeA-OpeB)	Inst. (HEX)	Action	Results
0	5	0	0010 1000 0011	283	RAM(8)+RAM(3)→RAM(3)	5+E = 3→M(3)
1	F	1	0000 0001 0100	014	RAM(1) → RAM(4)	F → M(4)
2	E	2	0001 0000 1001	109	RAM(0)+1→RAM(0)	5+1=6 → M(0)
3	E	3	0011 0001 0001	311	RAM(1)-RAM(1) → RAM(1)	F – F = 0 → M(1) → FZ=1
4	E	4	0100 0011 0111	437	When FZ=1, DirSalto→PC	PC=7
5	0	5	0000 0100 0000	040		
6	1	6	0000 0000 0000	000		
7	2	7	0000 0000 0000	000		
8	5					
9	F					
A	0					
B	0					
C	0					
D	0					
E	0					
F	0					



ALU1

ALU0

ADDRESS
PC

REG - INST

WR - RAM

