

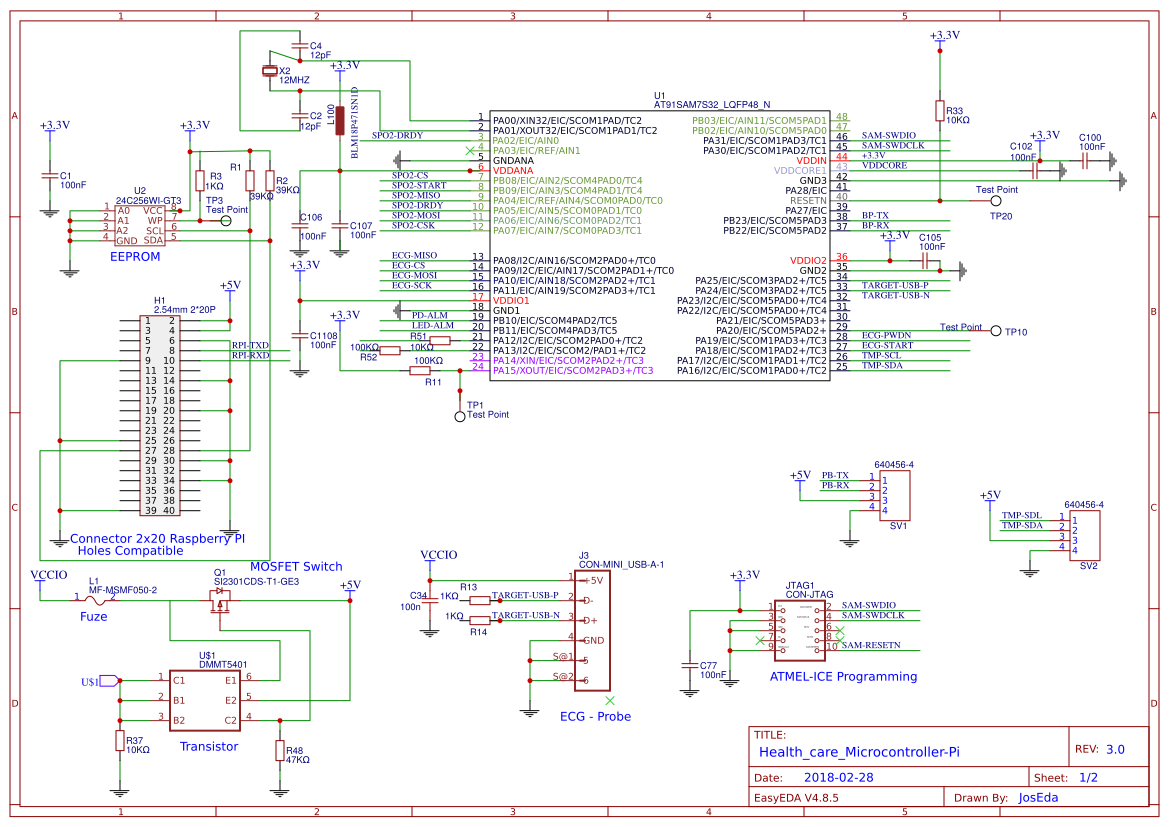
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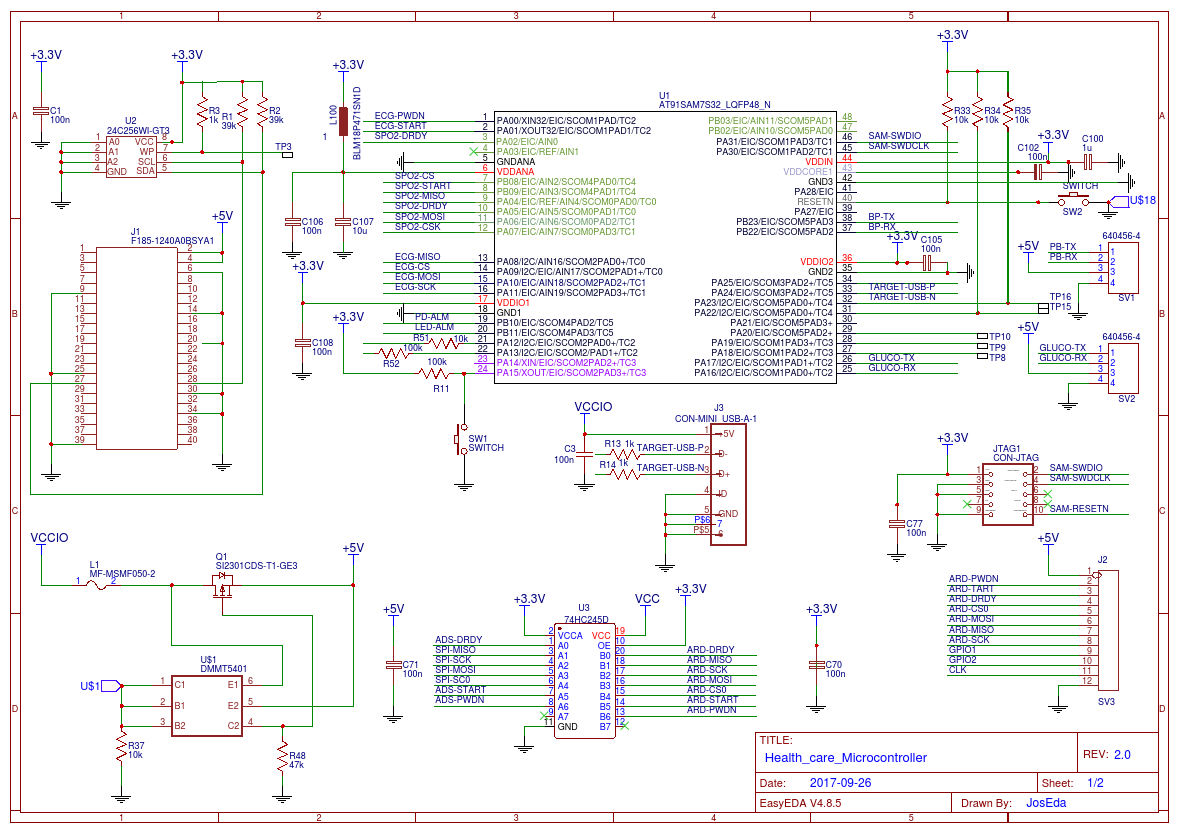
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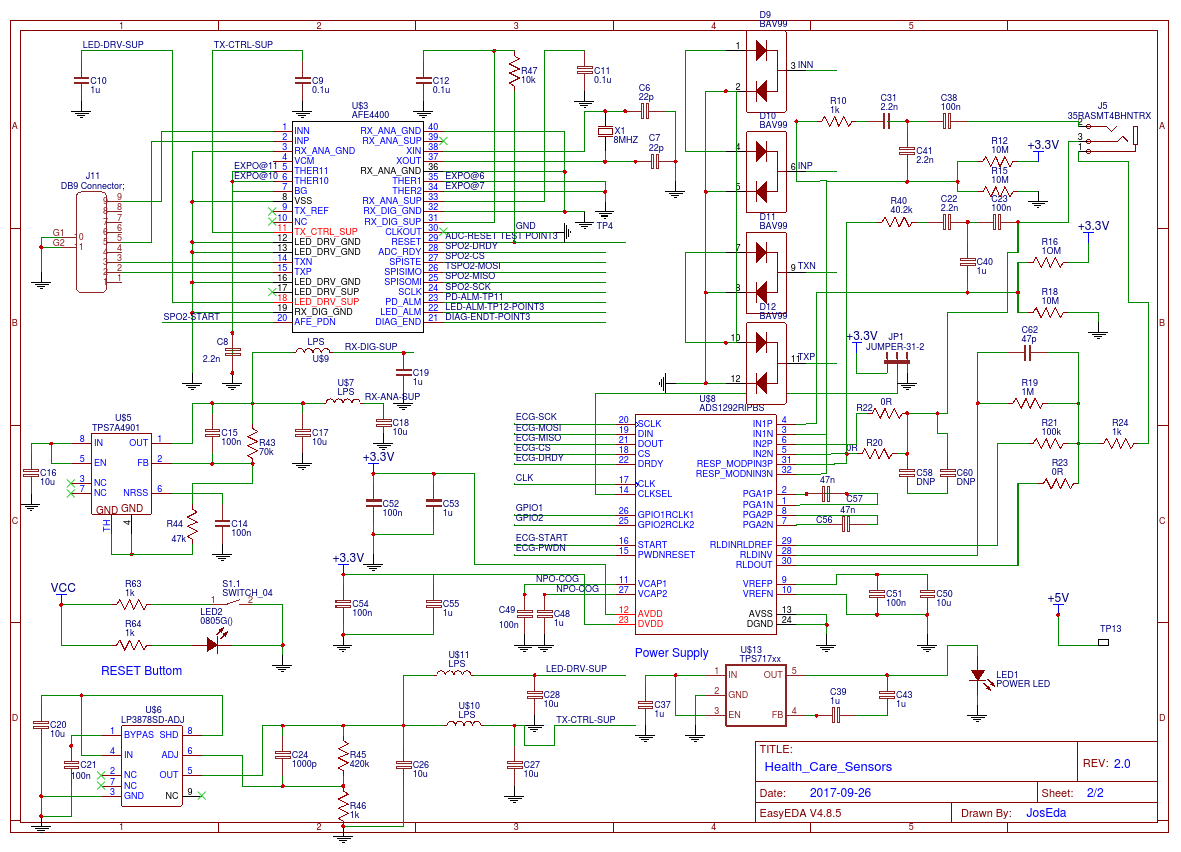
Schematics and Spice sub-circuit



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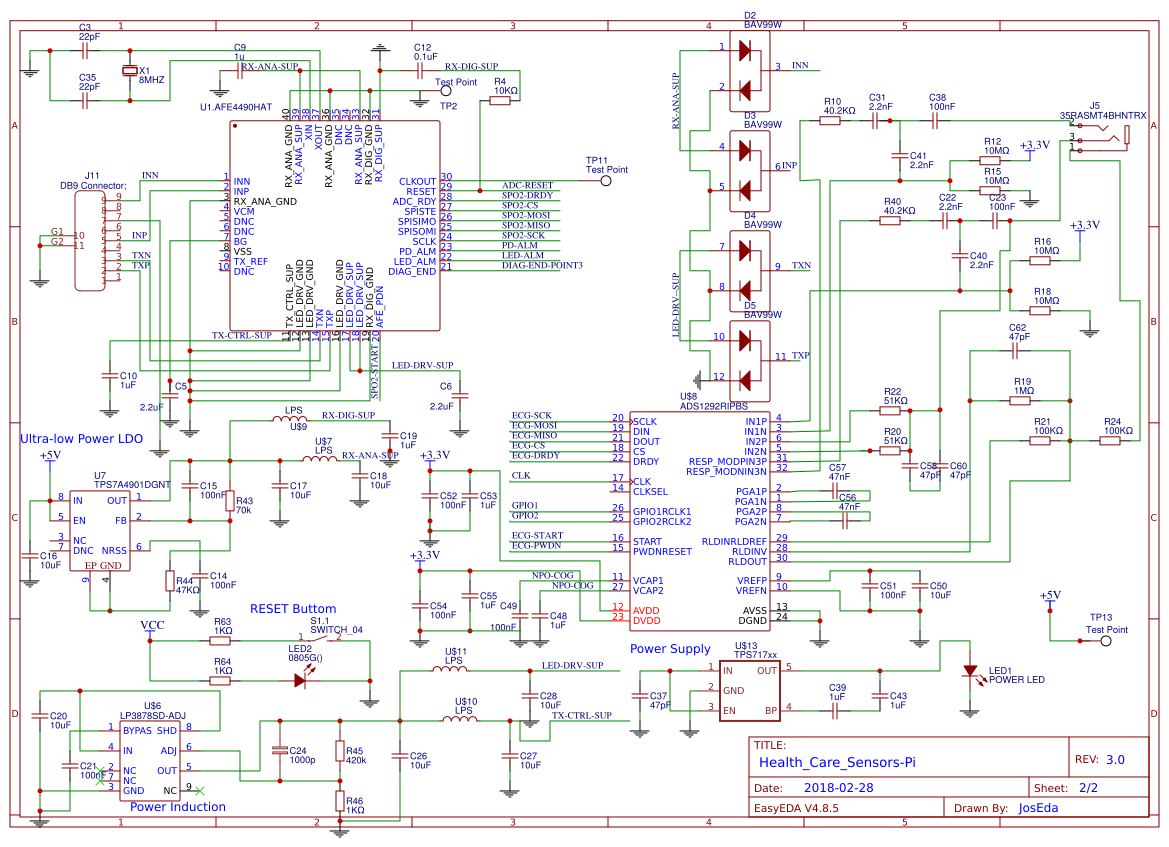
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New Schematic

PCB

New PCB

Simulation

No Simulation





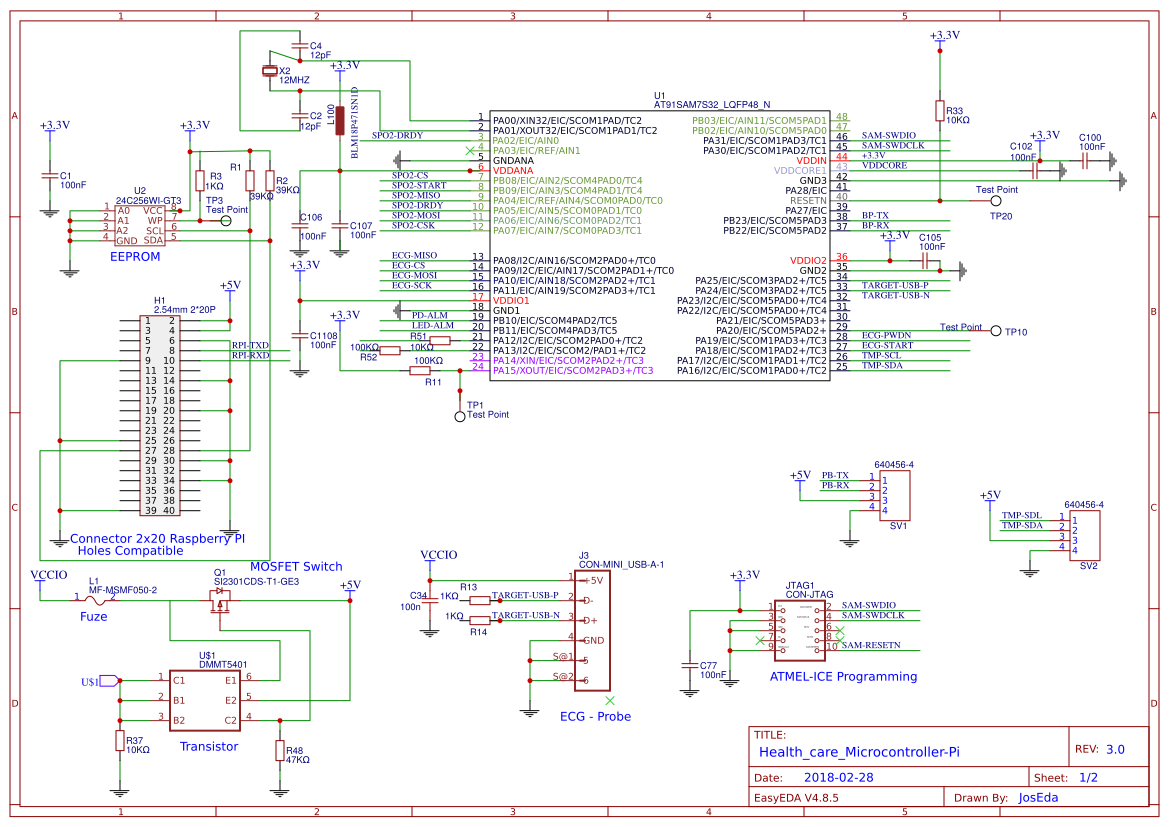
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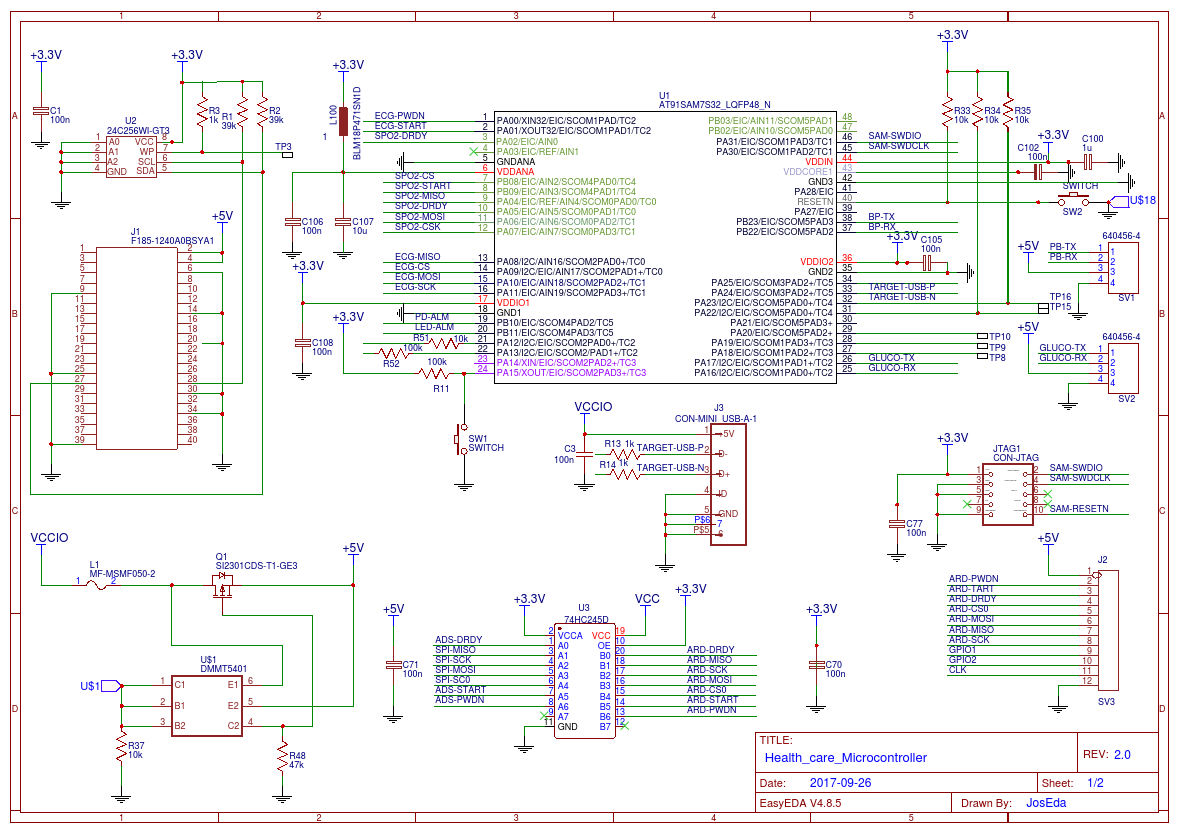
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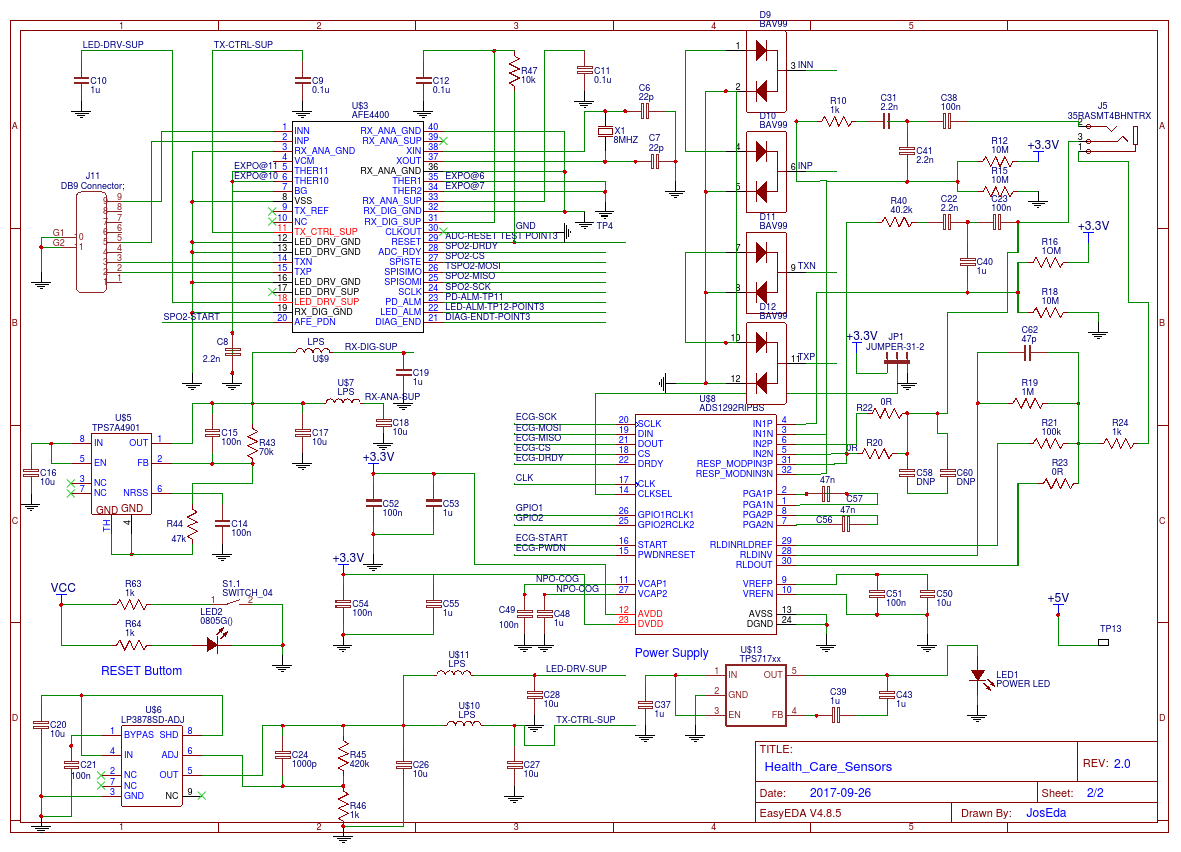
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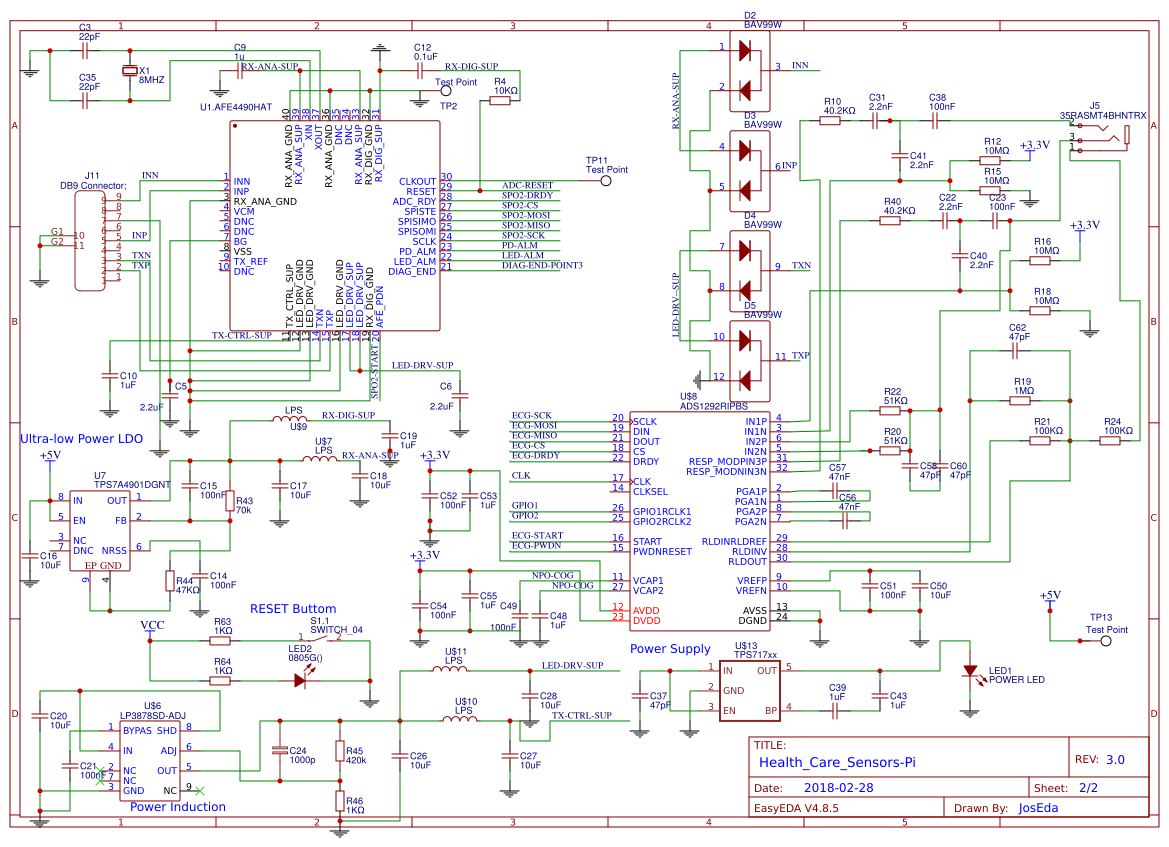
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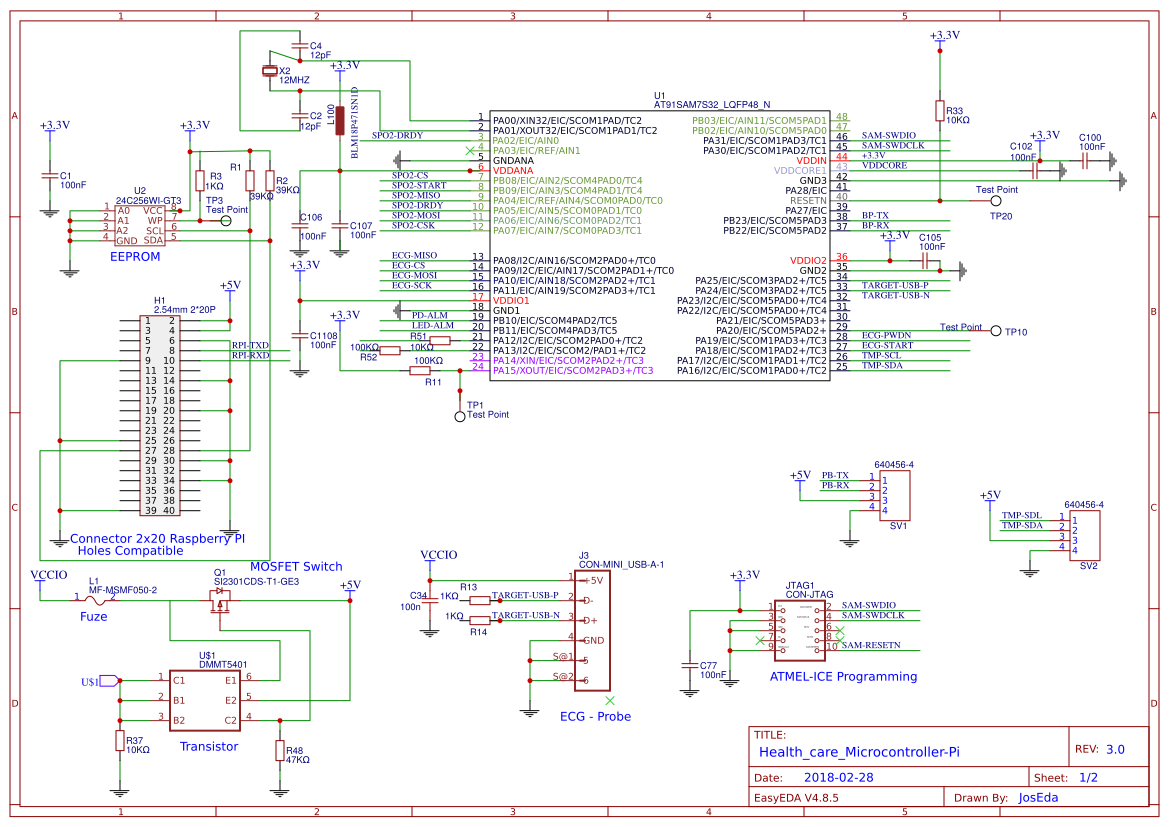
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