## **Synchronization Constructs:**

Atomic Operations (fine-grain synchronization):

jmp L4

```
adata-race.c > No Selection
   #include <stdio.h>
2 #include <math.h>
   #include <omp.h>
   int main(){
        double result={0};
        #pragma omp parallel for shared(result)
        for(int i=0; i<1000000;i++) {
10
            result+=sin(i);
11
12
        printf("%f", result);
13 }
    vmovsd (%r12), %xmm1
 L4:
    vxorpd %xmm0, %xmm0, %xmm0
    vcvtsi2sd %ebx, %xmm0, %xmm0
    vmovsd %xmm1, 8(%rsp)
    addl $1, %ebx
    call sin ; return value in %xmm0
    vmovsd 8(%rsp), %xmm1; result in %xmm1
    cmpl %ebx, %ebp
    vaddsd %xmm0, %xmm1, %xmm1
    ine L4
    vmovsd %xmm1, (%r12)
```

```
#include <stdio.h>
       #include <math.h>
        #include <omp.h>
       int main(){
            double result={0};
            #pragma omp parallel for shared(result)
            for(int i=0; i<1000000;i++) {
                 #pragma omp atomic
                 result+=sin(i);
    11
    12
    13
            printf("%f", result);
    14 }
L12:
   addl $1, %ebx
   cmpl %ebx, %r12d
   je L9
L5:
   vxorpd %xmm0, %xmm0, %xmm0
   vcvtsi2sd %ebx, %xmm0, %xmm0
   call _sin
   movq 0(%rbp), %rcx
   movq (%rcx), %rdx
   vmovq %rdx, %xmm2
   movq wrdx, %rax
   vaddsd %xmm2, %xmm0, %xmm1
   vmovg %xmm1, %rsi
                                   Repeat until successful update
   lock cmpxchgq %rsi, (%rcx)
   cmpq %rax, %rdx
   je L12
   movq %rax, %rdx
```