C:/Users/figue/Documents
/2.Electronics/1.Embedded
_Systems/STM32/Bare_Metal
/Boards/Nucleo_F401RE/Reusable
-Drivers/SPI/include/dio_cfg.h

C:/Users/figue/Documents /2.Electronics/1.Embedded _Systems/STM32/Bare_Metal /Boards/Nucleo_F401RE/Reusable -Drivers/SPI/include/dio.h C:/Users/figue/Documents /2.Electronics/1.Embedded _Systems/STM32/Bare_Metal /Boards/Nucleo_F401RE/Reusable -Drivers/SPI/src/dio_cfg.c

C:/Users/figue/Documents /2.Electronics/1.Embedded _Systems/STM32/Bare_Metal /Boards/Nucleo_F401RE/Reusable -Drivers/SPI/src/dio.c

C:/Users/figue/Documents /2.Electronics/1.Embedded _Systems/STM32/Bare_Metal /Boards/Nucleo_F401RE/Reusable -Drivers/SPI/src/main.c