

Circuit Theory and Electronics Fundamentals

Álvaro Dias Patrício, 95768

Inês de Sousa Afonso, 95797

José Severino Medeiro, 95811

Integrated Master in Aerospace Engineering, Técnico, University of Lisbon

Laboratory 1 Report

February 25, 2021

Contents

1	Introduction	1
2	Theoretical Analysis	2
2.1	Mesh Method	2
2.2	Nodal Method	3
3	Simulation Analysis	4
3.1	Operating Point Analysis	4
3.2	Transient Analysis	5
3.3	Frequency Analysis	5
3.3.1	Magnitude Response	5
3.3.2	Phase Response	5
3.3.3	Input Impedance	5
4	Conclusion	5

1 Introduction

The objective of this laboratory assignment is to study a circuit containing a sinusoidal voltage source V_I connected to a resistor R and a capacitor C in series. The circuit can be seen in Figure 1.

In Section 2, a theoretical analysis of the circuit is presented. In Section 3, the circuit is analysed by simulation, and the results are compared to the theoretical results obtained in Section 2. The conclusions of this study are outlined in Section 4.

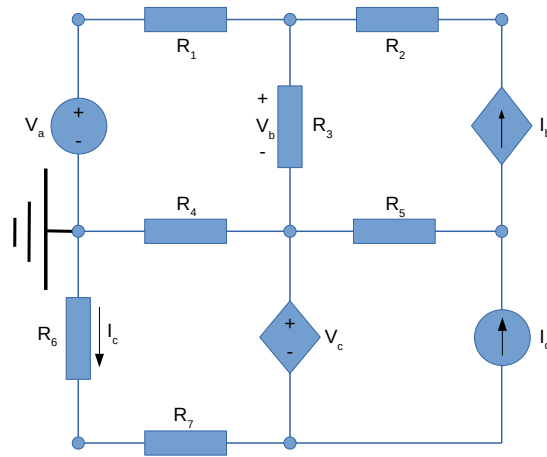


Figure 1: Circuit analysed.

2 Theoretical Analysis

In this section, the circuit shown in Figure 1 is analyzed theoretically with the Mesh Method and with the Nodal Method, in order to complement each other results.

2.1 Mesh Method

The Mesh Method consists in introducing currents that circulate in the meshes of the circuit, that is, in the loops that do not contain other loops, as shown in Figure 2, and then evaluate the circuit based on the new currents.

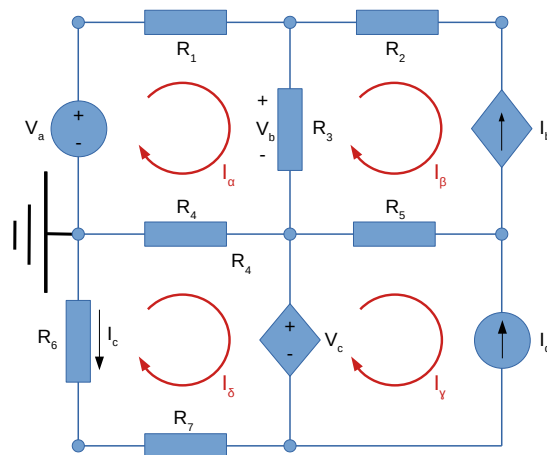


Figure 2: Circuit analysed with mesh currents.

After identifying the mesh currents, the next step in this method is to use the Kirchhoff Voltage Law (KVL) in the meshes that do not contain current sources (mesh α (1) and δ (2)) and to relate the mesh currents to the currents imposed by the sources (mesh β (3) and γ (4)):

$$R_1 I_\alpha + V_b + R_4 (I_\alpha - I_\delta) - V_a = 0; \quad (1)$$

$$R_6 I_\delta + R_4(I_\delta - I_\alpha) + V_c + R_7 I_\delta = 0; \quad (2)$$

$$I_\beta = -I_b; \quad (3)$$

$$I_\gamma = -I_d. \quad (4)$$

Since there are 8 variables in the circuit, $I_\alpha, I_\beta, I_\gamma, I_\delta, V_b, V_c, I_b, I_c$, there must be more four independent equations: two of them are already given,

$$I_b = K_b V_b; \quad (5)$$

$$V_c = K_c I_c. \quad (6)$$

The other two are found by examining the circuit and with Ohm's Law:

$$I_c = -I_\delta; \quad (7)$$

$$V_b = R_3(I_\alpha - I_\beta) \quad (8)$$

The solution to this linear system of equations is determined by Octave:

Name	Value [A or V]
@ I_α	2.342031e-04
@ I_β	2.455590e-04
@ I_γ	-1.047395e-03
@ I_δ	-9.770070e-04
V_b	-3.471054e-02
V_c	8.034375e+00
@ I_b	-2.455590e-04
@ I_c	9.770070e-04

Table 1: Variables in the Mesh Method. A variable preceded by @ is of type *current* and expressed in Ampere; other variables are of type *voltage* and expressed in Volt.

2.2 Nodal Method

In the Nodal Method, the determination of the values of current and voltage is made by, firstly, finding all the knots in the circuit, as made in Figure 3.

Then, it is used the Kirchhoff Current Law (KCL) in the nodes that are not connected to voltage sources (Equations 9 to 12) and relate the knots voltages with the voltage sources that are connected to them (Equations 13 and 14):

$$\frac{v_3 - v_2}{R_2} - \frac{V_b}{R_3} + \frac{v_1 - v_2}{R_1} = 0; \quad (9)$$

$$I_b + \frac{v_2 - v_3}{R_2} = 0; \quad (10)$$

$$\frac{v_5 - v_6}{R_5} - I_b + I_d = 0; \quad (11)$$

$$I_c + \frac{v_8 - v_7}{R_7} = 0; \quad (12)$$

$$v_1 - v_4 = V_a; \quad (13)$$

$$v_5 - v_8 = V_c. \quad (14)$$

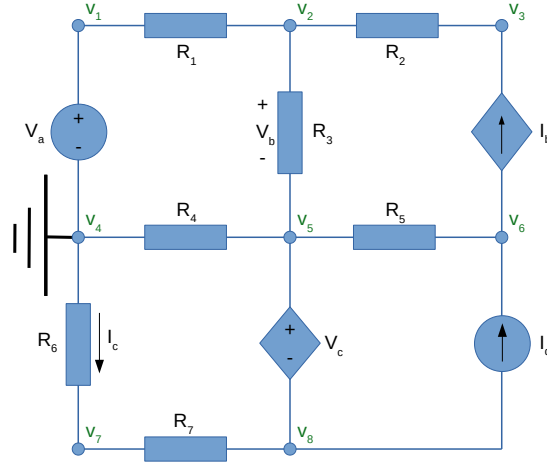


Figure 3: Circuit analysed with nodal voltages.

Since there are 8+4 variables (v_1 to v_8 , V_b , V_c , I_b , I_c), the system is defined by twelve independent equations. Two are provided in the circuit and are the same as in the Mesh Method, (5) and (6). By observing the circuit,

$$v_5 - v_8 = V_b. \quad (15)$$

Using Ohm's Law, we find the relation:

$$I_c = \frac{v_4 - v_7}{R_6}. \quad (16)$$

Because there needs to be a knot with a defined voltage, we chose v_4 to be connected to the ground:

$$v_4 = 0. \quad (17)$$

For the last equation, the continuity of current in the circuit can be used to create a “super-knot”, bypassing the voltage sources V_a and V_c , from which the equations are (18) and (19), respectively:

$$\frac{v_5 - v_4}{R_4} + \frac{v_2 - v_1}{R_1} - I_c = 0; \quad (18)$$

$$\frac{v_7 - v_8}{R_7} - I_d + \frac{v_6 - v_5}{R_5} + \frac{V_b}{R_3} + \frac{v_4 - v_5}{R_4} = 0. \quad (19)$$

Given that there only one equation is needed, we chose to use the more simple (18). The solution to this linear system of equations is determined by Octave:

3 Simulation Analysis

3.1 Operating Point Analysis

Table 3 shows the simulated operating point results for the circuit under analysis. Compared to the theoretical analysis results, one notices the following differences: describe and explain the differences.

Name	Value [A or V]
v_1	5.198324e+00
v_2	4.956490e+00
v_3	4.450997e+00
v_4	0.000000e+00
v_5	4.991201e+00
v_6	9.002253e+00
v_7	-2.050829e+00
v_8	-3.043174e+00
V_b	-3.471054e-02
V_c	8.034375e+00
@ I_b	-2.455590e-04
@ I_c	9.770070e-04

Table 2: Variables in the Nodal Method. A variable preceded by @ is of type *current* and expressed in Ampere; other variables are of type *voltage* and expressed in Volt.

3.2 Transient Analysis

Figure ?? shows the simulated transient analysis results for the circuit under analysis. Compared to the theoretical analysis results, one notices the following differences: describe and explain the differences.

3.3 Frequency Analysis

3.3.1 Magnitude Response

Figure ?? shows the magnitude of the frequency response for the circuit under analysis. Compared to the theoretical analysis results, one notices the following differences: describe and explain the differences.

3.3.2 Phase Response

Figure ?? shows the magnitude of the frequency response for the circuit under analysis. Compared to the theoretical analysis results, one notices the following differences: describe and explain the differences.

3.3.3 Input Impedance

Figure ?? shows the magnitude of the frequency response for the circuit under analysis. Compared to the theoretical analysis results, one notices the following differences: describe and explain the differences.

4 Conclusion

In this laboratory assignment the objective of analysing an RC circuit has been achieved. Static, time and frequency analyses have been performed both theoretically using the Octave maths tool and by circuit simulation using the Ngspice tool. The simulation results matched the theoretical results precisely. The reason for this perfect match is the fact that this is a straightforward

Name	Value [A or V]
@gb[i]	-2.45559e-04
@id[current]	1.047395e-03
@r1[i]	2.342031e-04
@r2[i]	2.455590e-04
@r3[i]	-1.13560e-05
@r4[i]	-1.21121e-03
@r5[i]	-1.29295e-03
@r6[i]	9.770070e-04
@r7[i]	9.770070e-04
v(1)	5.198324e+00
v(2)	4.956490e+00
v(3)	4.450997e+00
v(4)	0.000000e+00
v(5)	4.991201e+00
v(6)	9.002253e+00
v(7)	-2.05083e+00
v(8)	-3.04317e+00
v(2,5)	-3.47105e-02
v(5,8)	8.034375e+00

Table 3: Operating point. A variable preceded by @ is of type *current* and expressed in Ampere; other variables are of type *voltage* and expressed in Volt.

circuit containing only linear components, so the theoretical and simulation models cannot differ. For more complex components, the theoretical and simulation models could differ but this is not the case in this work.

Lorem ipsum dolor sit amet, consectetur adipiscing elit. Ut purus elit, vestibulum ut, placerat ac, adipiscing vitae, felis. Curabitur dictum gravida mauris. Nam arcu libero, nonummy eget, consectetur id, vulputate a, magna. Donec vehicula augue eu neque. Pellentesque habitant morbi tristique senectus et netus et malesuada fames ac turpis egestas. Mauris ut leo. Cras viverra metus rhoncus sem. Nulla et lectus vestibulum urna fringilla ultrices. Phasellus eu tellus sit amet tortor gravida placerat. Integer sapien est, iaculis in, pretium quis, viverra ac, nunc. Praesent eget sem vel leo ultrices bibendum. Aenean faucibus. Morbi dolor nulla, malesuada eu, pulvinar at, mollis ac, nulla. Curabitur auctor semper nulla. Donec varius orci eget risus. Duis nibh mi, congue eu, accumsan eleifend, sagittis quis, diam. Duis eget orci sit amet orci dignissim rutrum.