INSTITUTO FEDERAL
DE EDUCAÇÃO, CIÊNCIA E TECNOLOGIA
São Paulo

Simulação

TinkerCad e SimulIDE

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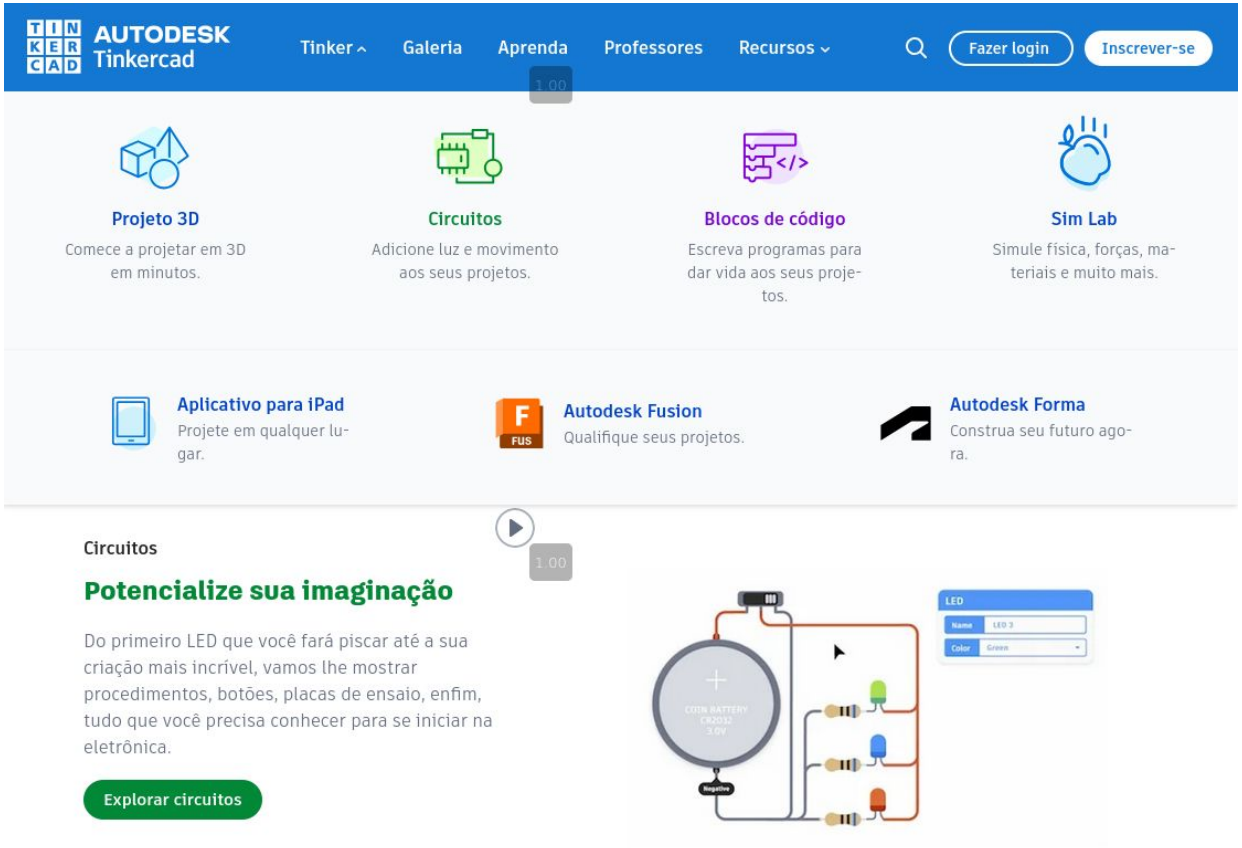
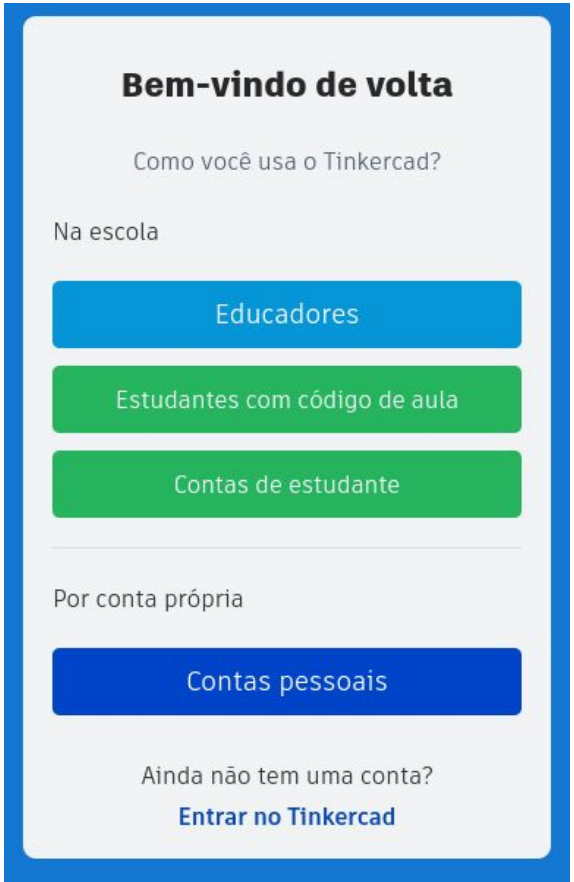
josewrpereira.github.io/docs

https://www.tinkercad.com/



Login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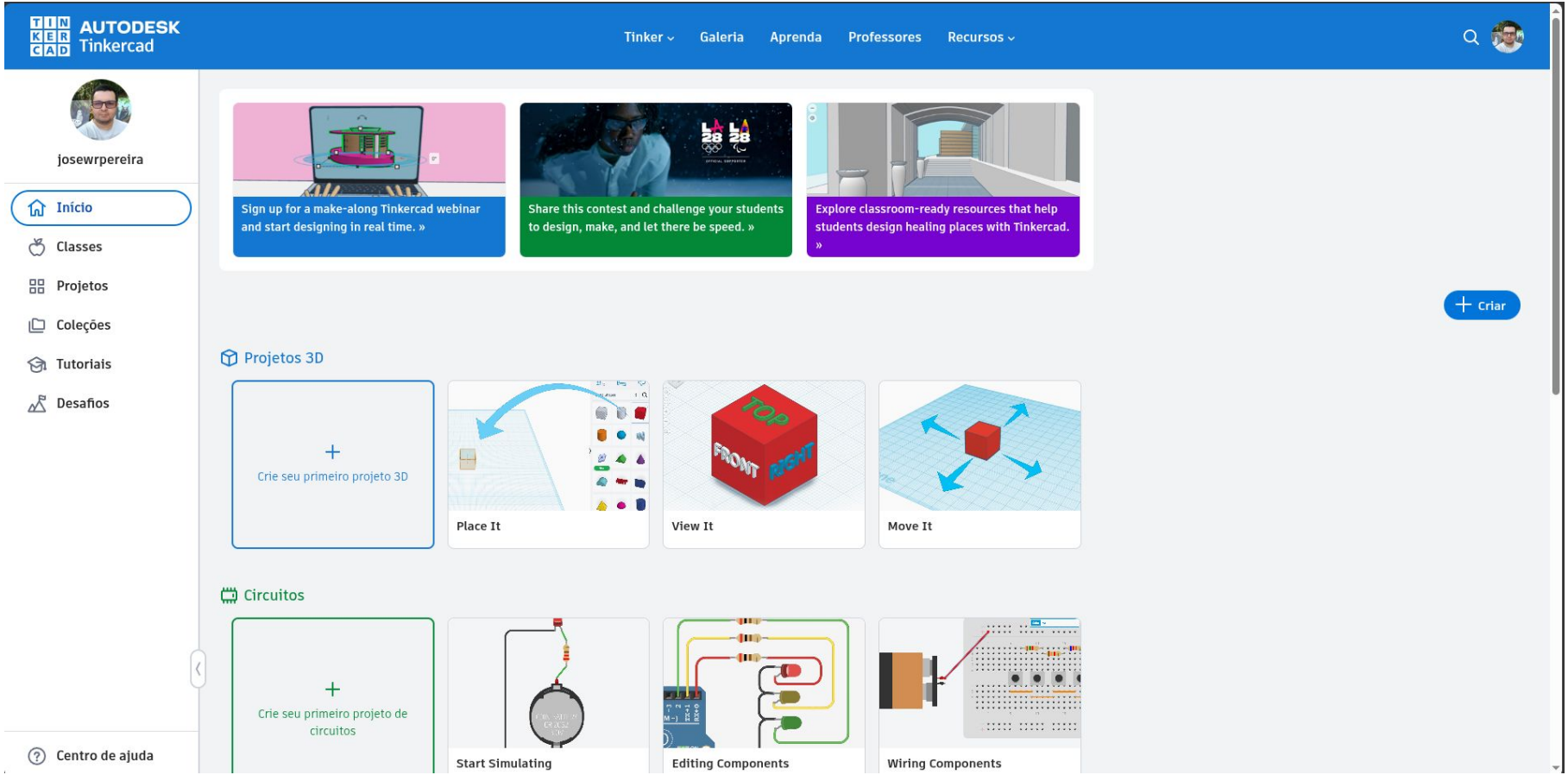
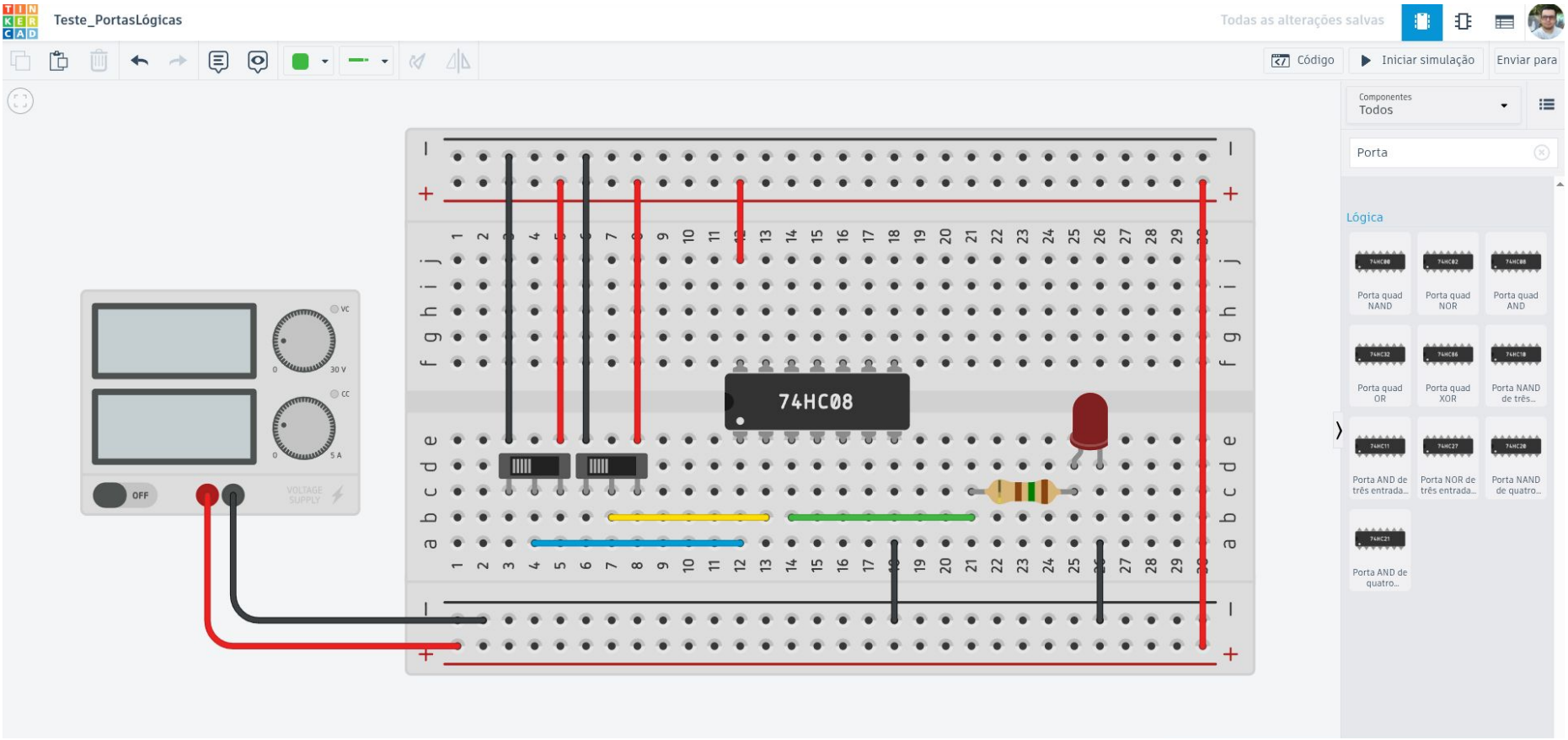
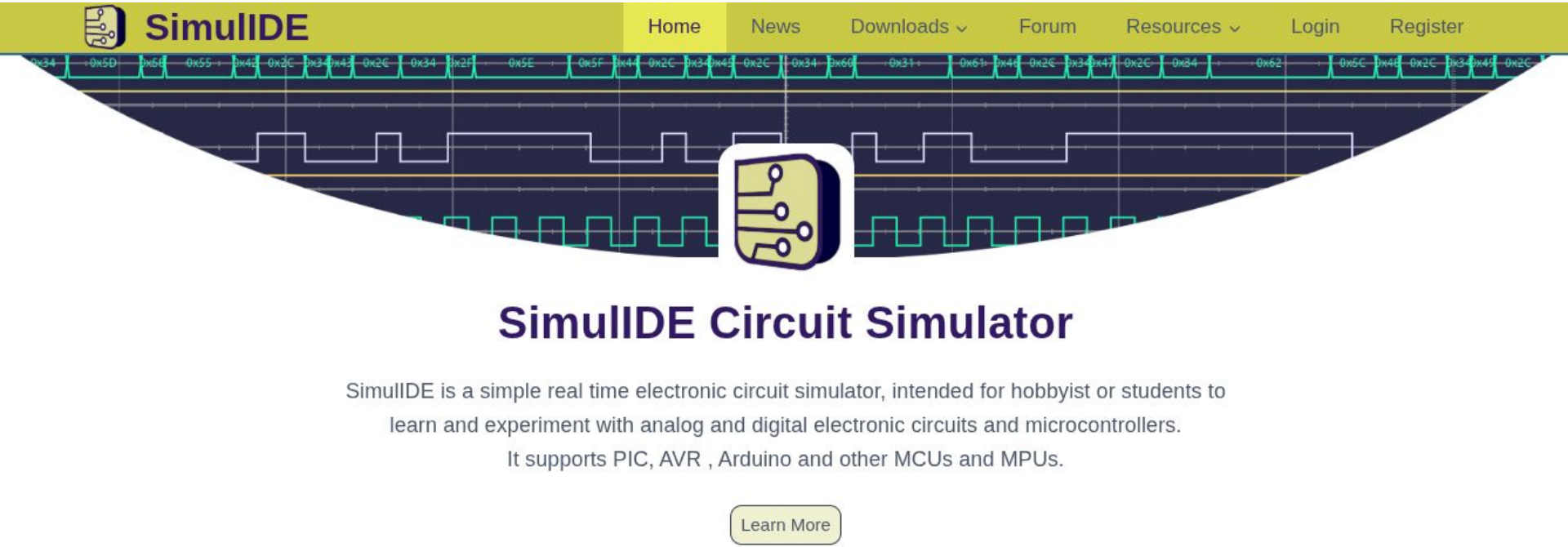
Tela inicial



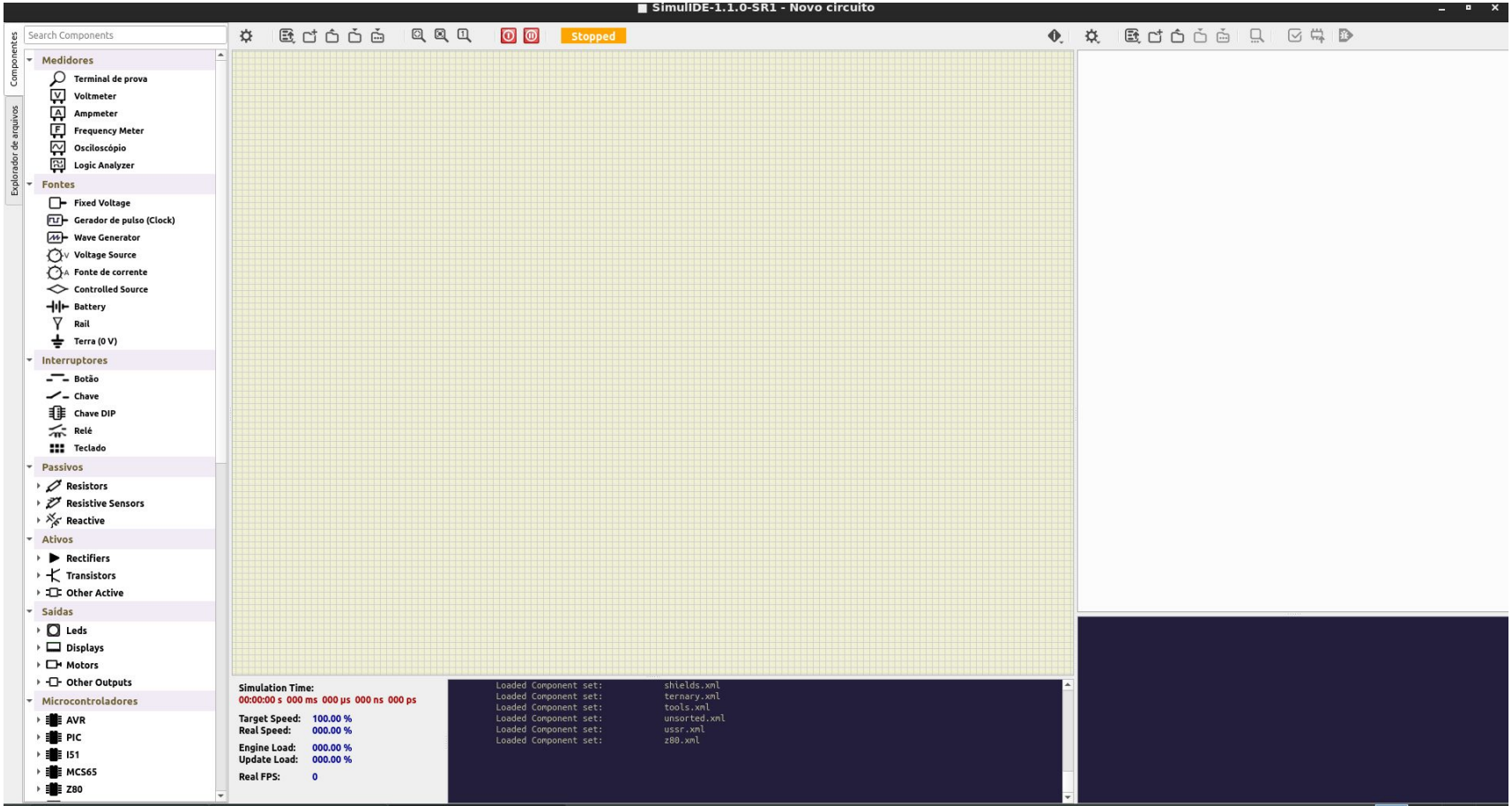
Circuito de teste das portas lógicas



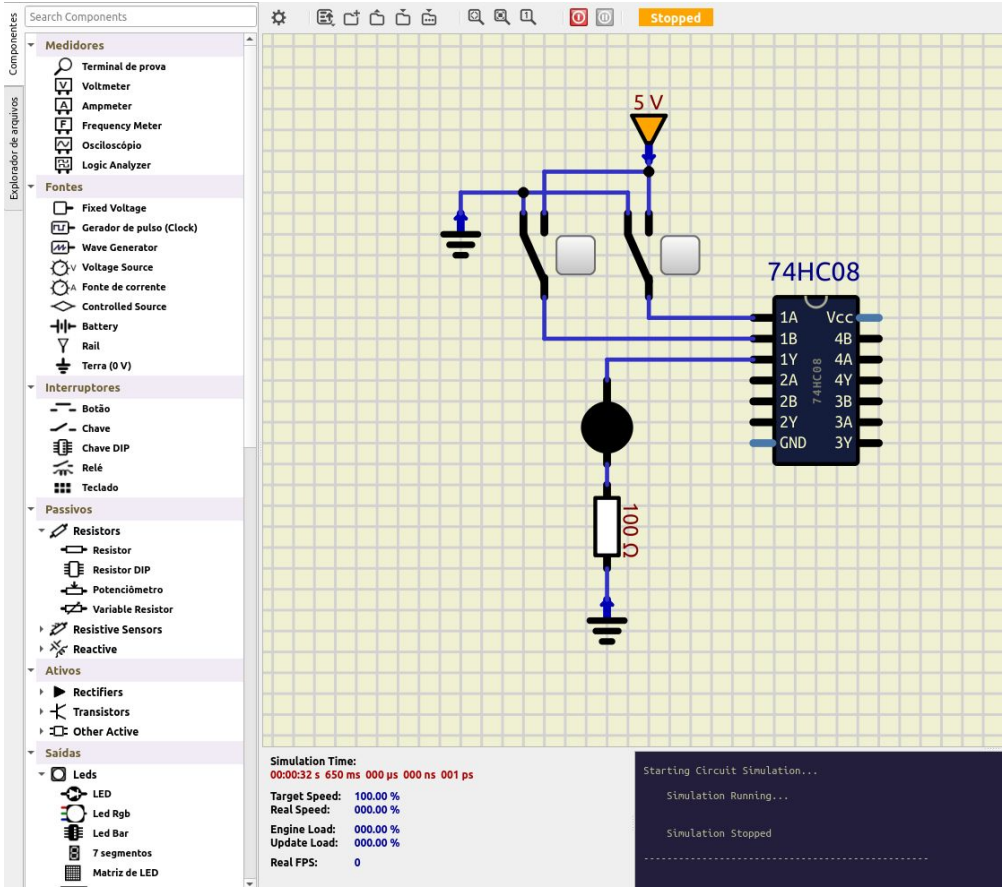
https://simulide.com/p/



SimulIDE


SimulIDE-1.1.0-SR1 - Novo circuito
Search Components
Componentes
Explorador de arquivos
Medidores
Terminal de prova
Voltmeter
Ampmeter
Frequency Meter
Osciloscópio
Logic Analyzer
Fontes
Fixed Voltage
Gerador de pulso (Clock)
Wave Generator
Voltage Source
Fonte de corrente
Controlled Source
Battery
Rail
Terra (0 V)
Interruptores
Botão
Chave
Chave DIP
Relé
Teclado
Passivos
Resistors
Resistive Sensors
Reactive
Ativos
Rectifiers
Transistors
Other Active
Saídas
Leds
Displays
Motors
Other Outputs
Microcontroladores
AVR
PIC
I51
MCS65
Z80
Stopped
Simulation Time:
00:00:00 s 000 ms 000 µs 000 ns 000 ps
Target Speed: 100.00 %
Real Speed: 000.00 %
Engine Load: 000.00 %
Update Load: 000.00 %
Real FPS: 0
Loaded Component set: shields.xml
Loaded Component set: ternary.xml
Loaded Component set: tools.xml
Loaded Component set: unsorted.xml
Loaded Component set: ussr.xml
Loaded Component set: z80.xml

Circuito de teste da porta lógica



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