

# Positive and negative sequence currents to improve voltages during unbalanced faults

CITCEA

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## 1 Definition

We start by defining the grid code requirements as shown in Figure 1. The positive sequence plot corresponds to the Danish grid code as explained in [mohseni2012review], and matches with the one described by National Grid in [nationalgrid]. The positive sequence current to inject can be regarded as a function of the positive sequence voltage, and the same applies to the negative sequence current with respect to the negative sequence voltage. Note that the voltages are in reality the pre-correction ones, that is, we suppose they are not yet influenced by the injection of current coming from the VSC.

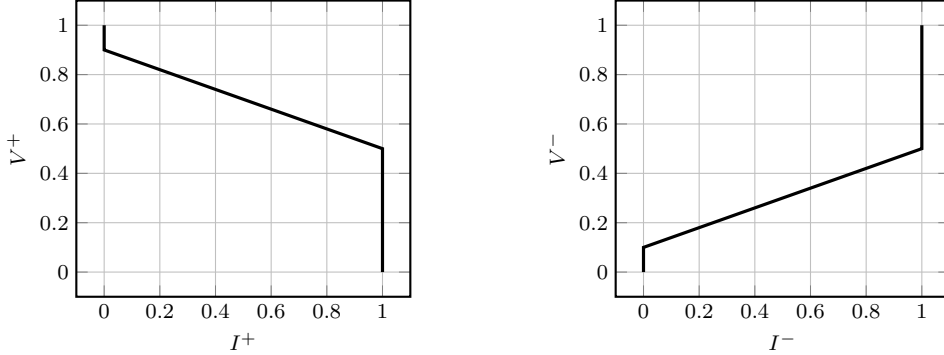


Figure 1: Grid code requirements for the positive and the negative sequence in case of voltage drops

This same profile is expressable in the form of a piecewise function for the positive sequence:

$$\begin{cases} I^+ = 0 & V^+ \geq 0.9 \\ I^+ = k_p(0.9 - V^+) & 0.5 \leq V^+ < 0.9 \\ I^+ = 1 & V^+ < 0.5 \end{cases} \quad (1)$$

where the  $k_p$  parameter is responsible for characterizing the slope of drop, and as can be deduced from Figure 1, it takes the value of 2.5. We have assumed the maximum current supported by the VSC to be 1.

Similarly, for the negative sequence:

$$\begin{cases} I^- = 0 & V^- \leq 0.1 \\ I^- = k_n(V^- - 0.1) & 0.1 \leq V^- < 0.5 \\ I^- = 1 & V^- > 0.5 \end{cases} \quad (2)$$

where  $k_n = 2.5$  as well.

To keep the expressions simple enough, we have not made any distinction between a positive or a negative voltage, nor a positive or negative current. As far as I understand it, we are concerned with improving the absolute value of the voltages, and hence, its angle is not specially relevant. However, it has to be considered to determine the direction of the current phasors. One approach would be to inject only reactive currents, as it is a commonality in grid codes [**mohseni2012review**, **haddadi2020negative**]. This is precisely the perspective taken in this study. Thus, the positive sequence current has to take a negative value to cause a positive voltage drop while the reactive negative sequence current has to become negative.

Besides, there is a potential incompatibility in the plots shown in Figure 1. For instance, the fault may turn out to be extremely severe and have both the positive and the negative sequence voltages approaching 0.5. In this case, the positive and the negative sequence current as well will tend to 1. Most likely some of the *abc* currents will surpass the limit. Therefore, some law is necessary to break from this conflicting situation and look for a trade-off.