

Positive and negative sequence currents to improve voltages during unbalanced faults

Josep Fanals

03/2021

1 Optimization overview and constant impedance analysis

The system we are considering is formed by an ideal grid (only positive sequence voltage is present) coupled to a VSC. The model is depicted in Figure 1. The VSC will be controlled in a way that leads to an improvement in the voltage at the PCC.

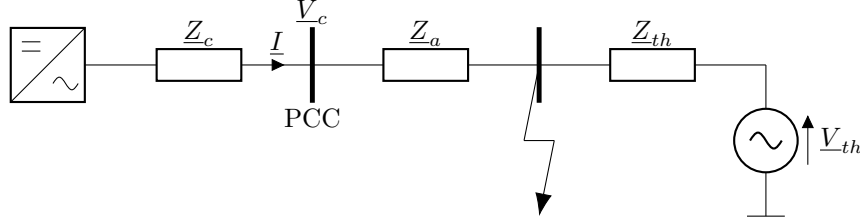


Figure 1: Single-phase representation of the simple system under a fault

This study considers the balanced fault type but also the unbalanced ones, which include the line to ground, the double line to ground and the line to line fault. The model in Figure 1 attempts to describe simple yet sufficient system to test the influence of the injected currents by the VSC on the voltage at the PCC. The system could be complicated by adding parallel capacitances on both sides of \underline{Z}_a to model a hypothetical submarine cable. However, we prioritize keeping the system simple.

We are concerned with improving the voltage \underline{V}_c as a function of the current \underline{I} injected by the converter. Formally speaking there is no clear definition on what improving the voltage means as it depends on rather pre-established preferences. For instance, one could try to maximize the positive sequence voltage, minimize the negative sequence voltage or even maximize the difference between both. These three strategies have been covered in [camacho2017positive]. A more flexible approach is based on defining the objective function as

$$f(\underline{I}^+, \underline{I}^-) = \lambda^+ |(|\underline{V}_c^+(\underline{I}^+, \underline{I}^-)| - 1)| + \lambda^- |(|\underline{V}_c^-(\underline{I}^+, \underline{I}^-)| - 0)|, \quad (1)$$

where the weighting factors $\lambda^+, \lambda^- \in \mathbb{R}$. By adjusting these factors one can follow the three aforementioned strategies. Note that the goal is to obtain a positive sequence voltage as close as possible to one (in per unit) while simultaneously approaching zero in the negative sequence voltage. Even though the problem is described as a function of the positive and negative sequence currents, it could also be stated as a function of the original abc currents without loss of generality. The associated expressions are gathered in the appendix as well.

Minimizing f does not come with complete freedom. That is, the currents are constrained so as not to exceed the IGBT limits. It becomes more convenient to express the constraints in the abc frame:

$$g(\underline{I}_a, \underline{I}_b, \underline{I}_c) = \begin{cases} |\underline{I}_a| \leq I_{max}, \\ |\underline{I}_b| \leq I_{max}, \\ |\underline{I}_c| \leq I_{max}. \end{cases} \quad (2)$$

For now we are not concerned with the voltages imposed to the semiconductors due to the fact that the filter \underline{Z}_c is most likely to take small values. In practical situations current limitations are the most serious constraint. Therefore, we define the optimization problem as follows:

$$\min_{\underline{I}^+, \underline{I}^-} f(\underline{I}^+, \underline{I}^-) \quad (3a)$$

$$\text{subject to } g(\underline{I}_a, \underline{I}_b, \underline{I}_c), \quad (3b)$$

where the currents in the abc frame can be related to the currents expressed in the symmetrical components form by means of Fortescue's transformation, and vice versa [fortescue1918method].

As a direct consequence of that, the analysis can be performed in the abc frame while expressing the voltages as a function of the positive, negative and homopolar components. Another valid procedure is to work with the symmetrical components and relate the currents to the abc ones. Both ways to confront the problem are equally valid. The results obtained in this study have been generated and validated with both paths.

Current grid codes define the low voltage ride through (LVRT) limit curve which indicates the relation between the duration of a fault and the voltage frontier at which for instance a wind turbine ought to disconnect. Such LVRT curves present slight variations from country to country [tsili2009review] but they all share the same pattern: in case of a severe fault, the disconnection should not take place if it has a low duration; on the contrary, less noticeable faults imply a larger disconnection time. Grid codes usually impose a curtailment of active power and enforce the generation of reactive power in order to collaborate on raising the voltage [altin2010overview, serban2016voltage].

Nevertheless, studies dealing with determining the optimal currents to inject are not precisely numerous. At most, some authors express the voltages in terms of active and reactive power but do not consider constraints [camacho2012flexible]. Others formulate the optimization problem and arrive to a closed-form expression, even though it becomes iterative [camacho2017positive]. The analysis is performed contemplating powers rather than currents. Because of that, this work focuses on computing the optimal currents to increase the positive sequence voltage as close to one as possible and achieving a negative sequence voltage that approaches zero. In essence, the results that follow come from solving Equations 3a and 3b.

Four types of faults are considered in this study. One is the balanced fault, which yields a simple yet valuable system to analyze the distribution of optimal currents. The remaining faults are unbalanced: the line to ground, the line to line and the double line to ground cases.

In all situations, we show plots representing the objective function depending on the real and imaginary parts of the positive and negative sequence currents. They are obtained in a brute force manner, that is, we generate multiple combinations of currents and store those points if they do not respect the constraints. The solution obtained by solving the optimization problem as such with the SciPy library is also displayed, together with optimal point supposing no active currents can be injected. The latter tries to emulate the outcome of following the grid codes to improve voltages. Note that grid codes also consider the injection of active power, but this serves the purpose of maintaining a stable system in terms of frequency, something not contemplated in this analysis.

For this and all the upcoming faults we have set the values shown in Table 1.

Magnitude	Value (pu)
\underline{Z}_f	$0.00 + 0.10j$
\underline{Z}_a	$0.01 + 0.10j$
\underline{Z}_{th}	$0.01 + 0.05j$
I_{max}	1.00
λ^+	1.00
λ^-	1.00
$ \underline{V}_{th}^+ $	1.00
$ \underline{V}_{th}^- $	0.00

Table 1: Values for the system under study

We have considered the impedances to be mainly inductive. However, adding a resistive part not only makes them more realistic, but it also may shed some light on if the truly optimal decision is to inject only reactive currents. One could anticipate that the larger the resistive part becomes, the greater the active current should be to cause a considerable voltage drop (positive or negative) so that the voltage is improved. Thus, for the given impedances in Table 1, we foresee the fact that reactive currents will turn out to be substantially larger than active currents. Besides, the same weighting is arbitrarily attributed to the positive sequence minimization subfunction as well as to the negative

sequence one.

1.1 Balanced fault

Balanced faults are commonly referred to as the most severe type of fault, as currents take the largest values [kothari2003modern]. Its representation in symmetrical components indicates a decoupling between sequences. As a result of that, a considerable positive sequence current should be injected to increase the positive sequence voltage. It should be mostly inductive. On the other hand, no negative sequence current has to be injected to keep a null negative sequence voltage (assuming that the grid presents no negative sequence voltage, as it is the case here). There are no homopolar currents in this and all the subsequent faults.

Figure 2 shows on the one hand the results with the brute force methodology. We have employed over 20^4 combinations to construct the plots. A more regular shape would be obtained in case we worked with smaller intervals. However, some points are suppressed because they exceed the current limitations. It is clear that the minimum is achieved when the negative sequence currents, both real and imaginary, tend to zero. The imaginary positive sequence current takes extreme negative values - close to I_{max} - while the real part remains small.

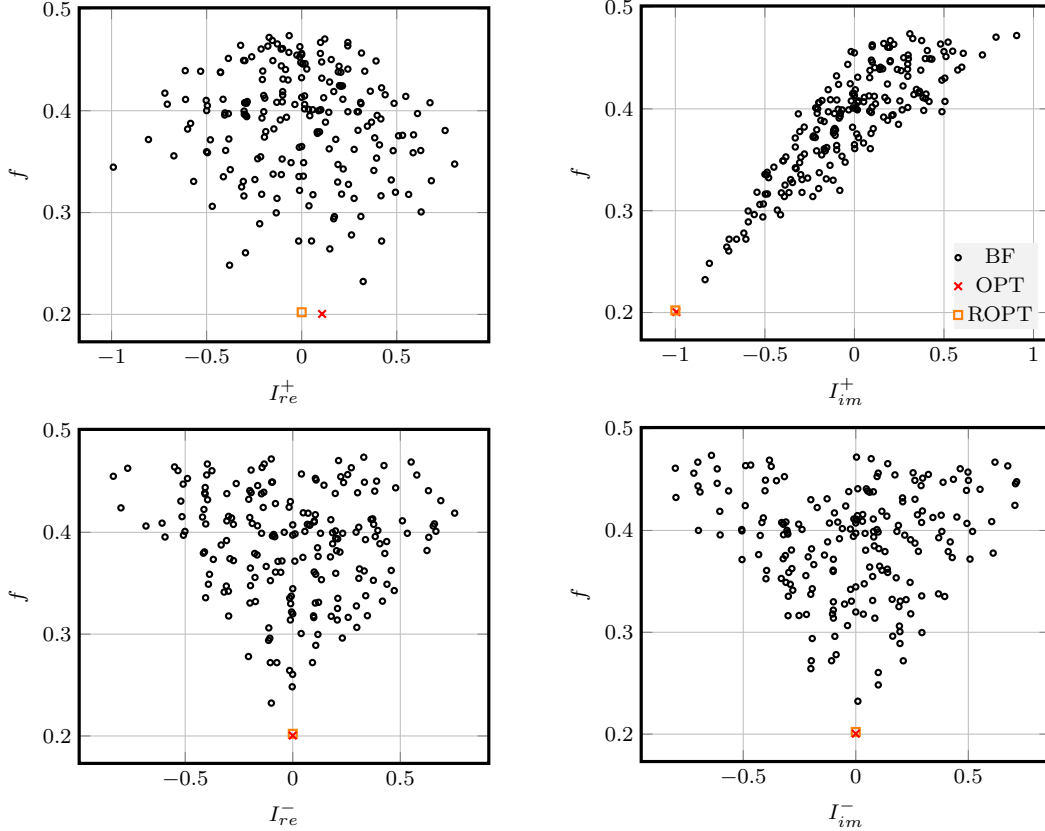


Figure 2: Influence of the currents on the objective function for the balanced fault when $\lambda^+ = 1$ and $\lambda^- = 1$. BF: brute force, OPT: solution to the optimization problem, ROPT: solution to the optimization problem restricted to only injecting reactive power.

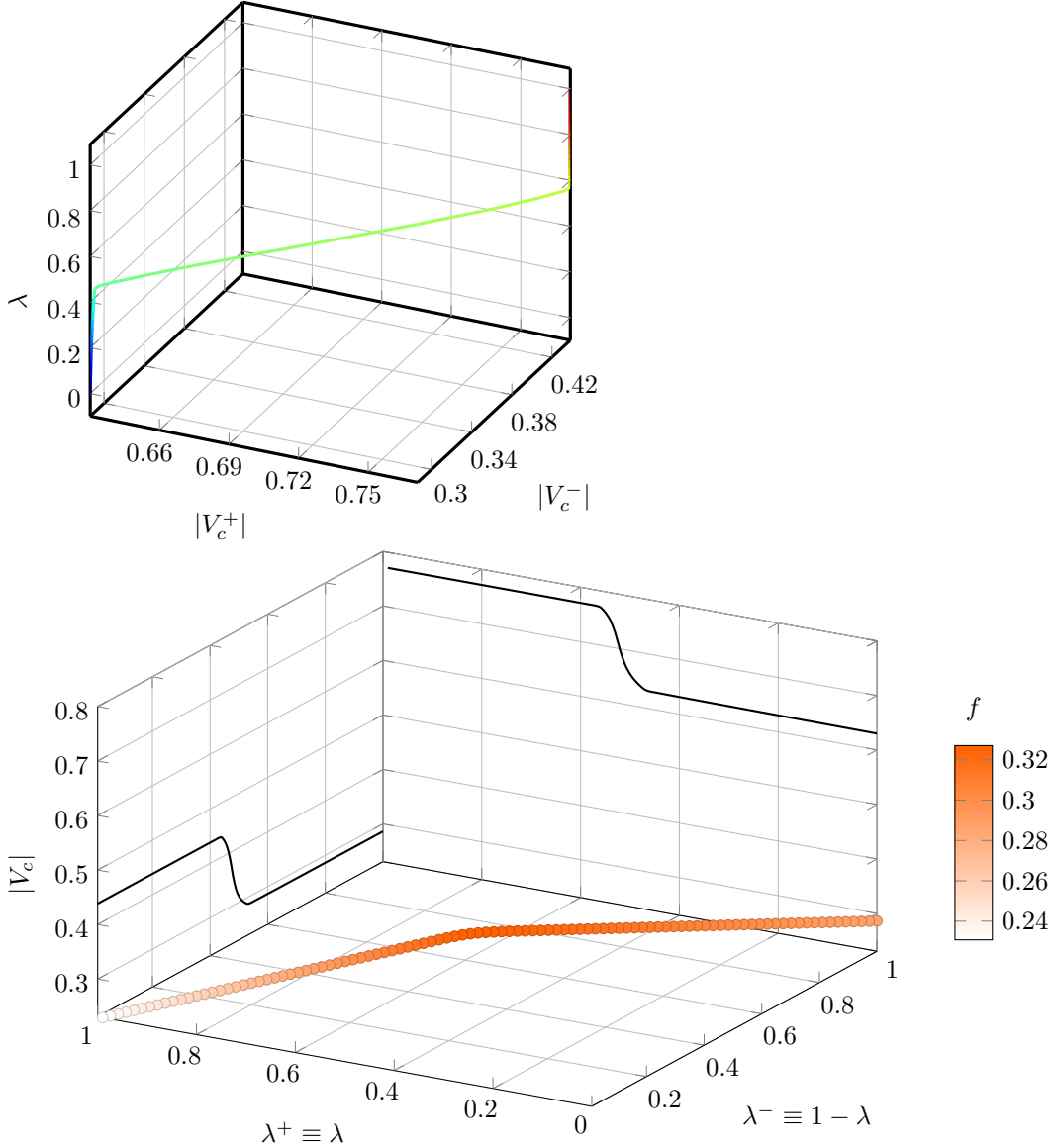
On the other hand, the point corresponding to the solution of the optimization problem illustrates what was already more or less deduced from the brute force computations. Nonetheless, solving the

optimization problem yields a more favorable result. The objective function is slightly smaller and the optimal point can be located in a zone where not many points coming from the brute force are present. Such irregular distribution of points is due to the fact that around the optimal points many combinations of currents do not meet the constraints. In any case, injecting a negative imaginary positive sequence current causes a positive voltage drop so that the voltage we wish to improve can be far apart from the faulted one. Taking into account that in this case $X \gg R$, the real positive sequence current becomes considerably smaller than the imaginary part.

The brute force calculation in Figure 2 was computed considering that $\lambda^+ = 1$ and $\lambda^- = 1$ as well. However, the optimality can also be studied for various λ values. This generic parameter would fit in the objective function as

$$f(\underline{I}^+, \underline{I}^-) = \lambda(|\underline{V}_c^+(\underline{I}^+, \underline{I}^-)| - 1) + (1 - \lambda)(|\underline{V}_c^-(\underline{I}^+, \underline{I}^-)| - 0), \quad (4)$$

where $\lambda = [0, 1]$. Therefore, bigger values of λ would imply that we prioritize the positive sequence voltage while small values will tend to give more importance to the negative sequence voltage. Figure XX shows the voltage profiles for a sweep of λ values.



1.2 Line to ground fault

The line to ground fault has the particularity of presenting a distribution of dots similar for both real and imaginary currents for both sequences. As shown in Figure 3, the minimum in the real currents plot takes place around the zero. Despite that, in reality, the real positive sequence current becomes slightly larger than zero, whilst for the negative sequence it takes a small but not negligible negative value. Observing Figure ?? it becomes clear that the real part of the positive sequence currents ought to be greater than zero whereas the negative sequence current should take negative values; these combinations cause the maximization of the positive sequence voltage and the minimization of the negative sequence voltage.

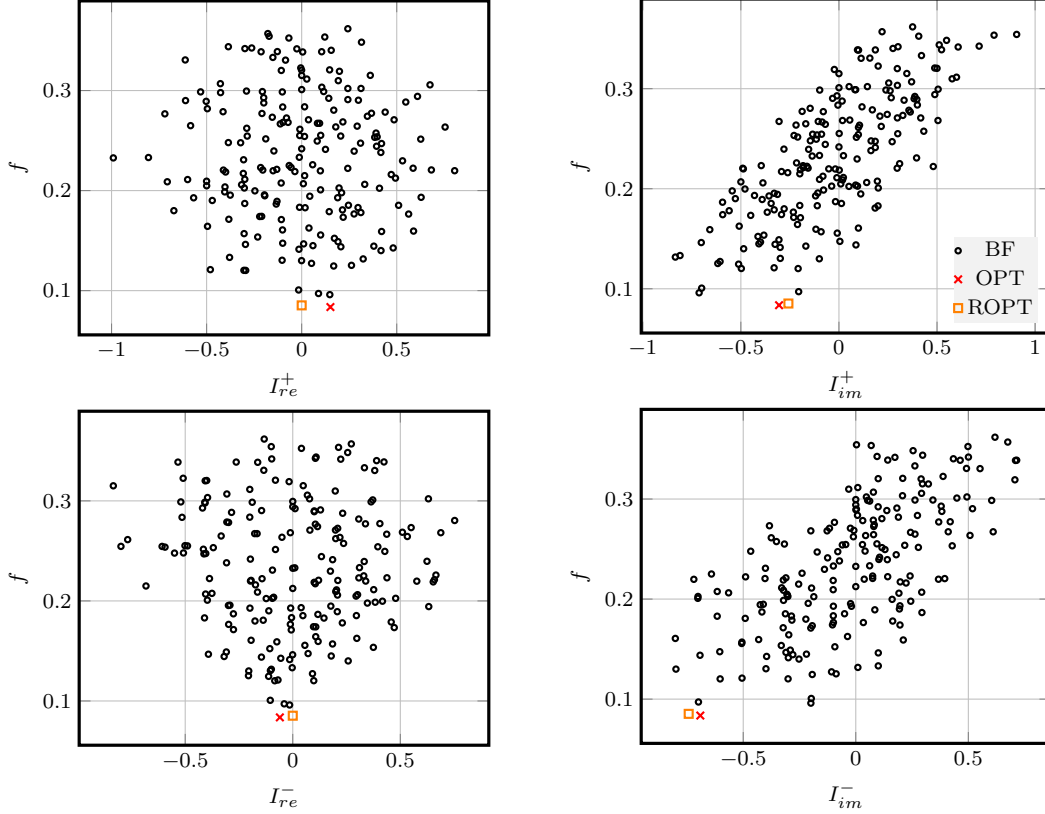


Figure 3: Influence of the currents on the objective function for the line to ground fault. BF: brute force, OPT: solution to the optimization problem, ROPT: solution to the optimization problem restricted to only injecting reactive power.

The imaginary part of the currents presents a similar distribution in both cases. Notice that contrarily to the balanced fault, where one vertex of the distribution coincided with the point at which f was at its minimum, there is a full edge in which the function becomes minimum. This phenomena suggests that maybe there is more than one minimum. For instance, carrying the analysis in the abc frame caused the optimal currents $[\underline{I}^+, \underline{I}^-]$ to become $[0.1715 - j0.4955, -0.0804 - j0.5003]$. Leaving aside the differences in current, the objective function became the same up to a precision of 10^{-10} . We have tested that this is indeed not an abnormality. Initializing differently the currents passed to the SciPy `minimize()` function results in variations in the imaginary currents across the zone where f becomes minimum. Besides, the variations between the OPT and the ROPT cases are almost imperceptible.

1.3 Line to line fault

The line to line fault can be considered to be a more severe fault compared to the line to ground case, since the function to minimize presents larger values. This can be understood when looking at the representations in Figures ?? and ??, where the fault impedance is between two phases and not phase and ground. Therefore, the voltage drop becomes larger. Figure 4 shows that even if the real part of the currents remains near the zero, visually speaking the imaginary parts take symmetrical values. The distribution of dots for the imaginary part of the negative sequence current is the main difference with respect to the line to ground fault.

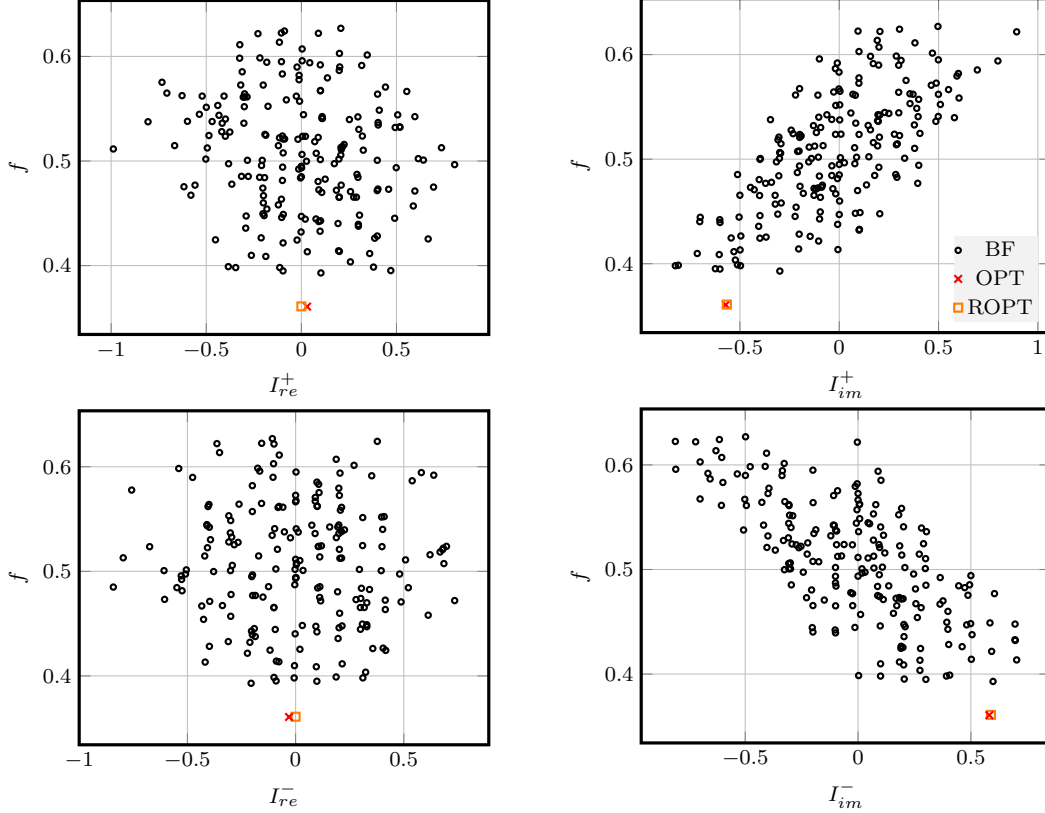


Figure 4: Influence of the currents on the objective function for the line to line fault. BF: brute force, OPT: solution to the optimization problem, ROPT: solution to the optimization problem restricted to only injecting reactive power.

However, this time it has been checked that there is only one point that minimizes the function, independently of the initialization vector. Not less surprising, the imaginary positive and negative sequence currents are practically the same with a change of sign. We can intuitively make sense of it as the positive sequence voltage at the PCC tends to one while the negative one approaches zero. Thus, the current flowing towards the \underline{Z}_f impedance is quite large and a big part of it is due to the injected positive sequence current. To achieve a small negative sequence voltage, as can be understood from Figure ??, the negative sequence current has to counteract the positive sequence current. Because of that, the optimal positive and negative sequence currents share almost the same magnitude and opposite signs.

1.4 Double line to ground fault

The double line to ground fault is likely to be the most severe in terms of voltage. It turns out to have the more distant voltages from the references, as shown in [taul2020modeling]. By nature, this fault is defined by a solid line to line connection and also a link between the two faulted phases and ground through an impedance. Figure 5 reveals that minimum of f falls considerably far from the ideal zero.

The double line to ground fault has many similarities with the line to line fault. First of all, the distribution of dots for the brute force computations is similar. Secondly, the optimal currents do not differ much from the ones obtained in Figure 4. All this could be deduced from its sequence representation. The same logic applies: the negative sequence current mirrors the positive sequence.

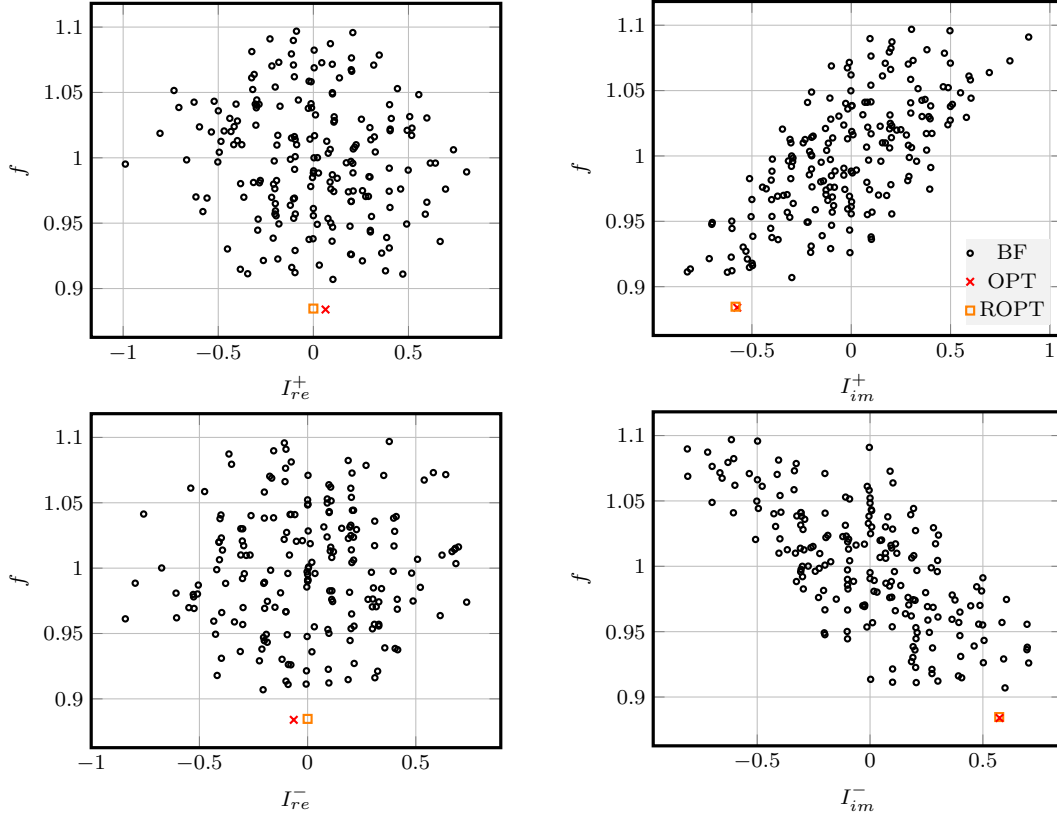


Figure 5: Influence of the currents on the objective function for the double line to ground fault. BF: brute force, OPT: solution to the optimization problem, ROPT: solution to the optimization problem restricted to only injecting reactive power.

Again, only injecting reactive currents (ROPF case) has been experimentally proved to become a suboptimal yet highly convenient choice. The objective functions present tiny variations in this case. Even though the real currents in both sequence differ a bit, for the imaginary ones the OPT and the ROPT computations generate almost the same value. Therefore, the injection of real currents becomes secondary for highly inductive impedances. Table 2 gathers the numerical results for all the studied faults.

Fault	f	$ V^+ $	$ V^- $	I_{re}^+	I_{im}^+	I_{re}^-	I_{im}^-
Balanced	0.200	0.800	0.000	0.107	-0.994	0.000	0.000
Line to ground	0.084	0.966	0.049	0.176	-0.535	-0.085	-0.461
Line to line	0.361	0.820	0.180	0.030	-0.571	-0.030	0.582
Double line to ground	0.884	0.525	0.408	0.064	-0.574	-0.064	0.574

Table 2: Main results for the balanced and the unbalanced faults