

Positive and negative sequence currents optimization to improve voltages during unbalanced faults

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Abstract—Grid faults constitute a series of unfortunate events that compromise power systems. With the increasing integration of renewables and their associated power electronics converters, the injected currents are controllable, but at the same time, they have to be limited so as not to damage the semiconductors. This poses the challenge to determine the combination of currents that improves the most the voltages at the point of common coupling. In this paper, such an issue is approached from an optimization perspective. Solving the optimization problem allows comparing its solutions with respect to the ones obtained by following the grid code control laws. Two fundamental scenarios are presented: one with a single converter, and another with two converters. Several parameters are varied for all kinds of faults to spot the changes on the currents, such as the severity of the fault, the distance of a hypothetical submarine cable, and the resistive/inductive ratio of the impedances. Overall the results indicate that injecting only reactive power is not always the preferable choice. While grid codes are not optimal, they can be regarded as near-optimal decision rules.

Index Terms—VSC, Grid-Support, Reference Optimization, Asymmetrical Fault, Current Saturation

I. INTRODUCTION

THE rise in renewable energies has carried along with it the inclusion of Voltage Source Converters (VSC) as a means of coupling energetic resources to the grid while providing controllability [1]–[3]. Adopting such power electronics equipment has induced a progressive shrinkage on synchronous generators' influence in power systems. The high flexibility of VSC control enables advanced grid-support control, which could enhance the system performance during the fault and ensure a fast recovery after the fault clearance. However, compared to electrical machines, VSCs cannot withstand overloads [4]. Indeed, current (and also voltage) limitations cause VSC to behave differently. They reach what is called a saturated state. Many equilibrium points may arise as a result of that, especially in grids formed by multiple converters operating in critical conditions. The solution to such systems is also likely to become an arduous task to compute, as saturation states are defined by non-linear equations, or in more detail, by piecewise functions.

Not only do currents have to be constrained to not exceed the limitations, but they also have to collaborate on improving the voltages [5]. This becomes visible when looking at the requirements imposed by Transmission System Operators (TSO) in its grid codes [6], [7]. Although this was not the case years ago, when wind power represented a small percentage of the electricity mix, nowadays wind power plants have to control active and reactive power [8]. Besides, they have to

transiently support the faults. The latter aspect is often referred to as low voltage ride through (LVRT) [9], [10]. The traditional approach to raise the voltage at the point of common coupling is to inject reactive power proportionally to the severity of the fault [8], [11]. During the analysis of faults, it is often the case that voltages are decomposed into positive, negative, and zero sequence values. By doing so, the study of the fault is expected to be simplified, and in addition to that, some intuition can be build from inspecting the positive and negative sequence voltages. A concerning unbalanced fault is such that substantially decreases the positive sequence voltage with respect to the nominal voltage while the negative sequence voltage increases. Both sequences have to be thoroughly controlled, as discussed in [11], [12].

Nevertheless, for the most part, grid codes only specify reactive power injection during faults [13], [14]. This is because transmission networks are often considered to have an inductive characteristic. The influence of the grid impedance characteristics of the system optimized under an equilibrium state is covered in [12], where the authors express the currents to inject as a function of the voltage and the impedances. A recursive relation between the voltage and the current is found, which invalidates the possibility of working with a closed-form expression from where to compute the optimal currents solution. Expressions of the same nature are proposed in [15], where instead of attempting to solve the optimization problem, a control parameter is introduced. This takes various values, but no analysis is carried out to determine the optimal choice. The effect of varying this control parameter is studied in [16], although it is not computed with a systematic approach, but rather, manually. Reference [17] proposes a maximum allowed support (MAS) control scheme that is said to provide the maximum voltage support and simultaneously satisfying the current limitations. The study does not explicitly indicate how the current is distributed among the real and the imaginary positive and negative sequence components, and variations in input parameters are rather limited. Another voltage support scheme is presented in [18], where the injected currents depend neither on the active power nor the filter resistance. In addition, positive and negative sequence grid voltage values are imposed, which facilitates the obtention of the steady-state current values. A variation of the grid code requirements is depicted in [19]. The authors found it to provide better results than conventional grid codes. The majority of the grid support strategies described above are compared and summarized in [20]. It is worth mentioning that the systems under study considered in these references include a single VSC. Further conclusions are expected to be extracted from examining a two-converter case study.

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Fig. 1: Average model of a VSC connected to the grid

This paper proposes a methodology to identify the optimized system equilibrium point during the fault considering converters' current limitations. Two other control rules are implemented. One focuses on solving the optimization problem but in addition to the other constraints, it is restricted to only injecting reactive power. The third control rule follows the implementation of grid code specifications with its characteristic droop profile. These three options are tested for both balanced and unbalanced faults, where this last category includes the line to ground, the line to line, and the double line to ground faults. First, a basic system with a single converter is studied. Then, the analysis is repeated for a system with two converters in order to identify their interaction in saturated states.

Two main contributions come from this paper. On the one hand, it indicates the preferable injected currents under diverse conditions. A deeper understanding of the optimality of the solutions is expected to be gained from it. On the other hand, comparisons between the optimal solution and the one obtained by following the grid code are presented. An assessment about the convenience of grid codes to support faults can be derived, which should be useful when proposing future modifications in order to evolve towards more resilient grids.

II. FORMULATION

A. System modeling

VSCs are elements that interconnect DC systems with AC systems. As shown in Figure 1, they can be modeled following the so-called average model, where the switchings of the semiconductors are excluded. The VSC has been assumed to be connected to the AC grid with a filter in between denoted by Z_z . The control strategy of the VSC consists of adjusting the voltages appropriately so that currents can indirectly meet the references [21].

Power systems are likely to involve more than a single converter. Therefore, the modeling is approached from a generalized perspective. Figure 2 presents the modeling for a system with n converters connected to a grid with n_g buses represented by its admittance matrix \underline{Y}_g . In addition, the grid has been modeled with a Norton equivalent instead of a Thevenin equivalent for simplification purposes. The VSCs are treated as current sources that inject currents of the form of $I_k \forall k \in [1, \dots, n]$. Additional impedances denoted by Z_{vk} connect the point of common coupling to the grid. Such point

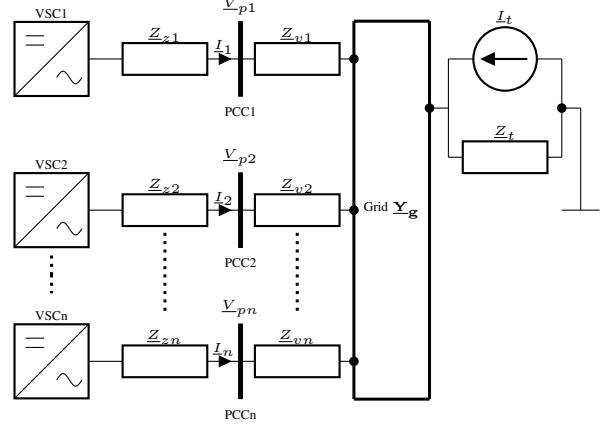


Fig. 2: Single-phase representation of a complete system

of common coupling is precisely where the voltages ought to be improved.

The relationship between currents and voltages can be established with an analysis in the natural reference frame or by employing symmetrical components as in [22]. Although both approaches are equally valid, working in the natural reference frame allows studying the system with greater flexibility, as only the particular elements of the admittance matrices that depend on the fault admittance experience changes. No other modifications in the topology have to be considered. Consequently, voltages and currents are related by

$$\begin{pmatrix} I_1 \\ I_2 \\ \vdots \\ I_n \\ I_t \end{pmatrix} = \begin{pmatrix} \underline{Y}_{v1} & 0 & \dots & 0 & -\underline{Y}'_{v1} \\ 0 & \underline{Y}_{v2} & \dots & 0 & -\underline{Y}'_{v2} \\ \vdots & \vdots & \ddots & \vdots & \vdots \\ 0 & 0 & \dots & \underline{Y}_{vn} & -\underline{Y}'_{vn} \\ -\underline{Y}'_{v1} & -\underline{Y}'_{v2} & \dots & -\underline{Y}'_{vn} & \underline{Y}_g \end{pmatrix} \begin{pmatrix} V_{p1} \\ V_{p2} \\ \vdots \\ V_{pn} \\ V_g \end{pmatrix}. \quad (1)$$

For a given converter k , its injected currents and its associated voltages at the point of common coupling are further developed as

$$\begin{cases} \underline{I}_k = [I_k^{(a)}, I_k^{(b)}, I_k^{(c)}]^T, \\ \underline{V}_{pk} = [V_{pk}^{(a)}, V_{pk}^{(b)}, V_{pk}^{(c)}]^T, \end{cases} \quad (2)$$

i.e., they contain the a , b and c phase values. Voltages and currents are related via admittance matrices of the form \underline{Y}_{vk} , which in normal operating conditions follow

$$\underline{Y}_{vk} = \begin{pmatrix} \frac{1}{Z_{vk}} & 0 & 0 \\ 0 & \frac{1}{Z_{vk}} & 0 \\ 0 & 0 & \frac{1}{Z_{vk}} \end{pmatrix}. \quad (3)$$

In case a fault occurs at the buses interconnected by Z_{vk} , elements $1/Z_f$ are added by observation, where Z_f denotes the fault impedance.

On the contrary, the admittance matrix \underline{Y}_g is a $3n_g \times 3n_g$ matrix also built by observation.

Voltages are expressed as a transformation of currents by computing the inverse of the general admittance matrix.

In order to have a clearer comprehension of the voltages, they are converted into symmetrical components by means of Fortescue's transformation [23]:

$$\begin{pmatrix} \underline{V}_{pk}^0 \\ \underline{V}_{pk}^+ \\ \underline{V}_{pk}^- \end{pmatrix} = \frac{1}{3} \begin{pmatrix} 1 & 1 & 1 \\ 1 & a & a^2 \\ 1 & a^2 & a \end{pmatrix} \begin{pmatrix} \underline{V}_{pk}^a \\ \underline{V}_{pk}^b \\ \underline{V}_{pk}^c \end{pmatrix}, \quad (4)$$

where $a = e^{j\frac{2\pi}{3}}$.

B. Optimization problem

Positive sequence voltages have to be maximized while negative sequence voltages have to be minimized. The zero sequence component of the voltages is a magnitude likely to become non-null under asymmetrical faults. Nevertheless, as VSC are unable to inject zero sequence currents, it will remain an uncontrolled variable in the sense that no efforts will be made towards reducing it. Current saturation restrictions imposed by the VSC characteristics have to be respected. This applies to each phase of each converter. Therefore, the generic optimization problem is

$$\begin{aligned} \min_{\underline{I}} \quad & \sum_{k=1}^n \left[\lambda_k^+ |(1 - |\underline{V}_{pk}^+(\underline{I})|)| + \lambda_k^- |(0 - |\underline{V}_{pk}^-(\underline{I})|)| \right], \\ \text{s.t.} \quad & \max(\underline{I}_k^a, \underline{I}_k^b, \underline{I}_k^c) \leq I_{\max,k} \quad \forall k \in [1, \dots, n], \\ & \underline{I}_k^a + \underline{I}_k^b + \underline{I}_k^c = 0 \quad \forall k \in [1, \dots, n], \end{aligned} \quad (5)$$

where the positive and negative sequence components of voltages \underline{V}_{pk} are symbolically expressed as functions of the currents, and $I_{\max,k}$ is the maximum allowed current by the k converter. It has been assumed that voltages at each phase of the converter do not surpass the limitations, which seems a fair assumption considering that voltages decreases substantially during faults. Ignoring voltage limitations tends to be a commonality in the literature as well.

The results gathered in this paper are computed with Python 3.9.1 and the aid of the Mystic package, a highly-constrained non-convex optimization framework [24], [25]. A differential global optimization solver has been employed with a relative precision up to $1e - 6$.

C. Grid code rules

III. SINGLE CONVERTER CASE STUDY

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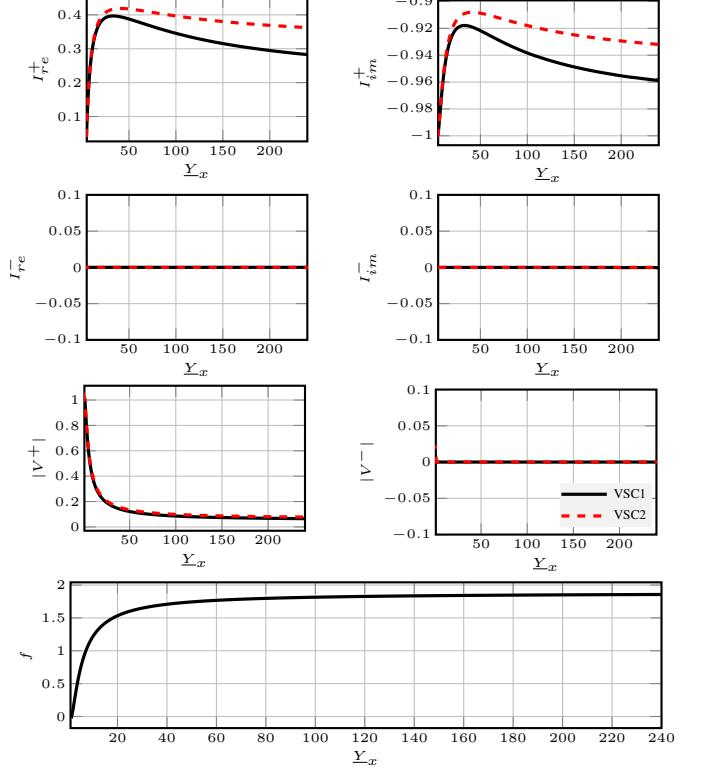


Fig. 3: Influence of the currents on the objective function for the balanced fault with a varying fault admittance

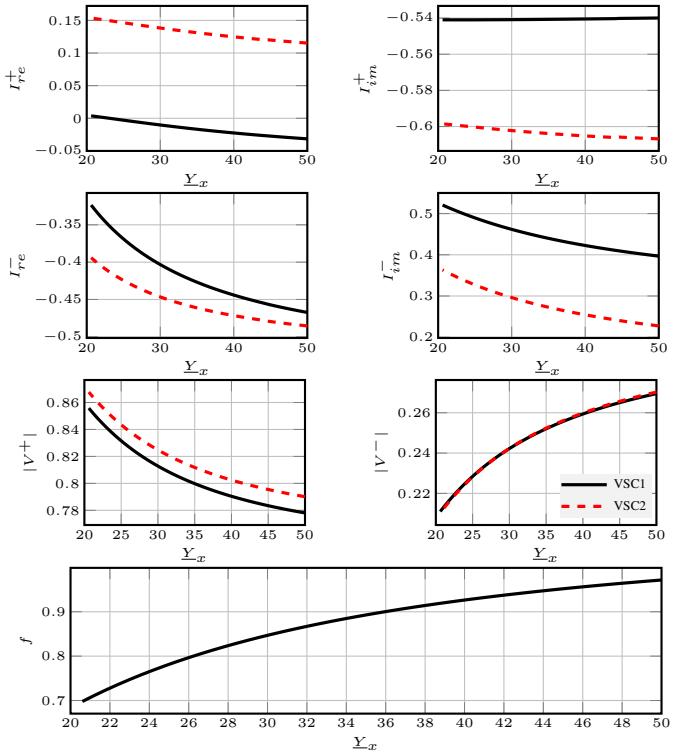


Fig. 4: Influence of the currents on the objective function for the line to ground with a varying fault admittance

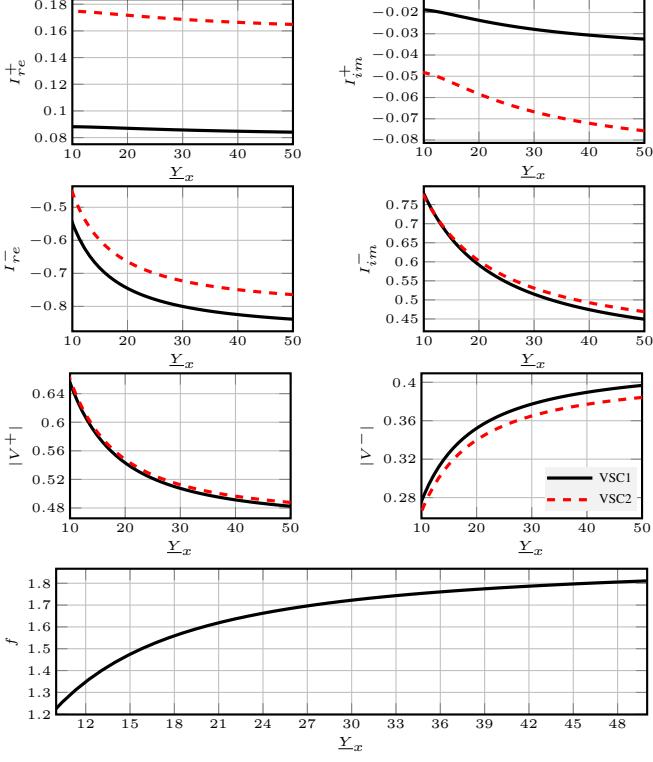


Fig. 5: Influence of the currents on the objective function for the line to line with a varying fault admittance

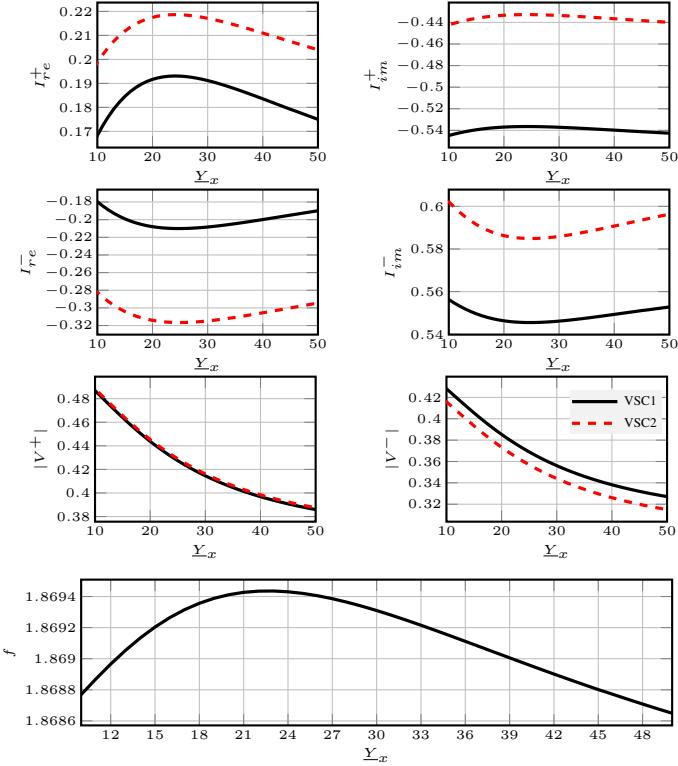


Fig. 6: Influence of the currents on the objective function for the double line to ground with a varying fault admittance

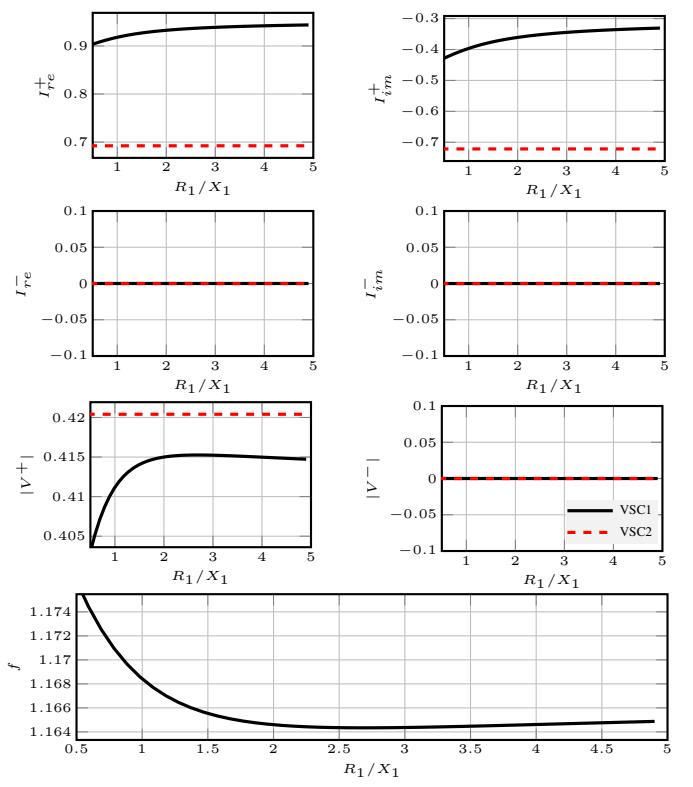


Fig. 7: Influence of the currents on the objective function for the balanced fault with a varying R_1/X_1 ratio and $Y_x = 10$

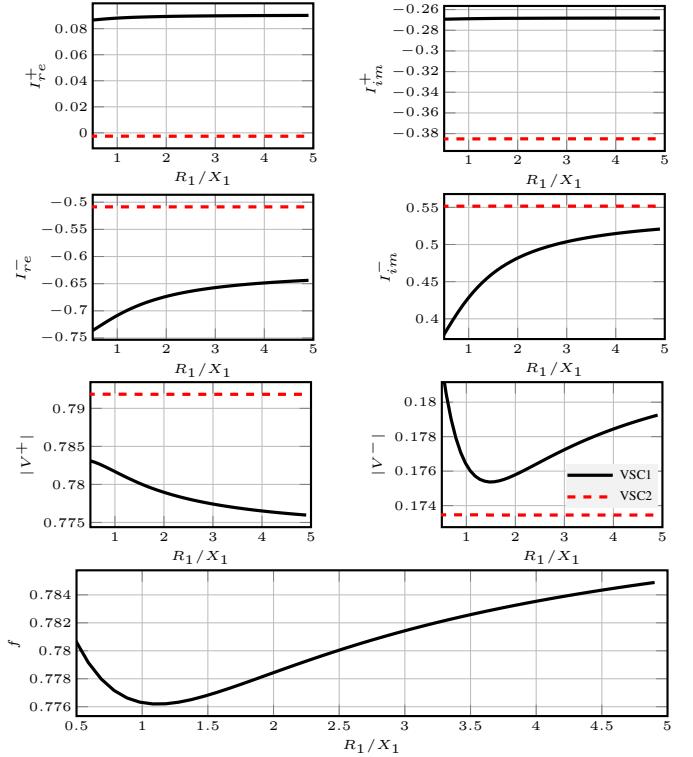


Fig. 8: Influence of the currents on the objective function for the line to ground fault with a varying R_1/X_1 ratio and $Y_{ag} = 25$

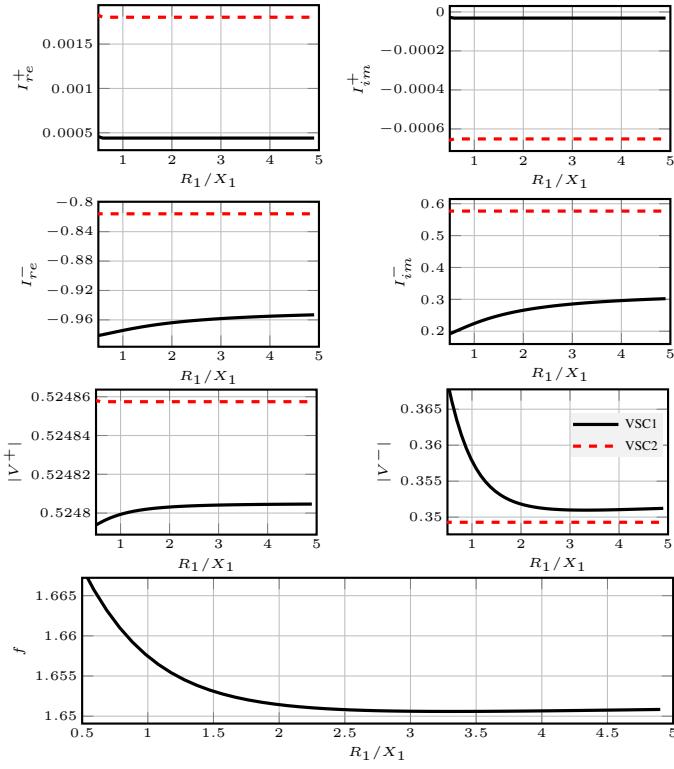


Fig. 9: Influence of the currents on the objective function for the line to line fault with a varying R_1/X_1 ratio and $\underline{Y}_{ab} = 25$

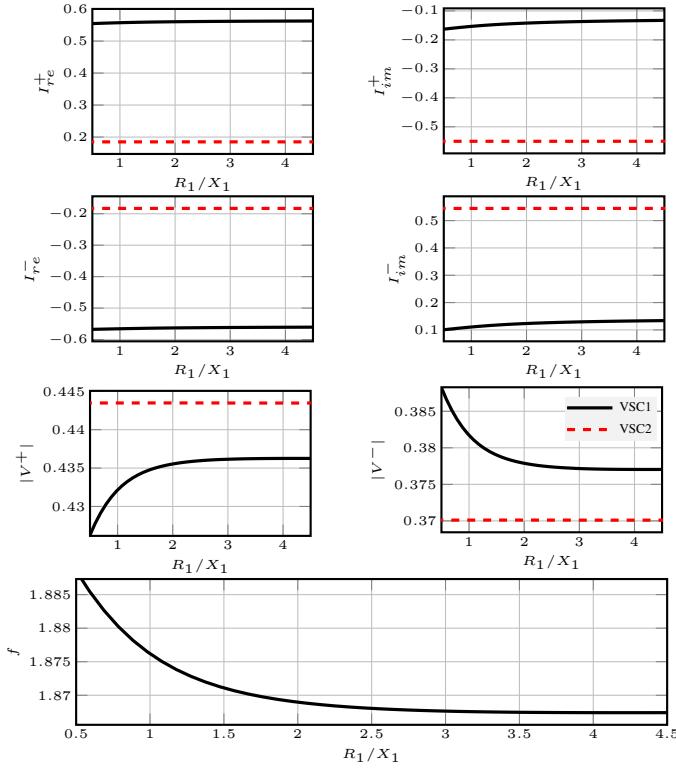


Fig. 10: Influence of the currents on the objective function for the double line to ground fault with a varying R_1/X_1 ratio and $\underline{Y}_{ag} = 25$

IV. TWO CONVERTER CASE STUDY

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V. CONCLUSION

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ACKNOWLEDGMENT

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REFERENCES

- [1] M. Imhof and G. Andersson, "Power system stability control using voltage source converter based hvdc in power systems with a high penetration of renewables," in *2014 Power Systems Computation Conference*. IEEE, 2014, pp. 1–7.
- [2] S. Eren, A. Bakhshai, and P. Jain, "Control of three-phase voltage source inverter for renewable energy applications," in *2011 IEEE 33rd International Telecommunications Energy Conference (INTELEC)*. IEEE, 2011, pp. 1–4.
- [3] F. Blaabjerg, Y. Yang, K. Ma, and X. Wang, "Power electronics—the key technology for renewable energy system integration," in *2015 International Conference on Renewable Energy Research and Applications (ICRERA)*. IEEE, 2015, pp. 1618–1626.
- [4] A. Abdou, A. Abu-Siada, and H. Pota, "Improving the low voltage ride through of doubly fed induction generator during intermittent voltage source converter faults," *Journal of Renewable and Sustainable Energy*, vol. 5, no. 4, p. 043110, 2013.
- [5] J. Morren, *Grid support by power electronic converters of Distributed Generation units*, 2006.
- [6] I. Erlich, W. Winter, and A. Dittrich, "Advanced grid requirements for the integration of wind turbines into the german transmission system," in *2006 IEEE Power Engineering Society General Meeting*. IEEE, 2006, pp. 7–pp.
- [7] X. Zhang, Z. Wu, M. Hu, X. Li, and G. Lv, "Coordinated control strategies of vsc-hvdc-based wind power systems for low voltage ride through," *energies*, vol. 8, no. 7, pp. 7224–7242, 2015.
- [8] M. Mohseni and S. M. Islam, "Review of international grid codes for wind power integration: Diversity, technology and a case for global standard," *Renewable and Sustainable Energy Reviews*, vol. 16, no. 6, pp. 3876–3890, 2012.
- [9] M. Tsili and S. Papathanassiou, "A review of grid code technical requirements for wind farms," *IET Renewable power generation*, vol. 3, no. 3, pp. 308–332, 2009.
- [10] J. Conroy and R. Watson, "Low-voltage ride-through of a full converter wind turbine with permanent magnet generator," *IET Renewable power generation*, vol. 1, no. 3, pp. 182–189, 2007.
- [11] A. Haddadi, I. Kocar, J. Mahseredjian, U. Karaagac, and E. Farantatos, "Negative sequence quantities-based protection under inverter-based resources challenges and impact of the german grid code," *Electric Power Systems Research*, vol. 188, p. 106573, 2020.
- [12] A. Camacho, M. Castilla, J. Miret, L. G. de Vicuña, and R. Guzman, "Positive and negative sequence control strategies to maximize the voltage support in resistive-inductive grids during grid faults," *IEEE Transactions on Power Electronics*, vol. 33, no. 6, pp. 5362–5373, 2017.
- [13] R. E. de España. (2016) Información sobre implementación de códigos de red de conexión. textos de los códigos de red de conexión europeos. reglamento 2016/631. [Online]. Available: <https://www.esios.ree.es/es/pagina/codigos-red-conexion>
- [14] A. G. Paspatis and G. C. Konstantopoulos, "Voltage support under grid faults with inherent current limitation for three-phase droop-controlled inverters," *Energies*, vol. 12, no. 6, p. 997, 2019.
- [15] A. Camacho, M. Castilla, J. Miret, J. C. Vasquez, and E. Alarcon-Gallo, "Flexible voltage support control for three-phase distributed generation inverters under grid fault," *IEEE transactions on industrial electronics*, vol. 60, no. 4, pp. 1429–1441, 2012.
- [16] X. Guo, X. Zhang, B. Wang, W. Wu, and J. M. Guerrero, "Asymmetrical grid fault ride-through strategy of three-phase grid-connected inverter considering network impedance impact in low-voltage grid," *IEEE Transactions on Power Electronics*, vol. 29, no. 3, pp. 1064–1068, 2013.
- [17] M. M. Shabestary and Y. A.-R. I. Mohamed, "An analytical method to obtain maximum allowable grid support by using grid-connected converters," *IEEE Transactions on Sustainable Energy*, vol. 7, no. 4, pp. 1558–1571, 2016.
- [18] Z. Dai, H. Lin, H. Yin, and Y. Qiu, "A novel method for voltage support control under unbalanced grid faults and grid harmonic voltage disturbances," *IET power Electronics*, vol. 8, no. 8, pp. 1377–1385, 2015.
- [19] M. M. Shabestary and Y. A.-R. I. Mohamed, "Asymmetrical ride-through and grid support in converter-interfaced dg units under unbalanced conditions," *IEEE Transactions on Industrial Electronics*, vol. 66, no. 2, pp. 1130–1141, 2018.
- [20] M. M. Shabestary, S. Mortazavian, and Y. I. Mohamed, "Overview of voltage support strategies in grid-connected vscs under unbalanced grid faults considering lvrt and hvrt requirements," in *2018 IEEE International Conference on Smart Energy Grid Engineering (SEGE)*. IEEE, 2018, pp. 145–149.
- [21] M. T. Andani, H. Pourgharibshahi, Z. Ramezani, and H. Zargazadeh, "Controller design for voltage-source converter using lqg/ltr," in *2018 IEEE Texas power and energy conference (TPEC)*. IEEE, 2018, pp. 1–6.
- [22] M. G. Taul, S. Golestan, X. Wang, P. Davari, and F. Blaabjerg, "Modeling of converter synchronization stability under grid faults: The general case," *IEEE Journal of Emerging and Selected Topics in Power Electronics*, 2020.
- [23] C. L. Fortescue, "Method of symmetrical co-ordinates applied to the solution of polyphase networks," *Transactions of the American Institute of Electrical Engineers*, vol. 37, no. 2, pp. 1027–1140, 1918.
- [24] M. M. McKerns, L. Strand, T. Sullivan, A. Fang, and M. A. Aivazis, "Building a framework for predictive science," *arXiv preprint arXiv:1202.1056*, 2012.
- [25] M. McKerns, P. Hung, and M. Aivazis, "mystic: highly-constrained non-convex optimization and uq," 2009.