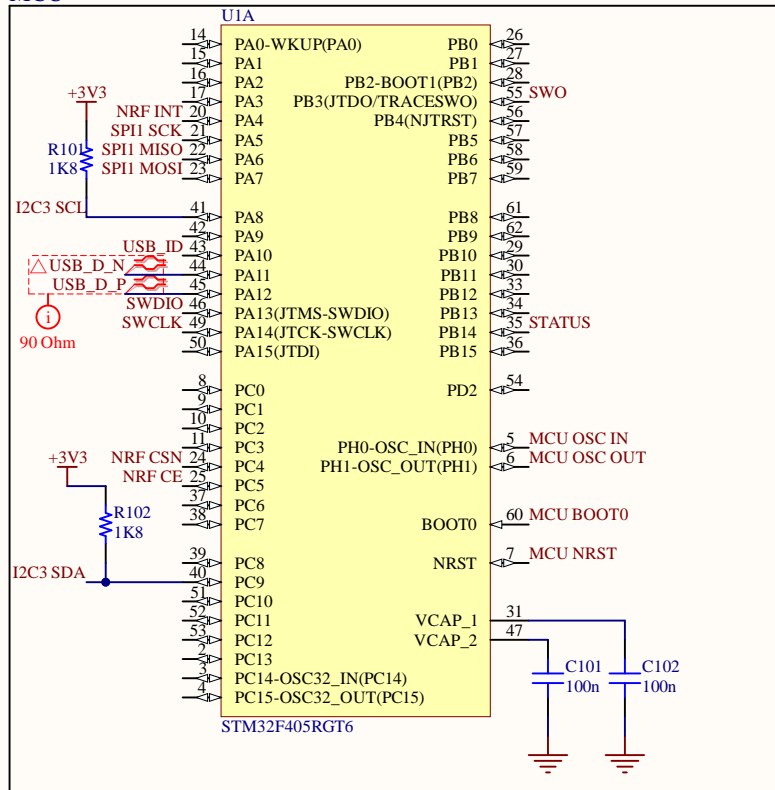
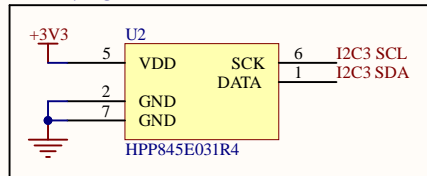


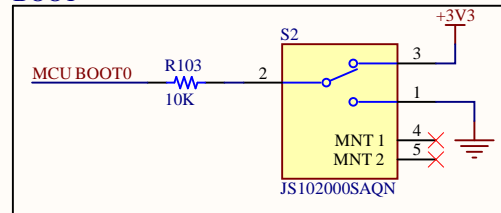
## MCU



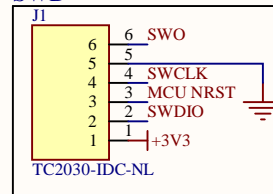
## TEMP/HUMID



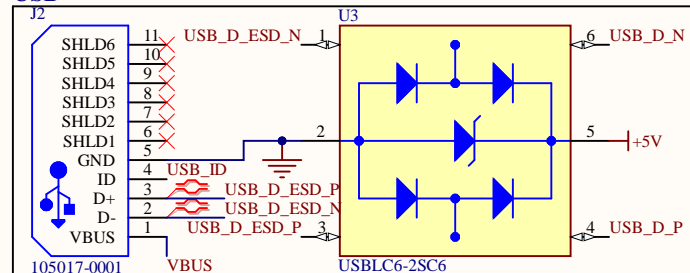
## BOOT



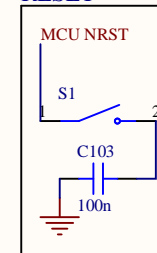
## SWD



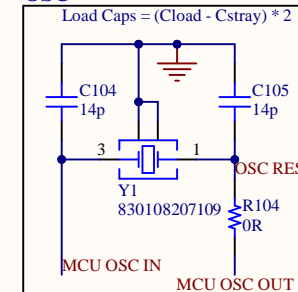
## USB



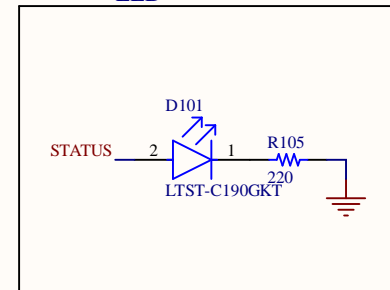
## RESET



## OSC

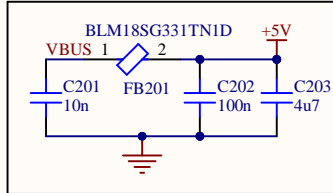


## LED

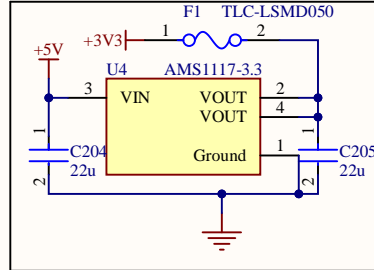


Title MCU + SENSORS			
Size A4	Number		Revision
Date:	3/07/2024	Sheet of	
File:	C:\Users\...\MCU + SENSORS.SchDoc	Drawn By:	

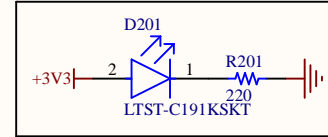
### VBUS FILTER



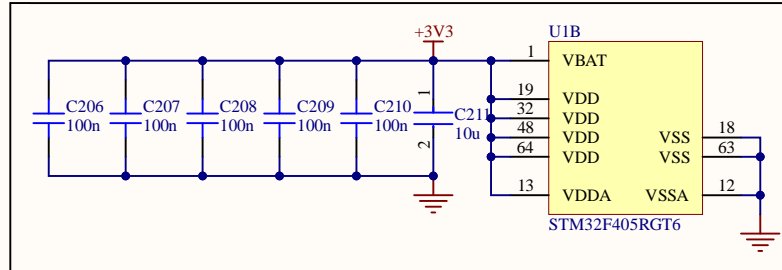
### 3V3 REG



### +3V3 LED



### MCU POWER



Title <b>POWER</b>		
Size A4	Number	Revision
Date:	3/07/2024	Sheet of
File:	C:\Users\...\POWER.SchDoc	Drawn By:

Pin connection diagram for the NRF24L01P module (U5). The module is connected to the following pins:

- Left side:**
  - SPI1 SCK (3) to SCK
  - SPI1 MOSI (4) to MOSI
  - SPI1 MISO (5) to MISO
  - NRF CSN (2) to CSN
  - NRF CE (1) to CE
  - NRF IREF (16) to IREF
  - +3V3 (15) to VDD
  - NRF PA (18) to VDD
  - NRF DVDD (19) to DVDD
- Right side:**
  - $\overline{\text{IRQ}}$  (6) to NRF INT
  - ANT1 (12) to NRF ANT1
  - ANT2 (13) to NRF ANT2
  - XC1 (10) to NRF XC1
  - XC2 (9) to NRF XC2
  - VSS (8, 14, 17, 20) to ground
- Bottom:**
  - C301 NRF24L01P-T (33nF) connected between DVDD and ground.

Load Caps = (Cload - Cstray) \* 2

C302 14p

C303 14p

Y2 830108207109

R301 0R

NRF XC1

NRF XC2

Title <b>RF</b>		
Size A4	Number	Revision
Date:	3/07/2024	Sheet of
File:	C:\Users\...\RF.SchDoc	Drawn By:

