


WiPy pinout and alternate functions table

F	E	D	C	B	A	PIN	WiPy	PIN	A	B	C	D	E	F
		ADC0_CH0		UART1_RX[6]	UART0_RX[3]	RESET		VIN (3.6-5.5V)						
				UART1_TX[6]	UART0_TX[3]	GP2		GND						
				I2C0_SCL[9]	UART1_TX[2]	GP23		3V3 OUT						
				I2C0_SDA[9]	UART1_RX[2]	GP24		GP10		UART1_TX[7]		PWM_7[3]	SD0_CLK[6]	I2C0_SCL[1]
		PWM_1[5]		I2C0_SDA[1]	UART1_RX[7]	GP11		GP9				PWM_6[3]	SD0_DATA0[6]	I2S0_DATA0[7]
I2S0_FS[13]	SD0_CMD[6]	PWM_8[3]		I2C0_SCL[5]	UART0_TX[7]	GP12		GP8						I2S0_FS[7]
I2S0_CLK[3]				I2C0_SDA[5]	UART0_RX[7]	GP13		GP7	UART0_RTS[10]	UART1_RTS[3]	UART1_TX[11]			I2S0_CLK[13]
				I2C0_SCL[5]		GP14		GP6	UART0_CTS[6]	UART1_CTS[3]				
SPI0_CLK[7]				I2C0_SDA[5]		GP15		GP30	UART0_TX[9]			SPI0_MISO[7]	I2S0_FS[3]	I2S0_CLK[2]
SPI0_MISO[7]	SD0_DATA0[8]					GP16		GP31	UART0_RX[9]	UART1_RX[2]		SPI0_CLK[7]	I2S0_FS[12]	I2S0_DATA0[6]
SPI0_MOSI[7]	SD0_CLK[8]				UART1_TX[5]	GP17		GP3		UART1_TX[6]				ADC0_CH1
SPI0_CS[7]	SD0_CMD[8]				UART1_RX[5]	GP17		GP0	UART0_RTS[3]	UART1_RTS[10]		SPI0_CS[9]	I2S0_DATA0[4]	I2S0_DATA1[6]
I2S0_FS[7]						GP22		GP4		UART1_RX[6]				ADC0_CH2
					SAFE_BOOT	GP28		GP5					I2S0_DATA1[6]	ADC0_CH3
							Antenna							

Timer	Channel	PWM pin
0	A	PWM_1
	B	
1	A	PWM_3
	B	
2	A	
	B	PWM_6
3	A	PWM_7
	B	PWM_8

- Remarks:
- The number in brackets next to each function is the one to be used when remapping the pin. In order to use the pin in GPIO mode, alternate function 0 must be selected
  - ADC pin input range is 0-1.4V (being 1.8V the absolute maximum that it can withstand). When GP2, GP3, GP4 or GP5 are remapped to the ADC block, 1.8 V is the maximum. If these pins are used in digital mode, then the maximum allowed input is 3.6V.
  - The heart beat LED is connected to GP25 and also has PWM\_3 functionality with the alternate function 9.