WiPy pinout and alternate functions table

F	E	D	C	B	A	PIN	WiPy	PIN	A	B	C	D	E	F
						RESET		VIN (3.6-5.5V)						
		ADC_CH1	TIM_CC2[7]	UART1_RX[6]	UART0_RX[3]	GP2		GND						
			TIM_CC1[7]	UART1_TX[6]	UART0_TX[3]	GP1		3V3 OUT						
				I2C_SCL[9]	UART1_TX[2]	GP23		GP10		UART1_TX[7]	TIM_CC1[12]	PWM_CH6[3]	SD_CLK[6]	I2C_SCL[1]
		PWM_CH0[5]	TIM_CC6[4]	I2C_SDA[9]	UART1_RX[2]	GP24		GP9			TIM_CC0[12]	PWM_CH5[3]	SD_DATA0[6]	I2S_DATA0[7]
I2S_FS[13]	SD_CMD[6]	PWM_CH7[3]	TIM_CC2[12]	I2C_SDA[1]	UART1_RX[7]	GP11		GP8			TIM_CC6[12]			I2S_FS[7]
I2S_CLK[3]			TIM_CC3[12]	I2C_SCL[5]	UART0_TX[7]	GP12		GP7	UART0_RTS[10]	UART1_RTS[3]	UART0_TX[11]			I2S_CLK[13]
			TIM_CC4[12]	I2C_SDA[5]	UART0_RX[7]	GP13		GP6	UART0_CTS[6]	UART1_CTS[3]	TIM_CC6[7]			
SPI_CLK[7]			TIM_CC5[12]	I2C_SCL[5]		GP14		GP30	UART0_TX[9]		TIM_CC5[4]	SPI_MISO[7]	I2S_FS[3]	I2S_CLK[2]
SPI_MISO[7]	SD_DATA0[8]		TIM_CC6[13]	I2C_SDA[5]		GP15		GP31	UART0_RX[9]	UART1_RX[2]		SPI_CLK[7]	I2S_FS[12]	I2S_DAT0[6]
SPI_MOSI[7]	SD_CLK[8]		TIM_CC7[13]		UART1_TX[5]	GP16		GP3		UART1_TX[6]				ADC_CH2
SPI_CS[7]	SD_CMD[8]				UART1_RX[5]	GP17		GP0	UART0_RTS[3]	UART1_RTS[10]	TIM_CC0[7]	SPI_CS[9]	I2S_DATA0[4]	I2S_DATA1[6]
I2S_FS[7]			TIM_CC4[5]			GP22		GP4		UART1_RX[6]				ADC_CH3
					SAFE_BOOT	GP28		GP5			TIM_CC5[7]		I2S_DATA1[6]	ADC_CH4
							Antenna							

Timer	Channel	CC pin	PWM pin
1	A	TIM_CC0	PWM_CH0
	B	TIM_CC1	
2	A	TIM_CC2	PWM_CH2
	B	TIM_CC3	
3	A	TIM_CC4	
	B	TIM_CC5	PWM_CH5
4	A	TIM_CC6	PWM_CH6
	B	TIM_CC7	PWM_CH7

Remarks:

- *The number in brackets next to each function is the one to be used when remapping the pin. In order to use the pin in GPIO mode, alternate function 0 must be selected*
- *ADC pin input range is 0-1.4V (being 1.8V the absolute maximum that it can withstand). When GP2, GP3, GP4 or GP5 are remapped to the ADC block, 1.8 V is the maximum. If these pins are used in digital mode, then the maximum allowed input is 3.6V.*
- *The heart beat LED is connected to GP25 and also has PWM_CH2 functionality with the alternate function 9.*