
Warning: Imaginary parts of complex X and/or Y arguments ignored

2.(a) Represents a low pass circuit with amplifier. During DC conditions, the capacitor will block current, however the resistors will let it pass freely. The inductor will also let it pass freely. However, once the current begins to alternate, the inductor will begin to resist the current and the capacitor will begin to conduct. This will cause parallel resistance and further the resistance of the circuit

2.(b) I would expect a pass band during the low frequencies and a 2nd order drop off at its respective frequency

C =

0	0	0	0	0	0	0
-0.2500	0.2500	0	0	0	0	0
0	0	-0.2000	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0

G =

1.0000	0	0	0	0	0	0
-1.0000	1.5000	-1.0000	0	0	0	0
0	1.0000	0	-1.0000	0	0	0
0	0	-1.0000	0.1000	0	0	0
0	0	0	0	-100.0000	1.0000	0
0	0	0	0.1000	-1.0000	0	0
0	0	0	0	0	-10.0000	10.0010

changing the time step to be larger, makes a more inaccurate model

C =

Columns 1 through 7

0	0	0	0	0	0	0
-0.2500	0.2500	0	0	0	0	0
0	0	-0.2000	0	0	0	0
0	0	0	0.0000	0	0	0
0	0	0	0	0	0	0
0	0	0	0.0000	0	0	0
0	0	0	0	0	0	0
0	0	0	0	0	0	0

Column 8

0
0

```

0
0
0
0
0
0
0

```

G =

Columns 1 through 7

```

1.0000      0      0      0      0      0      0
-1.0000  1.5000 -1.0000      0      0      0      0
      0  1.0000      0 -1.0000      0      0      0
      0      0 -1.0000  0.1000      0      0 -1.0000
      0      0      0      0 -100.0000  1.0000      0
      0      0      0  0.1000 -1.0000      0 -1.0000
      0      0      0      0      0      0  1.0000
      0      0      0      0      0 -10.0000      0

```

Column 8

```

0
0
0
0
0
0
0
0
0

```

10.0010

3.(c.6) Raising the Cn of the circuit tends to elongate the bandwidth
 3.(c.7) changing the timesteps with the In and Cn added,
 causes the peak positions to change, and the noise to become more or
 less

significant. Raising the time steps reduces noise, and lowering it
 increases it.

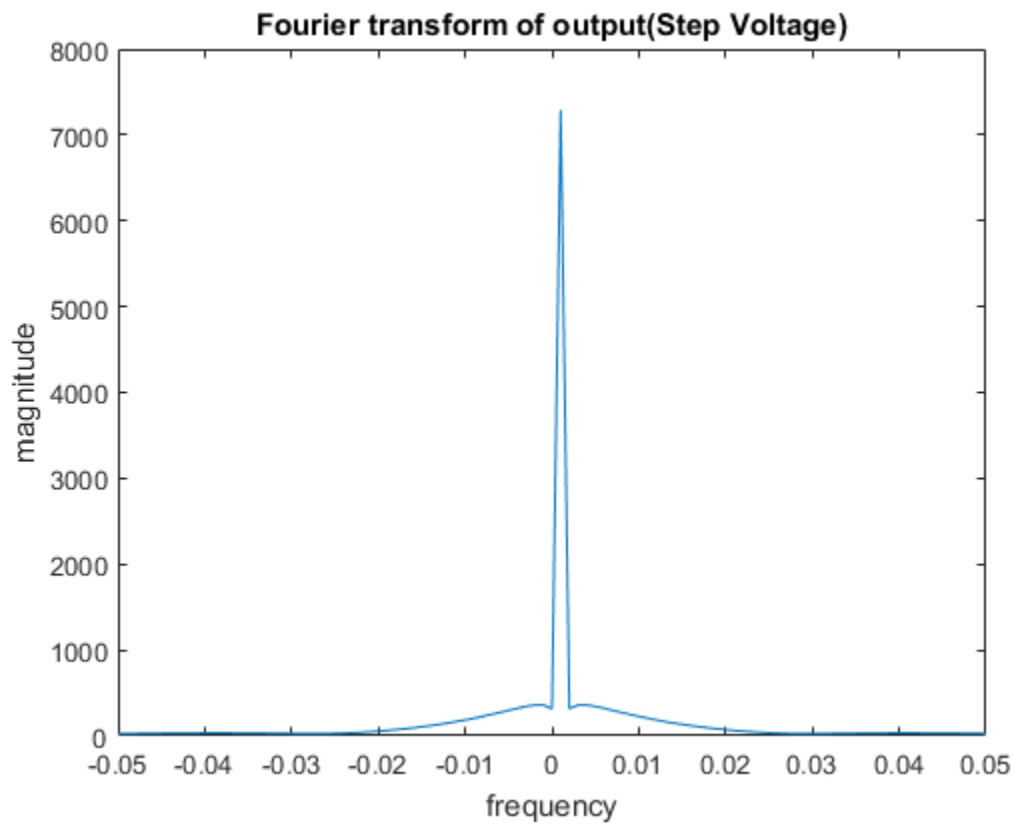
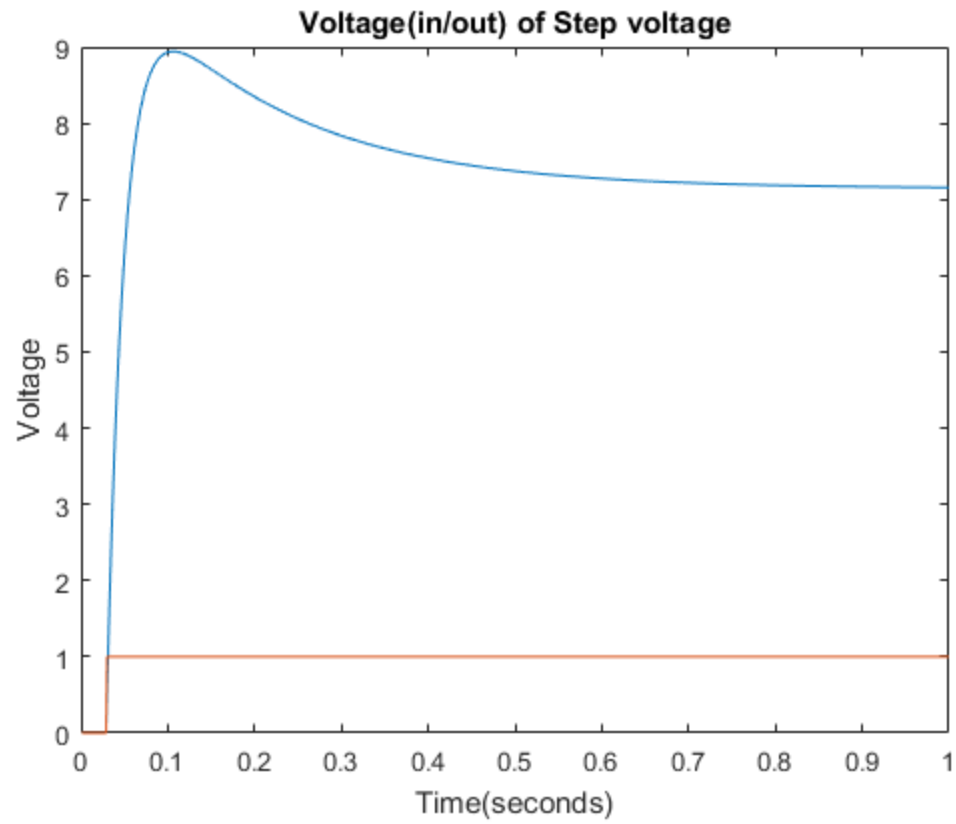
This is suspected to occur because it is changing how long a noise
 signal

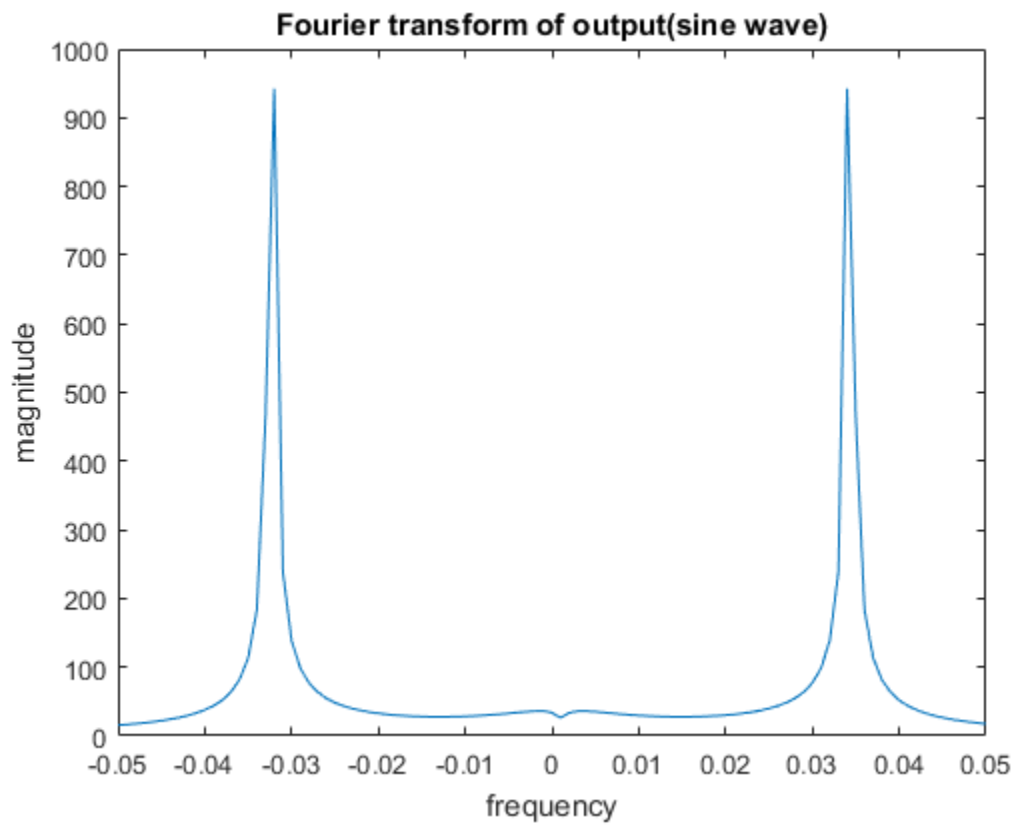
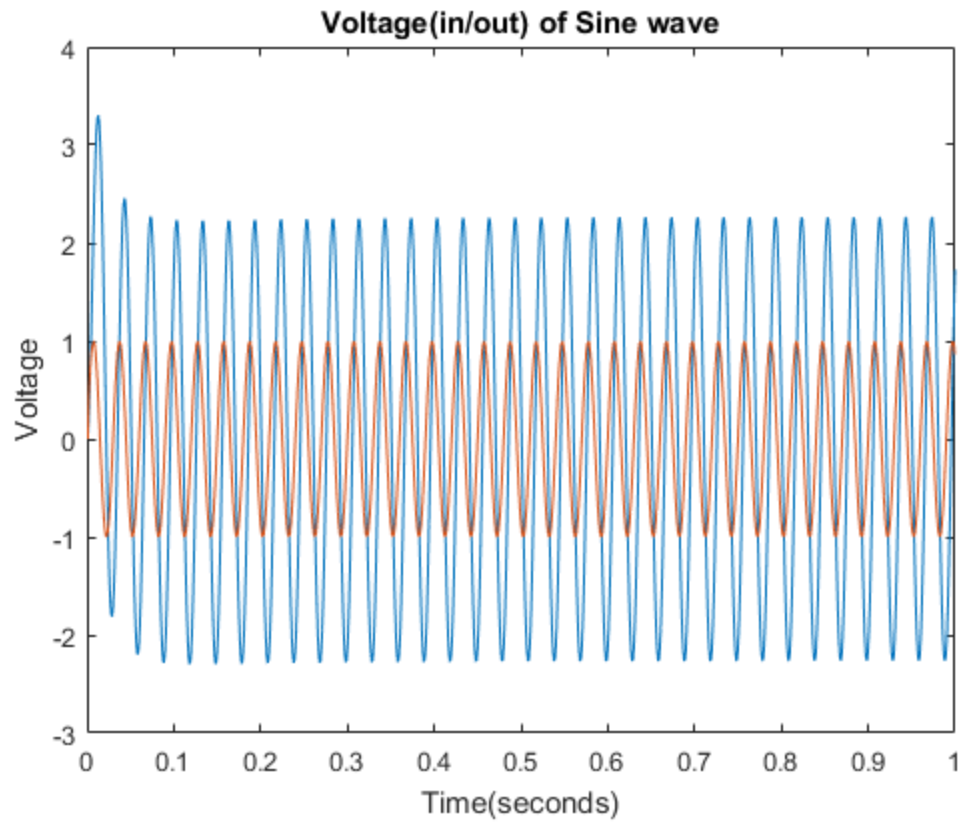
Will affect the circuit, including some of its transience

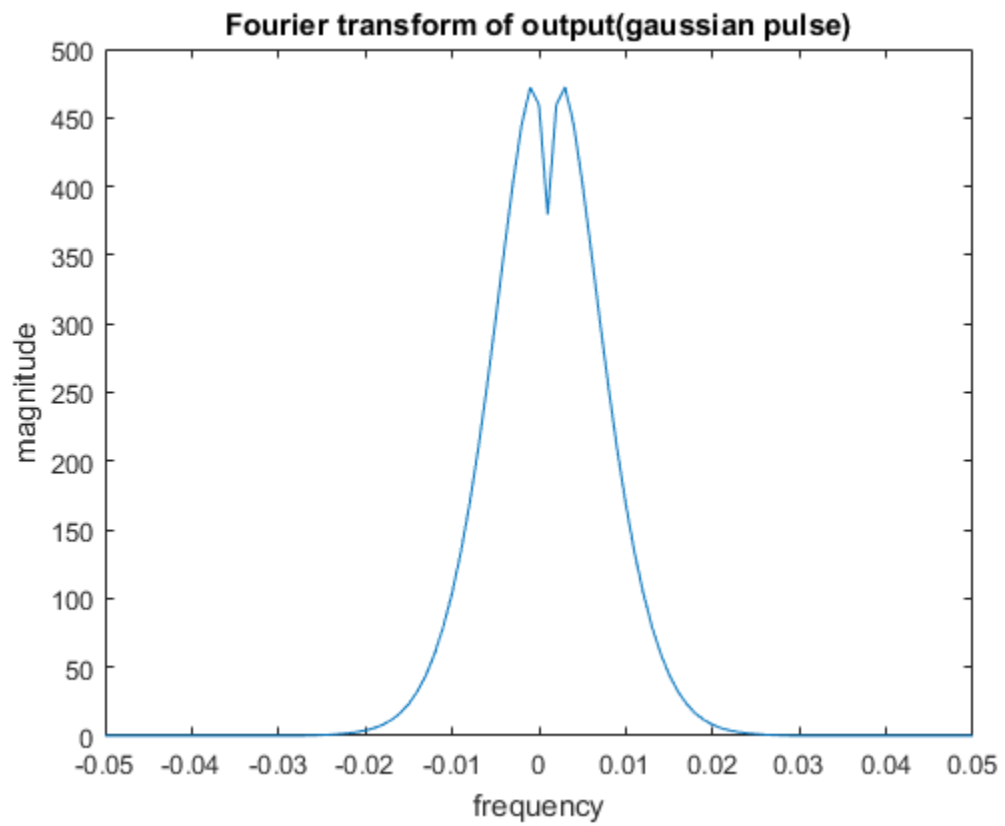
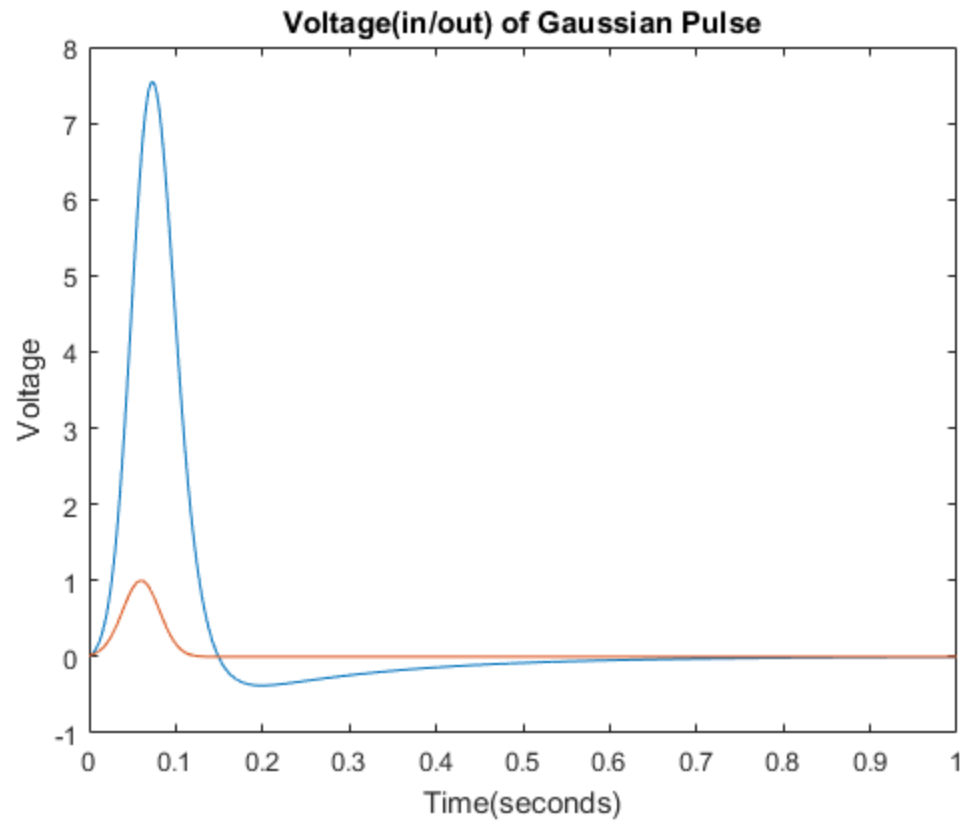
4.(a) model the equation $V4 - aI3 - BI3^2 - CI3^3$ as a polynomial and
 solve for it

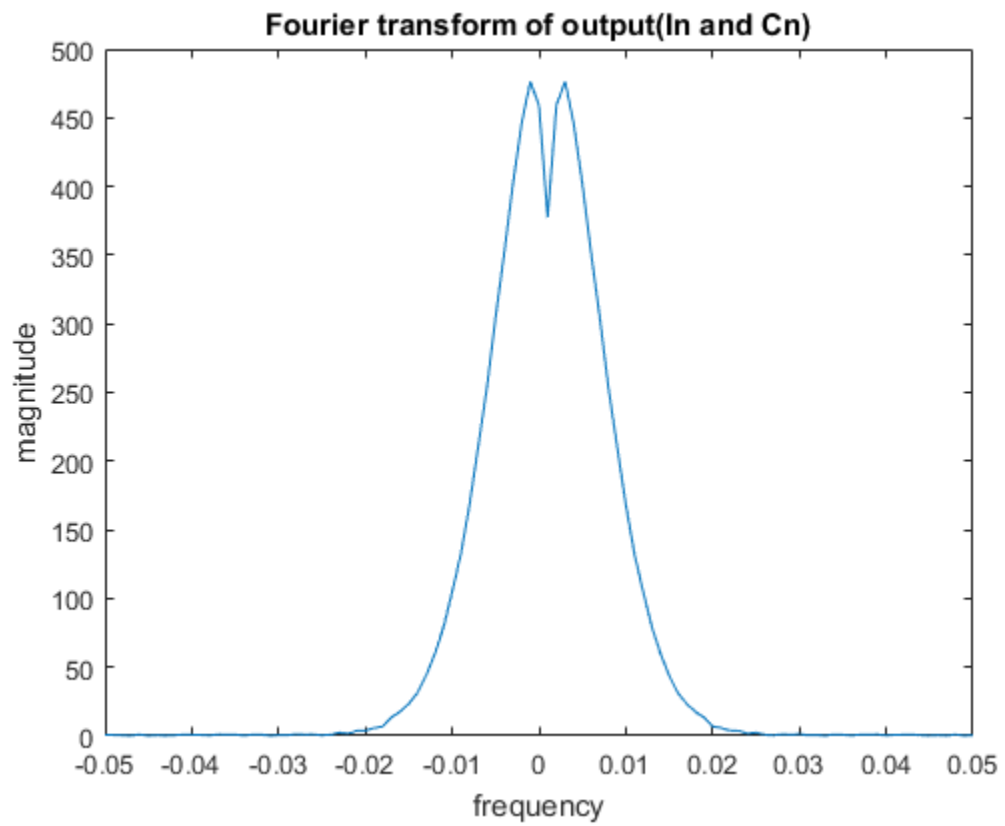
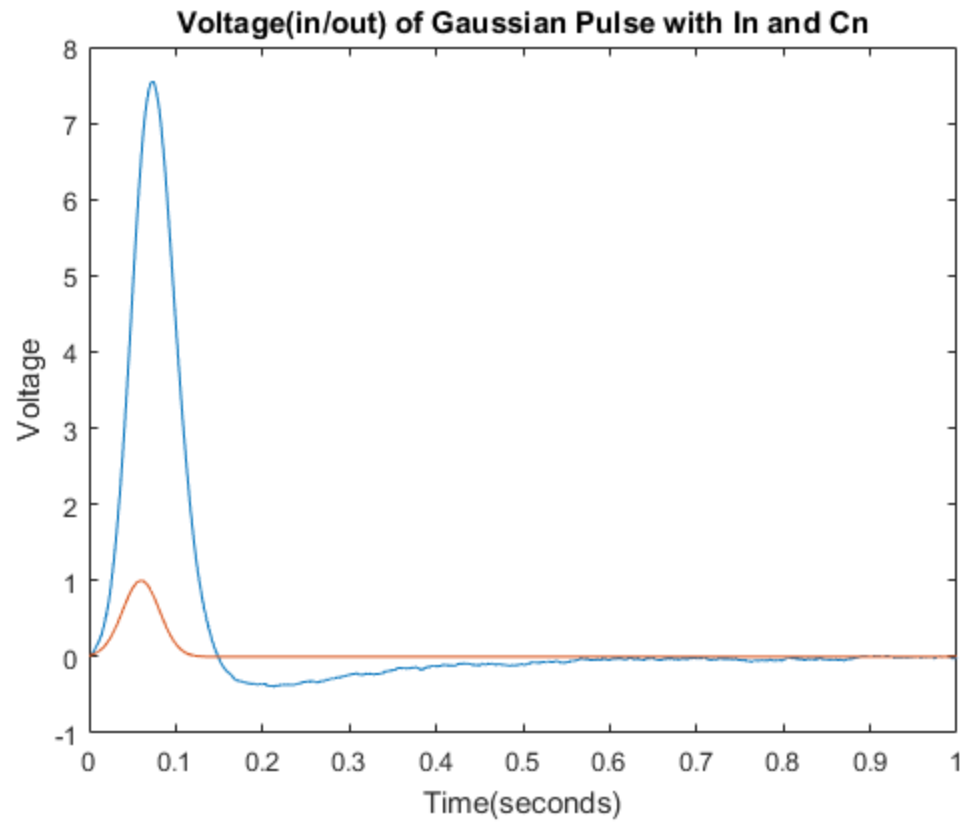
This would require a solve for the polynomial per iteration as V4
 changes

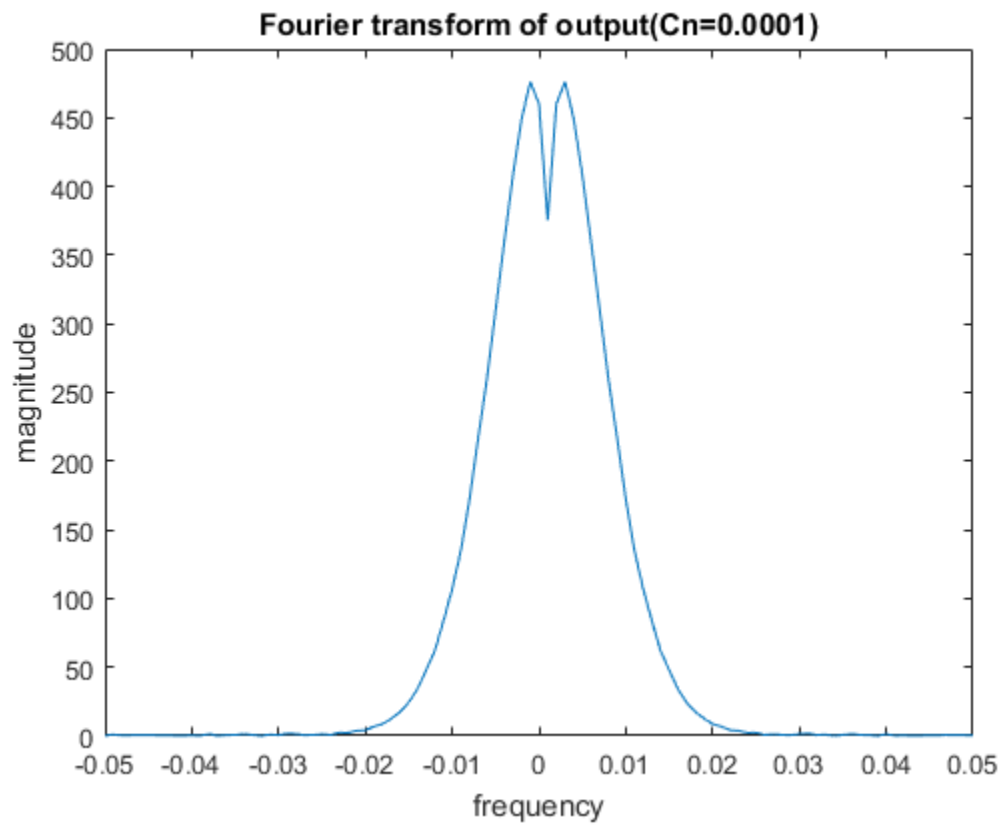
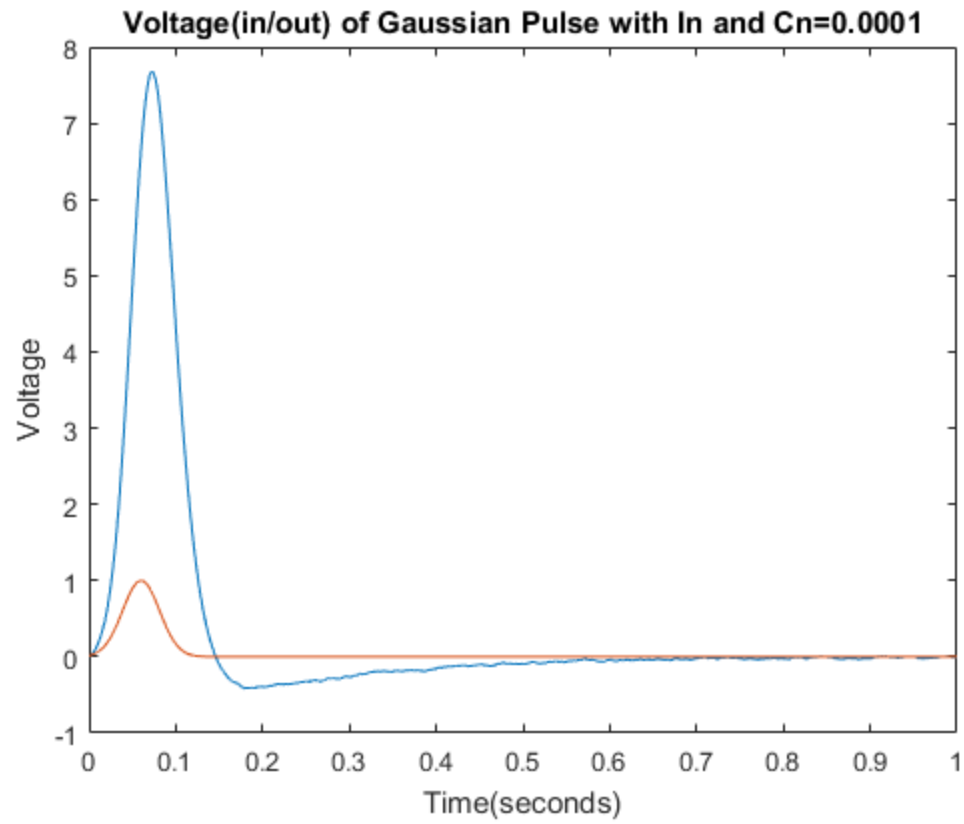
as well as an update to the F matrix per iteration to include the
 changing I3

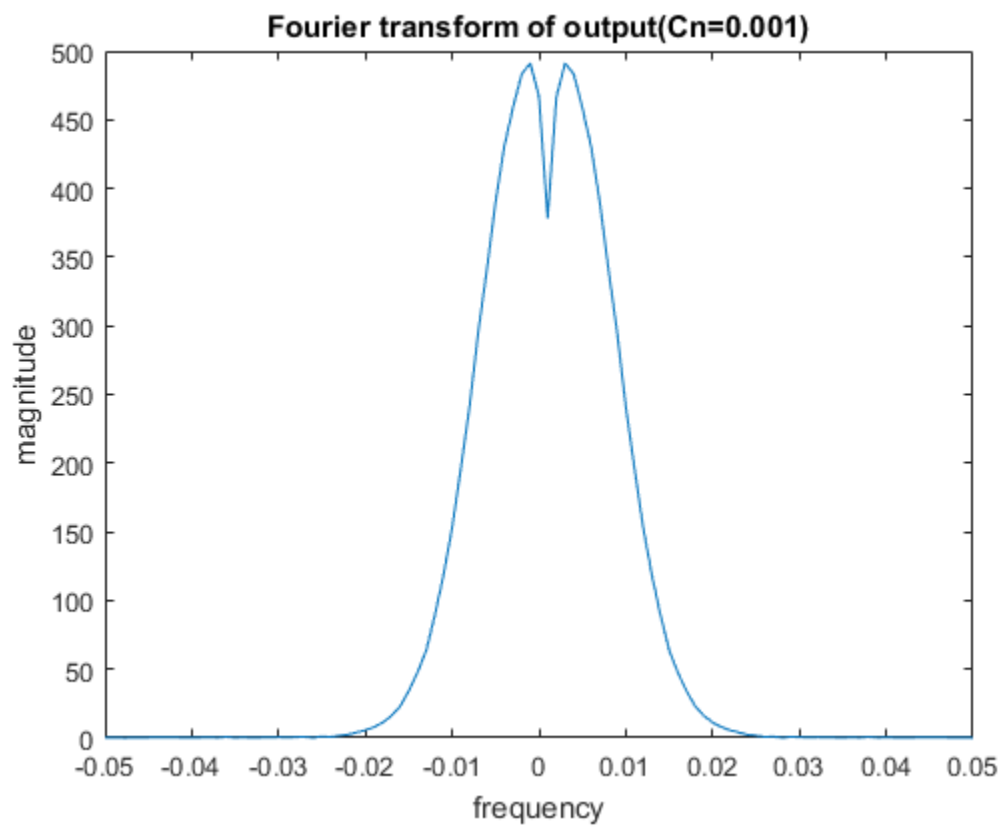
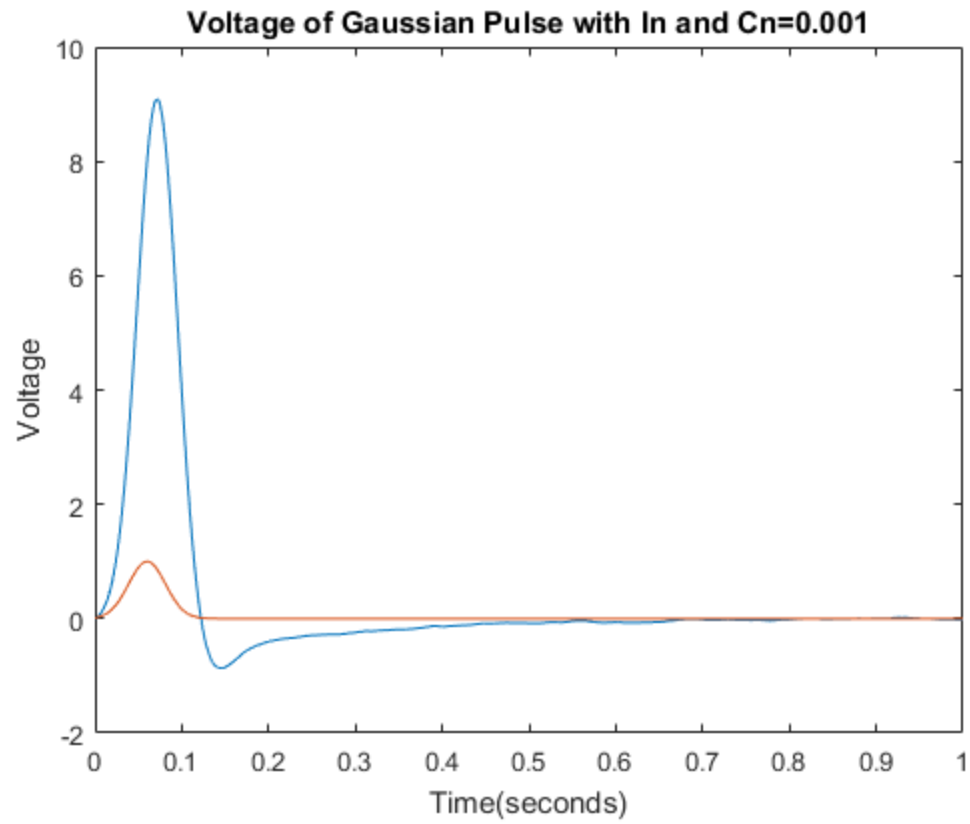


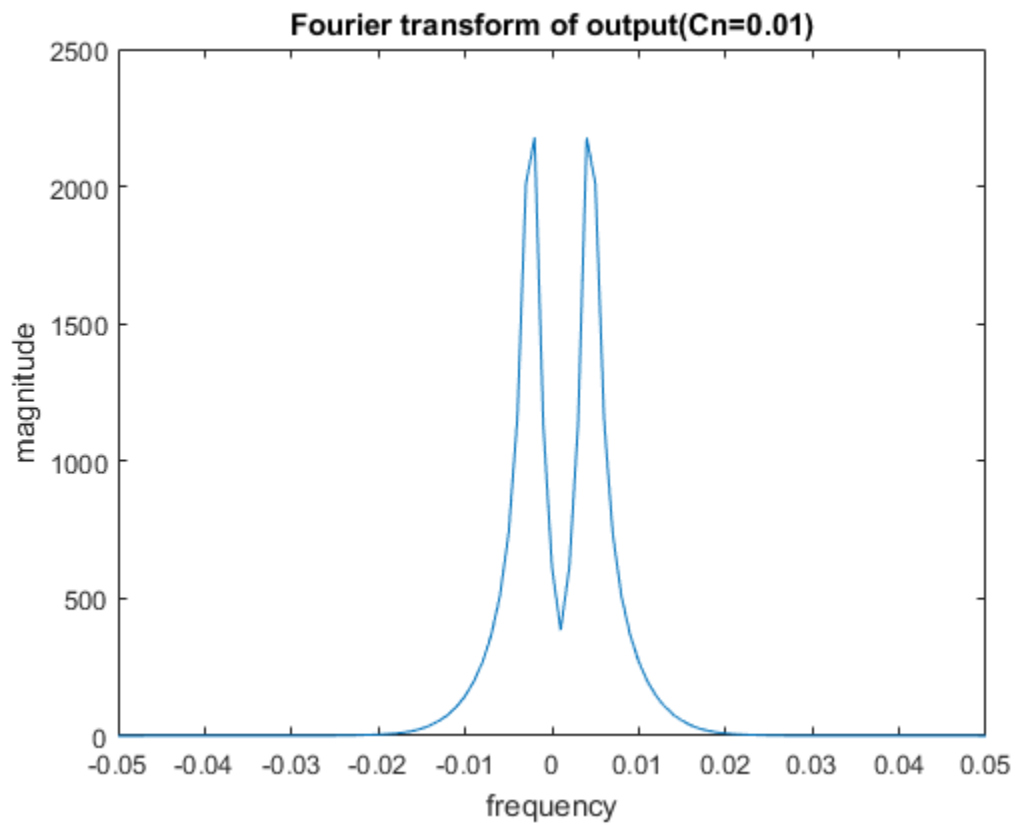
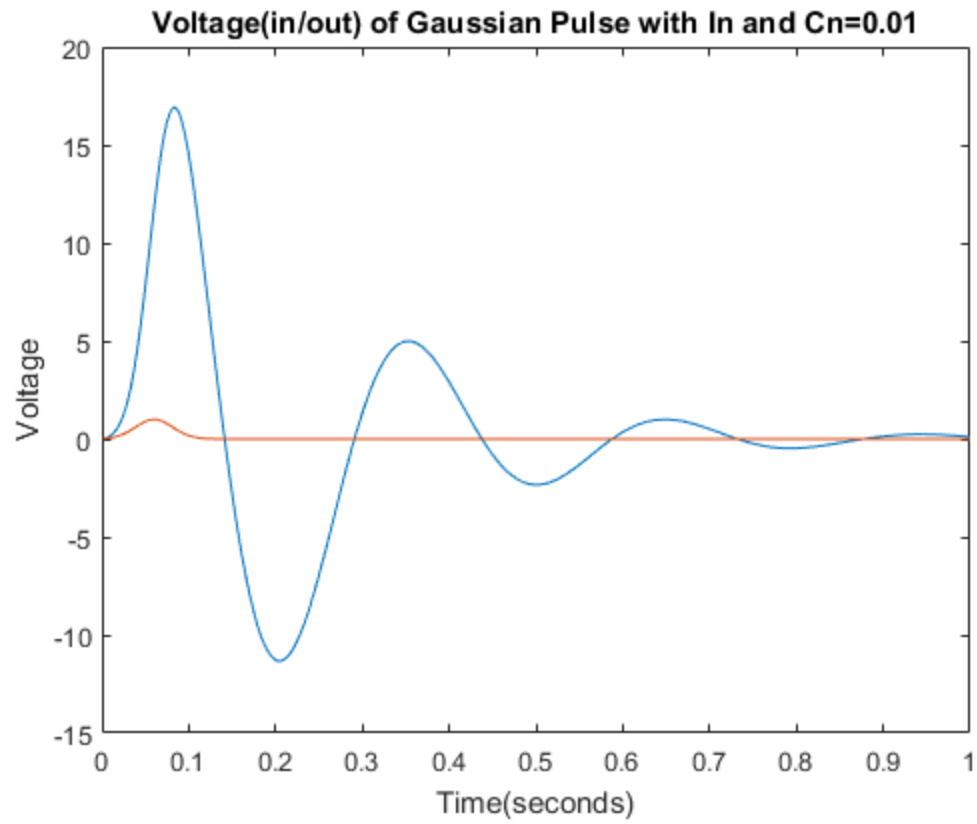


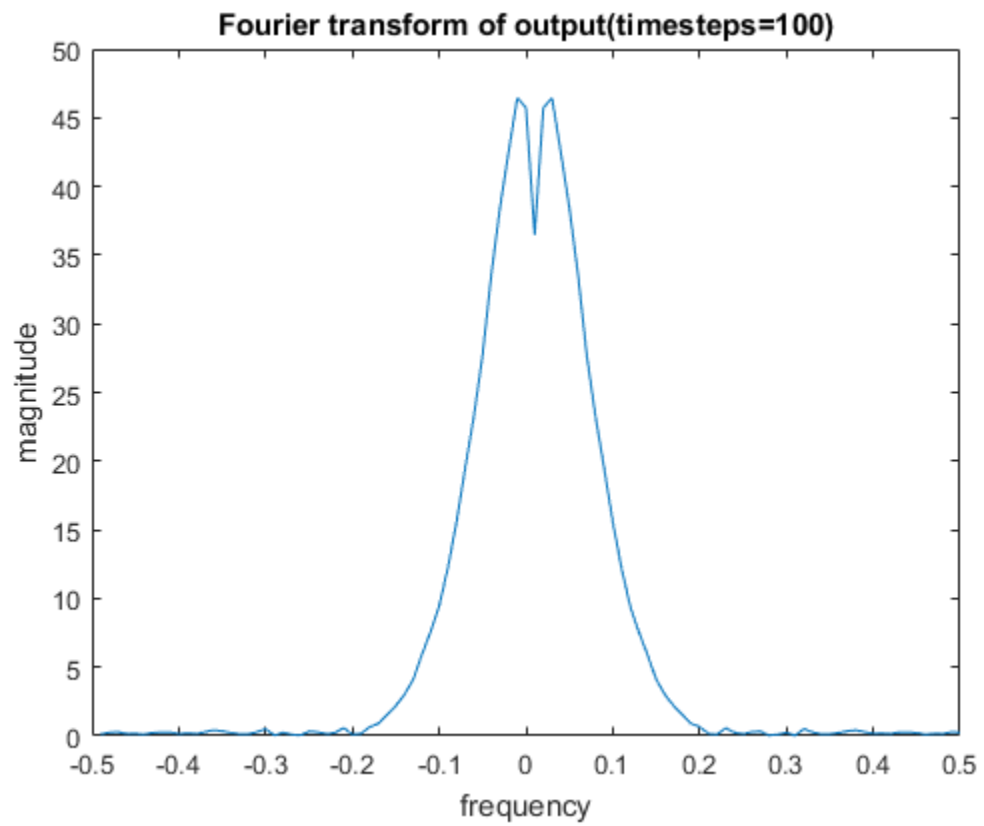
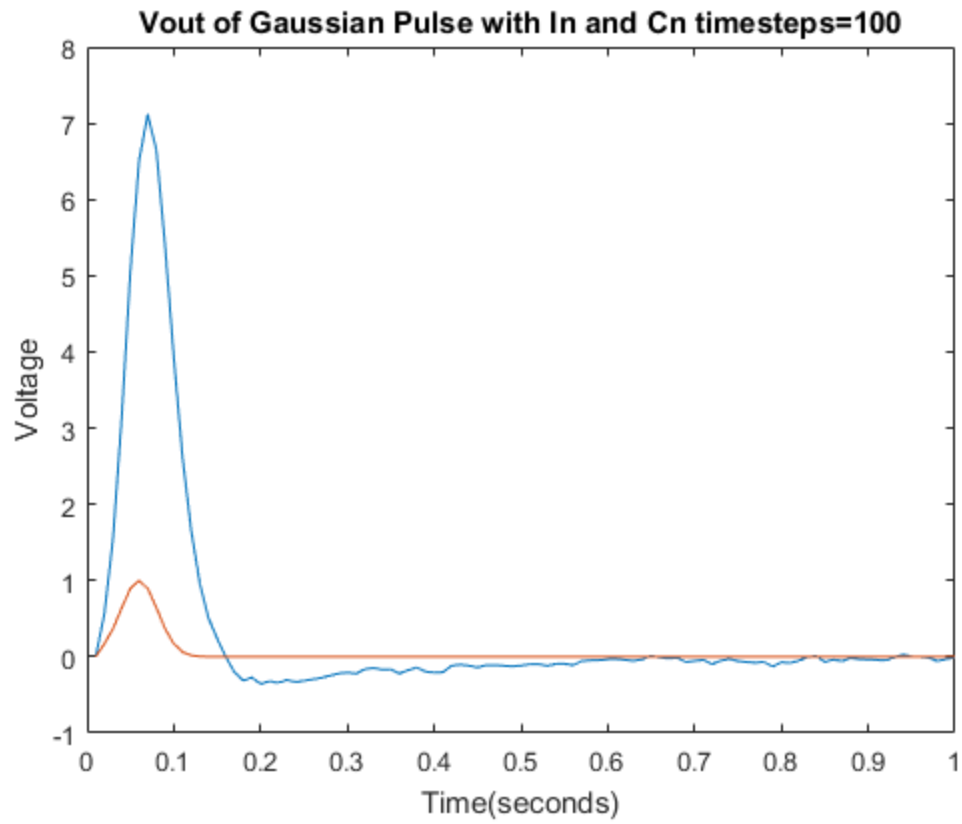


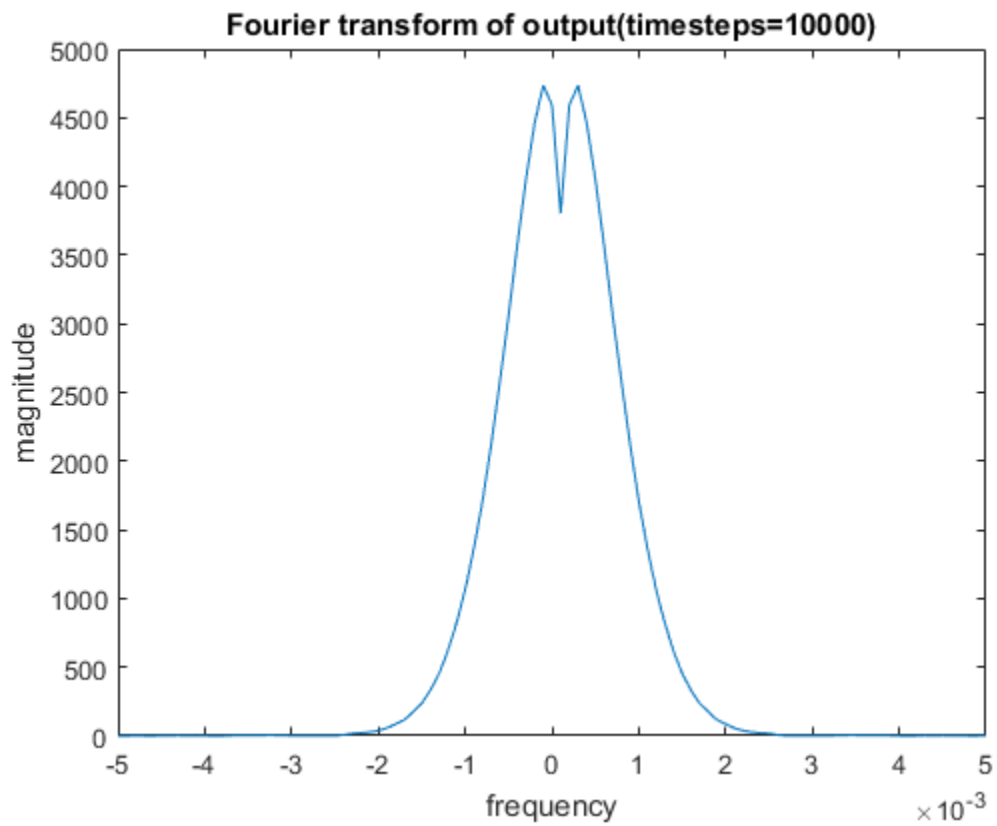
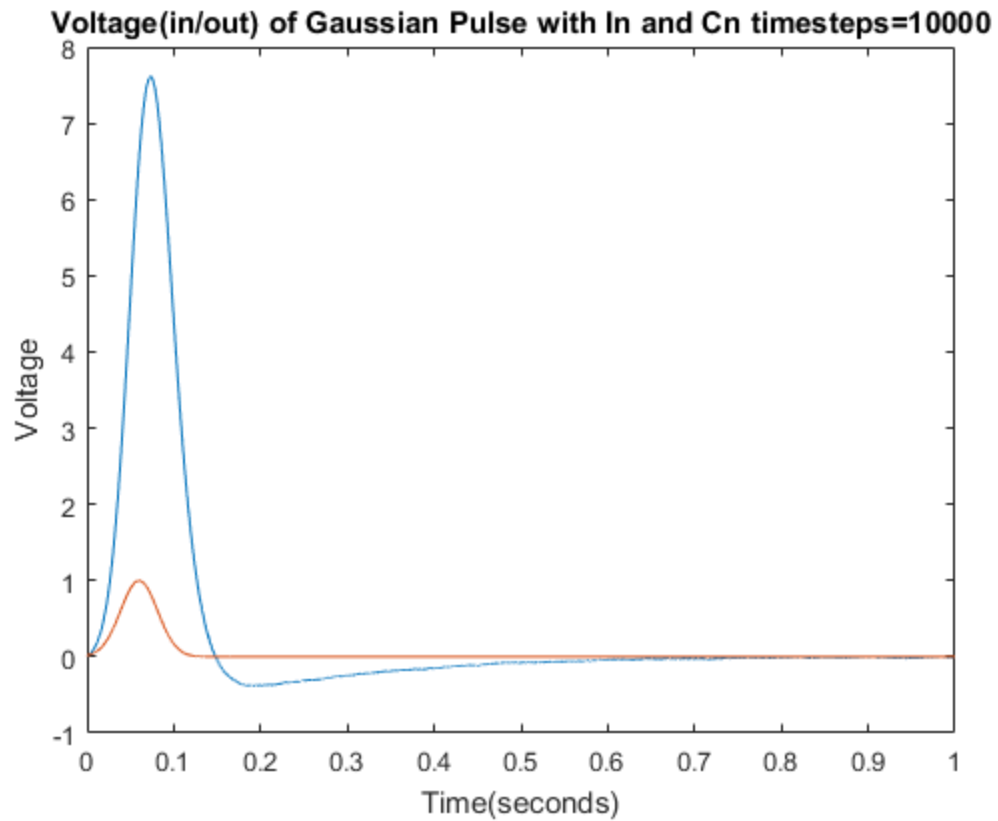












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