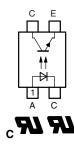


Vishay Semiconductors

Optocoupler, Phototransistor Output





17203-5

DESCRIPTION

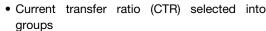
In the K817P part each channel consists of a phototransistor optically coupled to a gallium arsenide infrared-emitting diode in a 4 pin (single) plastic dual inline package.

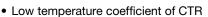
AGENCY APPROVALS

- BSI: EN 60065:2002, EN 60950-1:2006
- DIN EN 60747-5-2 (VDE 0884)
- FIMKO
- UL file no. E52744
- cUL tested to CSA 22.2 bulletin 5A

FEATURES

- Endstackable to 2.54 mm (0.1") spacing
- DC isolation test voltage 5000 V_{RMS}







- Low temperature decinicions of CTT
- Wide ambient temperature range
- · Available in single, dual and quad channel packages
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Programmable logic controllers
- Modems
- · Answering machines
- · General applications

ORDERING INFORMATION										
	_				1 -				DIP	
K	K 8		7 P		# x					
		PART N	IUMBER				ACKAGE OPTION		7.62 mm	
AGENCY CERTIFIED/					CTI	R (%)				
PACKAGE	5 mA		10) mA				5 mA		
VDE, BSI, FIMKO, UL, cUL	50 to 600	40 to 80	63 to 125	100 to 200	160 to 320	50 to 150	100 to 300	80 to 160	130 to 260	200 to 400
DIP-4	K817P	K817P1	K817P2	K817P3	K817P4	K817P5	K817P6	K817P7	K817P8	K817P9

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Reverse voltage		V_{R}	6	V				
Forward current		I _F	60	mA				
Forward surge current	t _p ≤ 10 μs	I _{FSM}	1.5	A				
Power dissipation		P _{diss}	70	mW				
Junction temperature		Tj	125	°C				
OUTPUT								
Collector emitter voltage		V _{CEO}	70	V				
Emitter collector voltage		V _{ECO}	7	V				
Collector current		I _C	50	mA				
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$	I _{CM}	100	mA				
Power dissipation		P _{diss}	70	mW				
Junction temperature		T _i	125	°C				



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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER TEST CONDITION SYMBOL VALUE UNIT									
COUPLER									
AC isolation test voltage (RMS)	t = 1 s	V _{ISO}	5000	V_{RMS}					
Total power dissipation		P _{tot}	200	mW					
Operating ambient temperature range		T _{amb}	- 40 to + 100	°C					
Storage temperature range		T _{stg}	- 55 to + 125	°C					
Soldering temperature (1)	2 mm from case, $t \le 10 \text{ s}$	T _{sld}	260	°C					

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to wave profile for soldering conditions for through hole devices.

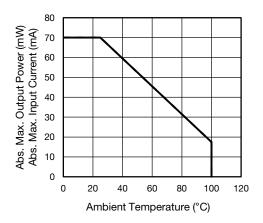


Fig. 1 - Absolute Maximum Output Power and Input Current vs. Ambient Temperature

ELECTRICAL CHARACTERISTICS									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
INPUT									
Forward voltage	I _F = 50 mA	V _F		1.25	1.6	V			
Junction capacitance	$V_R = 0 V, f = 1 MHz$	C _j		50		pF			
OUTPUT									
Collector emitter voltage	I _C = 100 μA	V_{CEO}	70			V			
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7			V			
Collector dark current	$V_{CE} = 20 \text{ V}, I_F = 0, E = 0$	I _{CEO}		10	100	nA			
COUPLER	·								
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 1 \text{ mA}$	V _{CEsat}			0.3	V			
Cut-off frequency	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V},$ $R_L = 100 \Omega$	f _c		110		kHz			
Coupling capacitance	f = 1 MHz	C _k		0.6		pF			

Note

T_{amb} = 25 °C, unless otherwise specified.

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.



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CURRENT TRANSFER RATIO									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	K817P	CTR	50		600	%		
		K817P1	CTR	40		80	%		
	V - 5 V I - 10 mA		63		125	%			
	$V_{CE} = 5 \text{ V}, I_{F} = 10 \text{ mA}$		100		200	%			
1 /1		K817P4	CTR	160		200 320	%		
I _C /I _F		K817P5	CTR	50		150	%		
		K817P6	CTR	100		300	%		
	$V_{CE} = 5 \text{ V}, I_{F} = 5 \text{ mA}$	K817P7	CTR	80		160	%		
		K817P8	CTR	130		260	%		
		K817P9	CTR	200		400	%		

SAFETY AND INSULATION RATED PARAMETERS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Partial discharge test voltage - routine test	100 %, t _{test} = 1 s	V _{pd}	1.6			kV		
Partial discharge test voltage - lot test (sample test)	$t_{Tr} = 60 \text{ s}, t_{test} = 10 \text{ s},$ (see figure 2)	V _{pd}	1.36			kV		
	V _{IO} = 500 V	R _{IO}	10 ¹²			Ω		
Insulation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	10 ¹¹			Ω		
modiation resistance	V _{IO} = 500 V, T _{amb} = 150 °C (construction test only)	R _{IO}	10 ⁹			Ω		
Rated impulse voltage		V _{IOTM}			6000	V _{peak}		
Max. working voltages	Recurring peak voltage	V _{IORM}			850	V _{peak}		
Forward current		I _{SI}			130	mA		
Power dissipation		P _{SO}			265	mW		
Safety temperature		T _{si}			150	°C		
Creepage distance					7.6	mm		

Note

• According to DIN EN 60747-5-5 (VDE 0884) (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

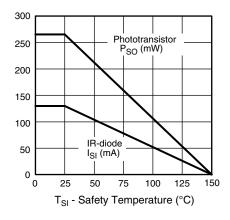


Fig. 2 - Derating Diagram

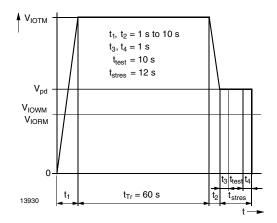


Fig. 3 - Test Pulse Diagram for Sample Test According to DIN EN 60747-5-2 (VDE 0884); IEC 60747-5-5



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SWITCHING CHARACTERISTICS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Delay time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t _d		3		μs		
Rise time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t _r		3		μs		
Fall time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t _f		4.7		μs		
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _s		0.3		μs		
Turn-on time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t _{on}		6		μs		
Turn-off time	V_S = 5 V, I_C = 2 mA, R_L = 100 Ω (see figure 1)	t _{off}		5		μs		
Turn-on time	V_S = 5 V, I_F = 10 mA, R_L = 1 k Ω (see figure 2)	t _{on}		3		μs		
Turn-off time	$V_S = 5 \text{ V}, I_F = 10 \text{ mA}, R_L = 1 \text{ k}\Omega \text{ (see figure 2)}$	t _{off}		10		μs		

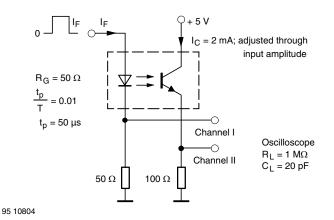


Fig. 1 - Test Circuit, Non-Saturated Operation

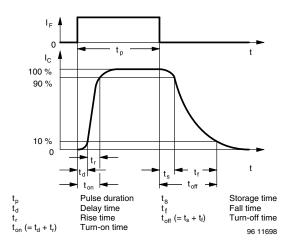


Fig. 3 - Switching Times

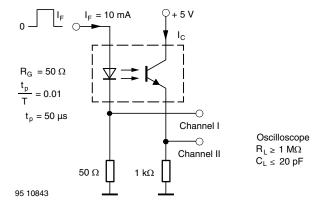


Fig. 2 - Test Circuit, Saturated Operation

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TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

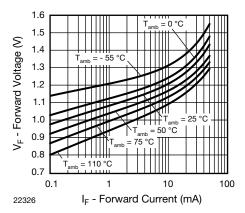


Fig. 4 - Forward Voltage vs. Forward Current

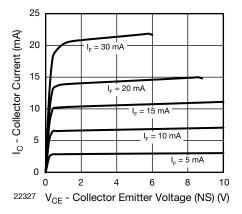


Fig. 5 - Collector Current vs. Collector Emitter Voltage (non-saturated)

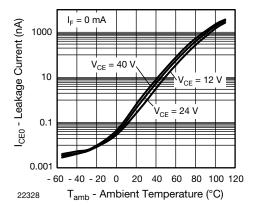


Fig. 6 - Leakage Current vs. Ambient Temperature

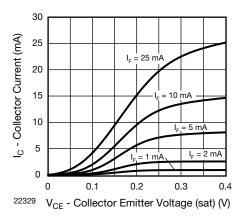


Fig. 7 - Collector Current vs. Collector Emitter Voltage (saturated)

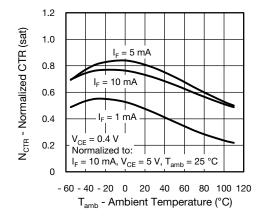


Fig. 8 - Normalized CTR (saturated) vs. Ambient Temperature

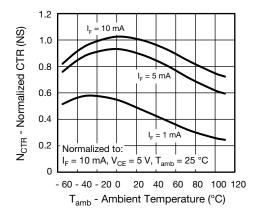


Fig. 9 - Normalized CTR (non-saturated) vs. Ambient Temperature



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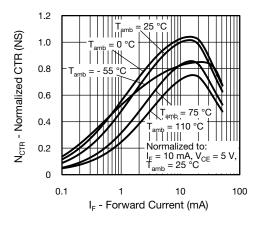


Fig. 10 - Normalized CTR (non-saturated) vs. Forward Current

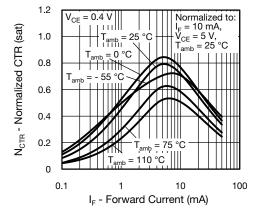


Fig. 11 - Normalized CTR (saturated) vs. Forward Current

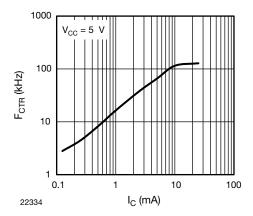


Fig. 12 - F_{CTR} vs. I_C (saturated) (mA)

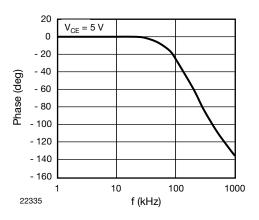


Fig. 13 - F_{CTR} vs. Phase Angle (kHz)

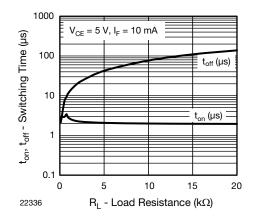
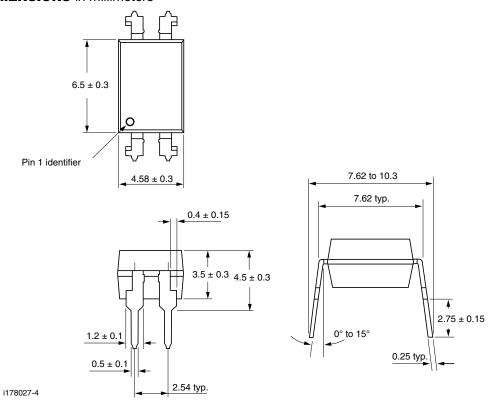


Fig. 14 - Switching Time vs. Load Resistance

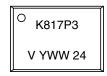


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PACKAGE DIMENSIONS in millimeters



PACKAGE MARKING





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Vishay

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