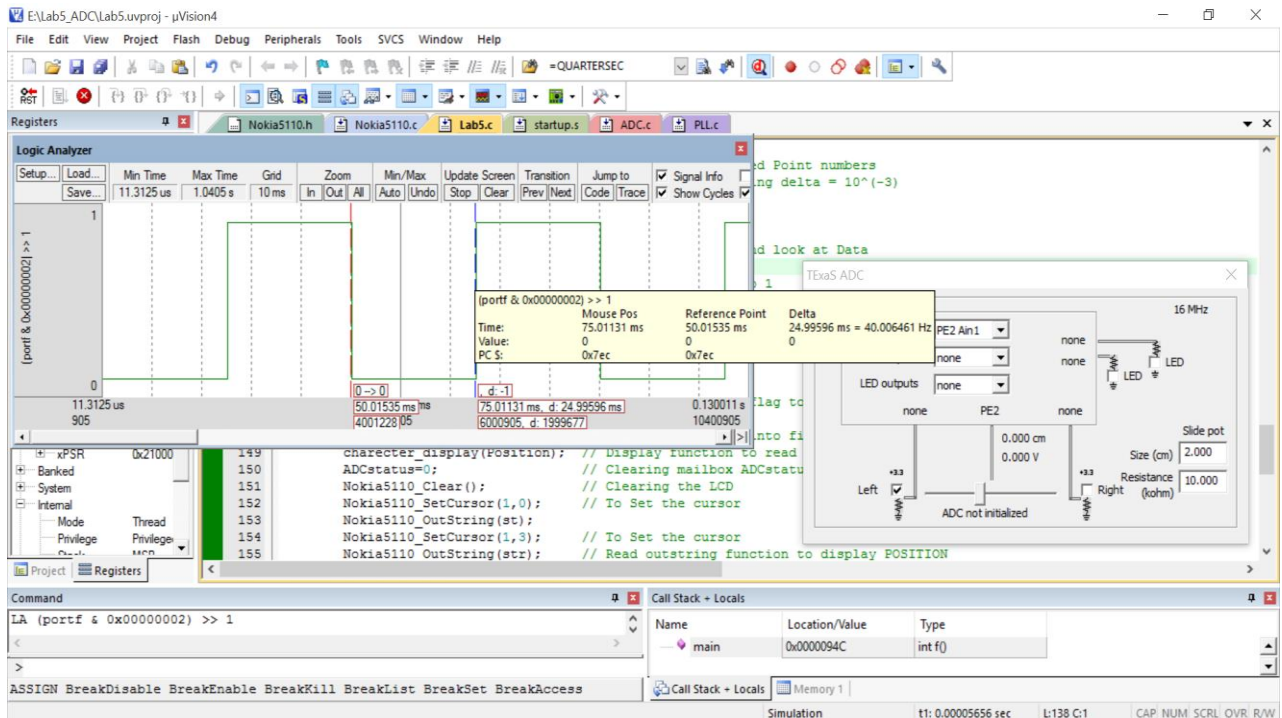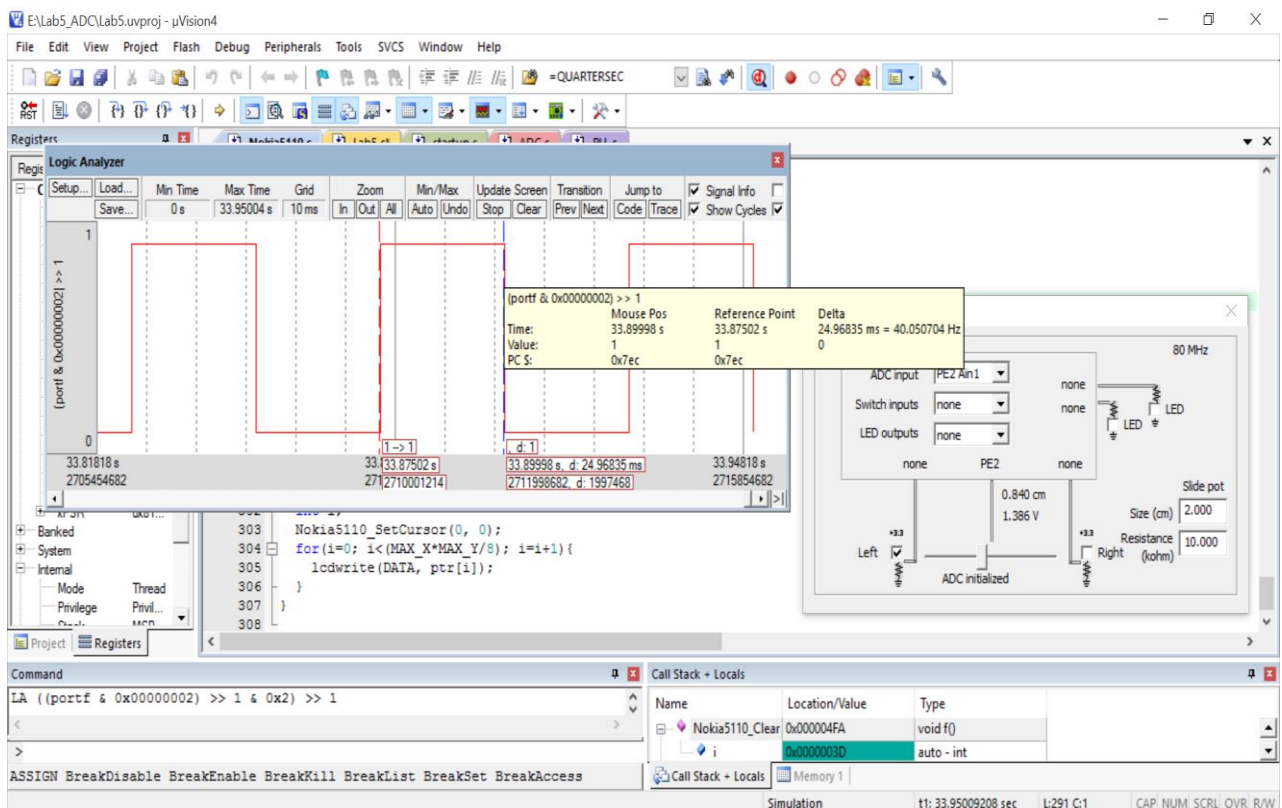1. Snap shot of the interrupt sampling on logic analyzer: Sampling rate=40Hz
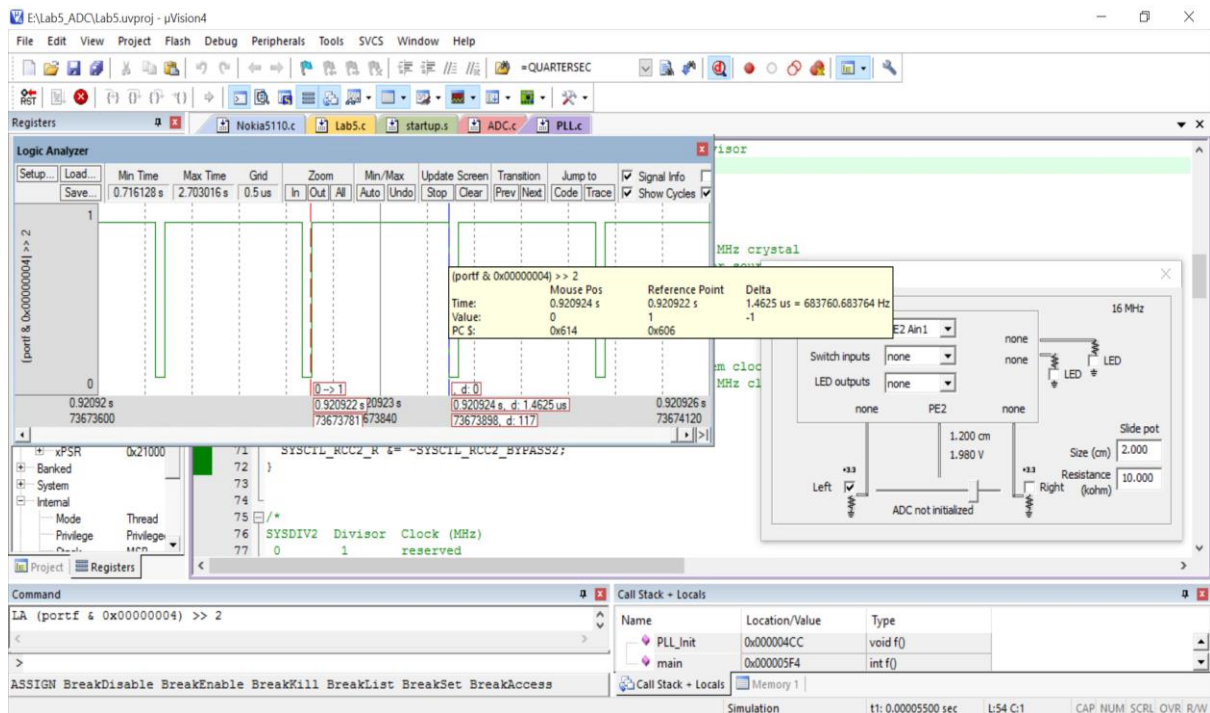

Logic analyzer output with sampling frequency 40Hz. Here we are using PF1 for output port.
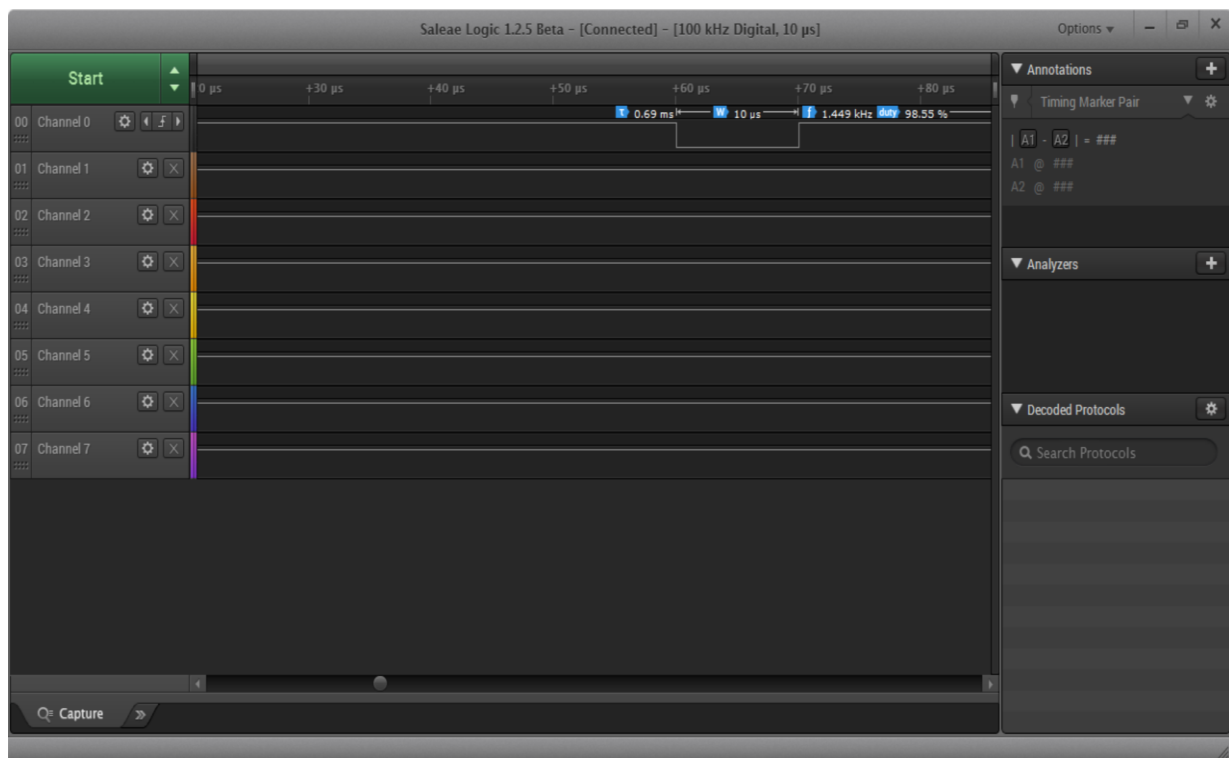

Logic analyzer output with sampling frequency 40Hz. Here we are using PE2 Ain1 for ADC input.

2. Snap shot of ADC convertion time:

Logic analyzer output for ADC convertion time. Here we are using PF2 for output port.

3. When ADC pin PE2 connected then the ADC convertion time waveform is-

Logic analyzer output with sampling frequency 40Hz along with time. Here we are using PF2.