

ECEN302 Lab 6 - Microblaze Softcore Processor

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1 Introduction

In this lab the primary objective was to implement the microblaze microcontroller with a FPGA. However, instead of manually programming this, the IP Catalog was utilised to speed up the development time. From this it will be possible to add a C programmable softcore microprocessor and “Intelligence” to FPGA designs as well as an understanding of how to implement such a function.

2 Methodology

To implement the microprocessor, the IP catalog was used to import it into a Vivado block diagram editor. This block diagram editor was used because it is drag and drop, making the development time and effort much less. Once it was added to the block diagram, because the board was predefined in the project creation as the Nexys4 DDR, it was possible to see all the available interfaces that the microprocessor could interact with. This provided the ability to drag in connections such as the FPGAs Clock, it's 16 input switches and output LEDs and the UART interface. Additionally, it is possible to configure the FPGA to have a certain size of memory which Vivado is able to automatically include, as well as any intermediate blocks for interfaces such as UART. An additional feature that makes development easy is the ability to automatically run connections due to the pins on the blocks being defined correctly. Once this was done, the block design was a fully functioning microcontroller that was clock with a UART interface, I/O and memory and this was done without writing any code.

The next step was to wrap the block diagram up as VHDL code and from there the bitstream could be generated just as if it were any other FPGA project. From here, the microcontroller could be programmed which was done in VITIS. The important thing to include for programming this was the XSA wrapper that Vivado can auto generate, this tells VITIS how to program the softcore microcontroller. This was uploaded using the system debugger built into VITIS which allows us full debug capabilities of the microcontroller, and step through it step by step to fully understand how the program works. Being a fully integrated IDE as well, we are able to easily navigate through all the files that make up a project.