

# Report On

# "mini C++ compiler (switch case)"

Submitted in partial fulfilment of the requirements for **Sem VI** 

# **Compiler Design Laboratory**

# Bachelor of Technology in Computer Science & Engineering

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## 1. INTRODUCTION

We have built a self-compiling mini-compiler for C++.

C++ is a general purpose programming language and widely used now a days for competitive programming. It has imperative, object-oriented and generic programming features.

The main constructs that we have focused on while building this compiler is 'switch'. The compiler also identifies arithmetic, boolean and logical operations.

The expected outcome of this project is to generate a Symbol Table, an Abstract Syntax Tree and Intermediate Three-Address code along with optimization.

#### 2. ARCHITECTURE

The mini compiler handles most cases in the C++09

Features Of The Lexer.

- 1. Identifies and removes comments
- 2. Identifies various operators in the language
- 3. Checks for validity of the identifiers
- 4. Identifying numeric constants and std::string type
- 5. Ignores white-spaces
- 6. Identifies scope of variables

Syntax is handled by YACC where grammar rules are specified for the entire language.

Semantics are handled using semantic rules for type checking while performing operations, to ensure operations are valid.

# 3. LITERATURE SURVEY

1. Lex & Yacc, O'Reilly, John R. Levine, Tony Mason, Doug Brown

# 4. CONTEXT FREE GRAMMAR

start: T\_INT T\_MAIN T\_OPEN T\_CLOSE comp\_stat {\$\$ = make\_leaf(\$1); \$ \$=make\_node("Main",\$2,\$5);

```
printf("\n\n");print_sym_tab(); YYACCEPT;}
comp stat: T OPBRACE SCOPE stat T CLBRACE {$
$=$3;scope[scope ind++]=0;}
SCOPE: {scope ctr++;scope[scope ind++]=scope ctr;}
stat:E T SC stat
                      {$$=make_node("Statement",$1,$3);}
                      {$$=make_node("Statement",$1,$2);}
  assign_expr stat
                      {$$=make node("Statement",$1,$2);}
  comp stat stat
                     {$$=make node("Statement",$1,$2);}
  |select stat stat
                            {$$=make_node("Statement",$1,$2);}
  Idecl stat
                      {$$=make node("Statement",$1,$2);}
  |jump stat stat
                     {$$=make_leaf(" ");}
  error T SC
ST: T SWITCH T OPEN T ID T CLOSE T OPBRACE {scope ctr+
+;scope[scope_ind++]=scope_ctr;} B T_CLBRACE {scope[scope ind+
+]=0;$$=make leaf($1);
                                           $$=make node("Switch",
(AST node*)$5,$7);if(!look up sym tab($3)){printf("Undeclared variable
%s\n", $3); YYERROR;}}
B : C
          {$$=$1;}
  |CD
           {$$=make node("Cases",$1,$2);}
          {$$=make node("Cases",$1,$2);}
  | C B
C : T CASE T NUM T COLON stat
                                  {$$=make node("Case",
(AST_node*)$2,$4);}
D : T DEFAULT T COLON stat
                               {$1=make leaf(" "); $
$=make node("Default",$1,$3);}
select stat: ST {$$=$1;}
```

```
jump stat:T CONTINUE T SC
                                   {$$=make leaf("Continue");}
            IT BREAK T SC
                              {$$=make leaf("Break");}
            |T RETURN E T SC {$1=make leaf("Return");$$ =
make node("Statement",$1,$2);}
decl:Type Varlist T SC {$1=(char*)make leaf($1); $$=make leaf($2); $
$=make node("Variable Declaration",(AST node*)$1,(AST node*)$2); }
  |Type assign expr1 {$1=(char*)make leaf($1); $
$=make node("Variable Declaration",(AST node*)$1,$2);}
Type:T INT
                 \{\$\$ = \$1; strcpy(typ,\$1); \}
     |T FLOAT
                 \{\$\$ = \$1; strcpy(typ,\$1); \}
      |T DOUBLE \{\$\$ = \$1; strcpy(typ,\$1); \}
      T CHAR
                       \{\$\$ = \$1; strcpy(typ,\$1); \}
Varlist: Varlist T COMMA T ID {$3=(char*)make leaf($3);$
$=(char*)make node("Variable List",(AST node*)$1,(AST node*)
$3);if(look up sym tab dec($3,scope[scope ind-1]))
{ yyerror("Redeclaration\n"); YYERROR; }
                                              if(scope[scope ind-1]>0)
{update sym tab($<var type>0,$3,yylineno,scope[scope ind-
1]);}else{int
scop=get scope();update sym tab($<var type>0,$3,yylineno,scop);}}
                             \{\$\$=(char*)make leaf(\$1);
if(look up sym tab dec($1,scope[scope ind-1])){    yyerror("Redeclaration\
n"); YYERROR; }
                                  if(scope[scope ind-1]>0)
{update sym tab($<var type>0,$1,yylineno,scope[scope_ind-
11); }else{int
scop=get scope();update sym tab($<var type>0,$1,yylineno,scop);}}
assign expr:T ID T ASSIGN E T COMMA assign expr
\{\$1=(char^*) \text{ make leaf}(\$1); \$\$=\text{make for node}(\$2,(AST node^*)\$1,
(AST_node*)$3,make_leaf(","),$5); if(!look_up_sym_tab($1))
{printf("Undeclared variable %s\n", $1); YYERROR;}}
             IT ID T ASSIGN E T SC
                                           {$1=(char*)make leaf($1); $
$=make node($2,(AST node*)$1,(AST node*)$3); if(!
look up sym tab($1)){printf("Undeclared variable %s\n", $1); YYERROR;}
```

```
assign expr1:T ID T ASSIGN E T COMMA assign expr1
{$1=(char*)make leaf($1);$$=make for node($2,(AST node*)$1,
(AST node*)$3,make leaf(","),$5);
if(look up sym tab dec($1,scope[scope ind-1])){    yyerror("Redeclaration\
n"); YYERROR; }
if(scope[scope ind-1]>0){update sym tab(typ,
$1,yylineno,scope[scope ind-1]);}else{int
scop=get scope();update sym tab(typ,$1,yylineno,scop);}}
            |T ID T ASSIGN E T SC
                                        {$1=(char*)make leaf($1);$
$=make node($2,(AST node*)$1,(AST node*)$3);
if(look up sym tab dec($1,scope[scope ind-1])){    yyerror("Redeclaration\
n"); YYERROR; } if(scope[scope ind-1]>0){update sym tab(typ,
$1,yylineno,scope[scope ind-1]);}else{int
scop=get scope();update sym tab(typ,$1,yylineno,scop);} }
E:E T PLUS T {$$=make node($2,$1,$3); }
IE T MINUS T {$$=make node($2,$1,$3);}
         {$$=$1;}
|T
T:T T MULT F {$$=make_node($2,$1,$3);}
|TTDIVF {$\$=make node(\$2,\$1,\$3);}
ΙF
        {$$=$1;}
F:T ID {$$=make leaf($1); if(!look up sym tab($1)){printf("Undeclared
Variable %s\n", $1); YYERROR;} }
IT NUM
              {$$=make leaf($1);}
|TOPENETCLOSE {$$=$2;}
|unary expr \{$$=$1;}
|s operation
               {$$=$1;}
s operation: T ID s op T ID {$1=(char*)make leaf($1);
$3=(char*)make leaf($3); $$=make node($2,(AST node*)$1,(AST node*)
$3); if(!look up sym tab($1)){printf("Undeclared variable %s\n", $1);
YYERROR; \(\) if(!look up sym tab(\(\$3\)) \{\) printf("Undeclared variable \%s\n",
$3); YYERROR; } }
                  | T ID s op T NUM {$1=(char*)make leaf($1);
$3=(char*)make leaf($3); $$=make node($2,(AST node*)$1,(AST node*)
```

```
$3); if(!look up sym tab($1)){printf("Undeclared variable %s\n", $1);
YYERROR; } }
                  | T ID s op T OPEN E T CLOSE
{$1=(char*)make leaf($1); $$=make node($2,(AST node*)$1,
(AST node*)$4);}
s op:T SPLUS {$$=$1;}
     |T SMINUS {$$=$1;}
     |T SMULT {$$=$1;}
     |T SDIV {$$=$1;}
unary expr:T INC T ID {$$=make leaf($1); $$=make leaf($2);$
$=make node("temp",(AST node*)$1,(AST node*)$2); if(!
look up sym tab($2)){printf("Undeclared variable %s\n", $2);
YYERROR; } }
            |T ID T INC {$$=make leaf($1); $$=make leaf($2);$
$=make node("temp",(AST node*)$1,(AST node*)$2); if(!
look up sym tab($1)){printf("Undeclared variable %s\n", $1);
YYERROR; } }
            IT DEC T ID {$$=make leaf($1); $$=make leaf($2);$
$=make node("temp",(AST node*)$1,(AST node*)$2); if(!
look up sym tab($2)){printf("Undeclared variable %s\n", $2);
YYERROR; } }
            |T ID T DEC {$$=make leaf($1); $$=make leaf($2);$
$=make node("temp",(AST node*)$1,(AST node*)$2); if(!
look up sym tab($1)){printf("Undeclared variable %s\n", $1);
YYERROR; } }
            | T MINUS T ID {$$=make leaf($1); $$=make leaf($2);$
$=make node("temp",(AST node*)$1,(AST node*)$2); if(!
look up sym tab($1)){printf("Undeclared variable %s\n", $1);
YYERROR; } }
            IT MINUS T NUM {$$=make leaf($1); $$=make leaf($2);$
$=make node("temp",(AST node*)$1,(AST node*)$2);}
```

#### 5. DESIGN STRATEGIES

#### SYMBOL TABLE

The Symbol Table is used for storing variables declared and their attributes, along with details about function calls. The Symbol table stores the size of a variable, it's scope and also the line numbers where the particular variable is used.

We used hash tables to implement the symbol table. The variable names were hashed and allocated a fixed amount of space in the table. Every new scope has it's own symbol table, which would be destroyed once the scope would end. All symbol tables are connected using an n-ary tree. So basically, we have implemented an n-ary tree of hash tables. Starting with the root node which has the global symbol table.

Upon encountering a new scope, a new hash table is created as a child node of the root node. If within the same scope another new scope is encountered a new child node of the current scope node is created, and so on. Sibling nodes (i.e child nodes on the same level) have different hash tables and hence cannot access each other's data. But a child node can access data from it's parent hash table.

#### • INTERMEDIATE CODE GENERATION

To convert our given C language into intermediate code, we have used the SDT scheme and made use of marker non-terminals in place of action records. Label generation:

We have made use of stack whose elements are of type records. Each entry to the stack is a hash table for which we have used maps. Each of marker non-terminal will push a map record to the stack which contains

#### CODE OPTIMIZATION

We have implemented Dead Code elimination for the ICG generated. Dead codes are pieces of code that contain temporaries that are not used further or anywhere else in the generated ICG. We keep track of all the useful temporaries and hence when we encounter a line which uses a non-useful temporary, we can eliminate that line of code from the ICG.

#### • ERROR HANDLING

We are handling syntax errors, which are generated during parsing. We are also handling re-declaration of variables in the same scope, and are showing appropriate error messages. We stop parsing the input on encountering these errors and display

the line number of the errors, intending the user to resolve the issue. Also handling type mismatch errors.

#### 6. IMPLEMENTATION DETAILS.

#### • SYMBOL TABLE

#### **Data Structure Implementation**

Structure to point to reference of each variable occurrence in program

```
struct node
      char token[100];
      char attr[100];
      struct node *next;
};
Functions
Functions that have been written for the symbol table
struct node * createNode(char *token, char *attr)
      struct node *newnode;
      newnode = (struct node *) malloc(sizeof(struct node));
      strcpy(newnode->token, token);
      strcpy(newnode->attr, attr);
      newnode->next = NULL;
      return newnode;
}
int hashIndex(char *token)
      int hi=0;
      int I,i;
      for(i=0;token[i]!='\0';i++)
            hi = hi + (int)token[i];
      hi = hi%eleCount;
      return hi;
}
```

```
void insertToHash(char *token, char *attr)
     int flag=0;
     int hi;
     hi = hashIndex(token);
     struct node *newnode = createNode(token, attr);
     /* head of list for the bucket with index "hashIndex" */
     if (hashTable[hi].head==NULL)
           hashTable[hi].head = newnode;
           hashTable[hi].count = 1;
           return;
     struct node *myNode;
      myNode = hashTable[hi].head;
      while (myNode != NULL)
     if (strcmp(myNode->token, token)==0)
                 flag = 1;
                 break;
      myNode = myNode->next;
     if(!flag)
           //adding new node to the list
           newnode->next = (hashTable[hi].head);
           //update the head of the list and no of nodes in the current
bucket
           hashTable[hi].head = newnode;
           hashTable[hi].count++;
      }
     return;
}
Functions Implemented
All the functions that have been implemented for Symbol Table
struct node * createNode(char *token, char *attr)
int hashIndex(char *token)
void insertToHash(char *token, char *attr)
```

#### • INTERMEDIATE CODE GENERATION

```
Data Structure Implementation
Following is the type of non-terminals used in our grammar.
struct exprType{
      char *addr;
      char *code:
};
Functions
Functions that have been written for the Intermediate Code Generator
//Function to generate new temporary variables
char * newTemp(){
      char *newTemp = (char *)malloc(20);
      strcpy(newTemp,"t");
      num to concatinate[0]=0;
      snprintf(num to concatinate, 10, "%d", n);
      strcat(newTemp,num to concatinate);
      n++;
      return newTemp;
}
//Function to generate new labels
char * newLabel(){
      char *newLabel = (char *)malloc(20);
      strcpy(newLabel,"L");
      snprintf(num to concatinate I, 10,"%d",nl);
      strcat(newLabel,num to concatinate I);
      nl++;
      return newLabel;
}
//Function to replace a substring str with another substring label in the
original string s1
void replace(char* s1,char* str, char* label)
{
      char* check = strstr (s1,str);
                  while(check!=NULL){
                  strncpy (check,label,strlen(label));
                  strncpy (check+strlen(label)," ",(4-strlen(label)));
                  check = strstr (s1,str);}
```

```
}
```

#### **Functions Implemented**

All the functions that have been implemented for Intermediate Code Generator

```
char * newTemp()
char * newLabel()
void replace(char* s1,char* str, char* label)
```

#### CODE OPTIMIZATION

#### **Data Structure Implementation**

Following is the type of non-terminals used in our grammar.

```
union {
    int ival;
    float fval;
    char *sval;
    struct exprType *EXPRTYPE;
}
```

#### ERROR HANDLING

Function to handle syntax errors, gets called automatically by yacc on encountering syntax errors

```
void yyerror(const char* msg)
{
    printf("%s", msg);
}
```

For re-declaration check we used a flag variable declare=0, which would be set and unset accordingly in the same scope. If it was previously set and is set again in the same scope, we can catch the error and display appropriate error messages.

#### • RUNNING CODE

o Running Lexer

```
lex lexer.l
gcc lex.yy.c
./a.out testcase1.cpp
```

o Running Parser

flex switch.I yacc -d switch.y gcc y.tab.c lex.yy.c -w ./a.out < testcase1.cpp

• Running Intermediate Code Generator

flex icg\_ket.y yacc -d icg\_ket.y gcc y.tab.c -ll -ly -w ./a.out > output.txt

- Running Code Optimizer
  - For Dead Code Elimination
  - Constant Folding

python codeopt.py

## 7. RESULTS

We were able to successfully generate the different representations of input code written in C++ along with performing simple machine independent optimization on intermediate code which was generated.

#### **Shortcomings**

- 1. Error handling could have been done better by adding more rules.
- 2. There could've been more ways for us to implement Code Optimization techniques

# 8. SNAPSHOTS

#### • INPUT

```
#include <iostream>
int main()
    char oper = '+';
    float num1 = 3, num2 = 5;
    switch (oper) {
        case '+':
            cout << num1 << " + " << num2 << " = " << num1 + num2;
            break;
        case '-':
            cout << num1 << " - " << num2 << " = " << num1 - num2;
            break;
        case '*':
            cout << num1 << " * " << num2 << " = " << num1 * num2;
            break;
        case '/':
            cout << num1 << " / " << num2 << " = " << num1 / num2
        default:
            // operator is doesn't match any case constant (+, -, *, /)
            cout << "Error! The operator is not correct";</pre>
            break;
    return 0;
```

#### • SYMBOL TABLE

	****	**** SYMBOL TA	BLE *****	
SNo	I	Token		Token Type
1		(		SPECIAL SYMBOL
2		j		SPECIAL SYMBOL
3		0		INTEGER CONSTANT
4		1		INTEGER CONSTANT
5		3		INTEGER CONSTANT
6		5		INTEGER CONSTANT
7		7		INTEGER CONSTANT
В				SPECIAL SYMBOL
9		=		OPERATOR
10		56		INTEGER CONSTANT
11		Х		IDENTIFIER
12		у		IDENTIFIER
13		Z		IDENTIFIER
14		{		SPECIAL SYMBOL
15		}		SPECIAL SYMBOL
16		209		INTEGER CONSTANT
17		case		KEYWORD
18		break		KEYWORD
19		switch		KEYWORD
20		default		KEYWORD

#### • TOKENS GENERATED FROM LEXER

x	TDENTTETER	Line 1
=	IDENTIFIER OPERATOR INTEGER CONSTANT	Line 1
3	INTEGER CONSTANT	Line 1
;	SPECIAL SYMBOL IDENTIFIER OPERATOR INTEGER CONSTANT SPECIAL SYMBOL IDENTIFIER OPERATOR	Line 1
y	TOENTIETER	Line 2
=	OPERATOR	Line 2
5	THERED CONSTANT	Line 2
;	SDECTAL SYMBOL	Line 2
Z	THENTIETED	Line 3
=	ODEDATOD	Line 3
9	INTEGER CONSTANT	Line 3
		Line 3
		Line 5
		Line 5
		Line 5
x )		Line 5
		Line 5
0200	KEYWODD	Line 6
0	THITECED CONSTANT	Line 6
:z		Line 7
=	OPERATOR	Line 7
		Line 7
;	CDECTAL CVMPOL	Line 7
, break		Line 8
case	KEYWORD KEYWORD	Line 9
1	THIECED CONSTANT	Line 9
:Z		Line 9
		Line 10
= 56		Line 10
	CDECTAL CVMPOL	Line 10
; break	KEYWORD	Line 10
·	SPECIAL SYMBOL	Line 11
, case	KEYWORD	Line 12
3	INTEGER CONSTANT	Line 12
:Z	IDENTIFIER	Line 12
=	OPERATOR	Line 13
209	INTEGER CONSTANT	Line 13
	SPECIAL SYMBOL	Line 13
; break	KEYWORD	Line 13
·	SPECIAL SYMBOL	Line 14 Line 14
0300	KEYWORD	Line 14
case 7	INTEGER CONSTANT	Line 15
	TATELOUIS FAINT	Lile 12

#### • PARSER OUTPUT

```
Parsing the following Input:
int main() {
    char oper;
    float num1, num2;
    cout << "EnterUndeclared Variable cout</pre>
 an operator (+, -, *, /): ";
    cin >> oper;
    cout << "Enter two numbers: " << endl;</pre>
    cin >> num1 >> num2;
    switch (oper) {
        case '+':
            cout << num1 << " + " << num2 << " = " << num1 + num2;
        case '-':
            cout << num1 << " - " << num2 << " = " << num1 - num2;
            break;
            cout << num1 << " * " << num2 << " = " << num1 * num2;
            break;
            cout << num1 << " / " << num2 << " = " << num1 / num2;
            break;
        default:
            (+, -, *, /)
cout << "Error! The operator is not correct";
            break;
Symbol Table:
Token: oper, Type: char, Size: 1, Line Number: 2, Scope: 1
Token: num1, Type: float, Size: 8, Line Number: 3, Scope: 1
Token: num2, Type: float, Size: 8, Line Number: 3, Scope: 1
Success
```

#### • INTERMEDIATE CODE GENERATOR

```
Input
                                                   Output
 1 switch(a)
                                                    2 ----- FINAL THREE ADDRESS CODE -----
 2 {
                                                    4 if(a =0) goto L3
 3
                 case 0: {a=b+c;}
                                                    5 goto L4
                                                    6 L3 : t1=b+c
7 a=t1
                 case 1: {p=q+r;}
 4
                                                    8 goto L5.
  5
                  default:
                                                    9 L4 :
                                                   10 if(a =1) goto L1
  6
                                i=0;
                                                   11 goto L2
                                                   12 L1 : t3=q+r
 7 }
                                                   13 p=t3
                                                   14 goto L5
 8
                                                   15 L2 : i=0
16 L5 : y=0
17 L6 : END OF THREE ADDRESS CODE !!!!!
```

```
----- FINAL THREE ADDRESS CODE ------
if(a =0) goto L5
goto L6
L5 : t1=b+c
a=t1
goto L7
L6 :
if(a =1) goto L3
goto L4
L3 : t3=q+r
t4=t3-y
p=t4
goto L7
L4 :
if(a =2) goto L1
goto L2
L1 : t6=h*u
t7=t6-d
t8=t7+e
g=t8
goto L7
L2 : i=0
L7 : END OF THREE ADDRESS CODE !!!!!
python a.py
*******Quadruple ******
operator operand1
                    operand2
                              NULL
                              NULL
                              NULL
```

#### CODE OPTIMIZER

# 9. CONCLUSIONS

By doing this project, we have gained a better insight into the phases of the compiler. YACC provided us with a better knowledge about bottom-up parser and while performing the different phases of the compiler, our code efficiency in writing code and dealing with complex data structures has significantly improved.

# 10. FURTHER ENHANCEMENTS

Would like to include more ways of Code Optimizer

# 11. REFERENCES/BIBLIOGRAPHY

Lex & Yacc, O'Reilly, John R Levine, Tony Mason, Doug Brown

Our Code can be found on https://github.com/Joshua-Phillips1999/CompilerDesign