Hex							
0	0x0	0000					
1	0x1	0001					
2	0x2	0010					
4	0x4	0100					
8	8x0	1000					

Registers

regis	ters						
n	10	hex	bin	Φ-0	1.0	010	10000
\$0	0	0x00	00000	\$s0	16	0x10	10000
\$at	1	0x01	00001	\$s1	17	0x11	10001
				\$s2	18	0x12	10010
\$v0	2	0x02	00010	\$s3	19	0x13	10011
\$v1	3	0x03	00011	\$s4	20	0x14	10100
\$a0	4	0x04	00100	\$s5	21	0x15	10101
\$a1	5	0x05	00101	\$s6	22	0x16	10110
\$a2	6	0x06	00110	\$s7	23	0x17	10111
\$a3	7	0x07	00111	\$t8	24	0x18	11000
\$t0	8	0x08	01000	\$t9	25	0x19	11001
\$t1	9	0x09	01001	\$k0	26	0x1a	11010
\$t2	10	0x0a	01010	\$k1	27	0x1b	11011
\$t3	11	0x0b	01011				
\$t4	12	0x0c	01100	\$gp	28	0x1c	11100
				\$sp	29	0x1d	11101
\$t5	13	0x0d	01101	\$fp	30	0x1e	11110
\$t6	14	0x0e	01110	\$ra	31	0x1f	11111
\$t7	15	0x0f	01111	ΨΙα		VAII	

- callee saved registers: \$s0-\$s7, \$sp, \$gp, \$fp
 * save parent's value at beginning of function
- caller saved registers: basically all the others
 * save your value before calling subroutine
- general format is to list destination first, then operands

J format (absolute branching)

- cannot change the top 4 bits of PC. (PC[31:28])
- range:
 - * total of 2^{26} instructions or 2^{28} bytes
 - because range is $[0, 2^{26} 1]$
 - * farthest possible next instruction is 2^{26} away (if PC+4 lies at the beginning of a 2^{28} byte boundary)
 - * worst case is you can only jump 1 instruction ahead (if PC+4 lies at the end of a 2^{28} byte boundary)
- conversion:
 - * instruction stores 26 bits
 - * right pad with two 0s to get 28
 - * take the top four bits from current PC to get 32
- mask of top 4 bits: 0xF0000000
- target = (PC AND 0xF0000000) OR (addr << 2)

Relative Branching

- range: $[PC 2^{17}, PC + 2^{17} 4]$
 - * that's in bytes. It's a range of $2^{15} 1$ words
 - * you lose one from the exponent because it's 2's complement

- conversion
 - * take 16 bit offset, zero pad by 2 (multiply by 4)
 - * add to PC+4 (next PC)
- target = (PC + 4) + (addr << 2)
- due to the PC+4 thing, if you want to jump back to the same instruction, the immediate value will be -1

Endianness

Value: 0xA0B0C0D0

index 0 1 2 3

- little 0xD0 0xC0 0xB0 0xA0 big 0xA0 0xB0 0xC0 0xD0
 - * Little Endian puts the least significant (littlest) stuff first
- x86 is little endian, MIPS is big endian
- networking is done in big endian

Two's Complement

- N bits can represent a range $[-2^N, +2^N-1]$
- methods for converting negative values
- method 1:
 - * start with absolute value
 - * flip all bits (bitwise not)
 - * add 1
- method 2:
 - * use N+1 bits $(2^N \text{ is } N+1 \text{ bits})$
 - * start with absolute value x
 - * find $2^N x$
 - * truncate

Shifts

- shift left always fills with 0s
- Logical left shift fills with 0s
- Arithmetic left shift sign-extends
 - * extends based on far left bit (most significant)

Assembler

- Spilling: when a compiler puts a variable in main memory because it's run out of registers
 - * the variable has spilled to RAM
 - * inverse is filling
- Object file sections: header; text; data; relocation information; symbol table; debugging information
 - * Object file is assembled assuming that instructions start at 0x00. (this is corrected later by the linker)
- Global label can be referenced in any file
 - * you must declare it global in the file where it is defined, and declare it global again where it's used
 - * main must be global so the linker can find it
 - * printf is global so you can use it (but you must still declare it as global in that file where you use it)
- local label can be referenced in only the current file
 labels are local by default
- Symbol Table: contains all external references

- * also lists unresolved references (e.g. printf)
- * as far as assembler is concerned, symbol table contains both local and global labels, resolved and unresolved.
- * The final assembled object file only contains global labels
- Relocation Table: contains references to all things that depend on absolute addresses
 - * e.g. all absolute jumps, load address
 - * these must be changed after loading into memory
 - * does not contain addresses of labels