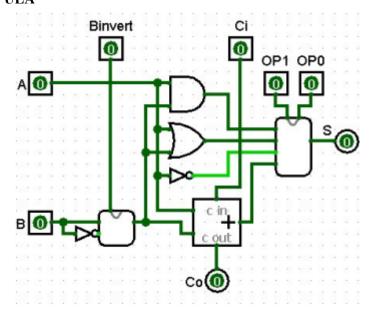
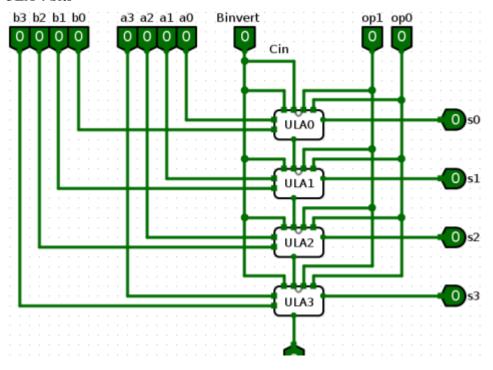
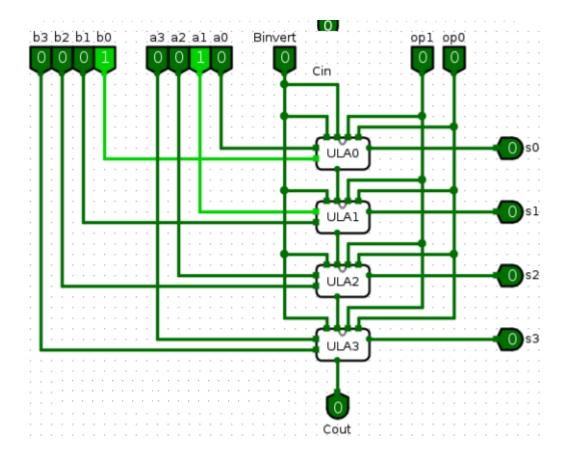
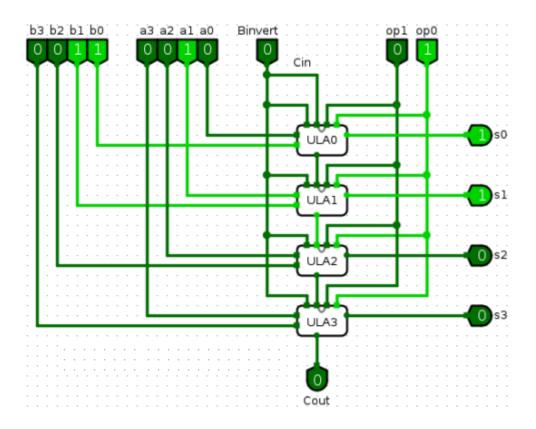
EP02 - Arquitetura de Computadores \parallel ULA

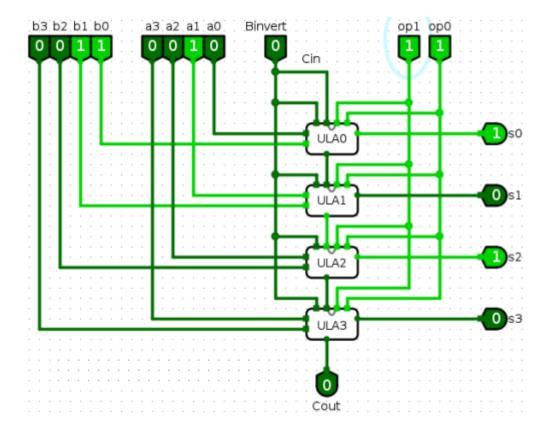


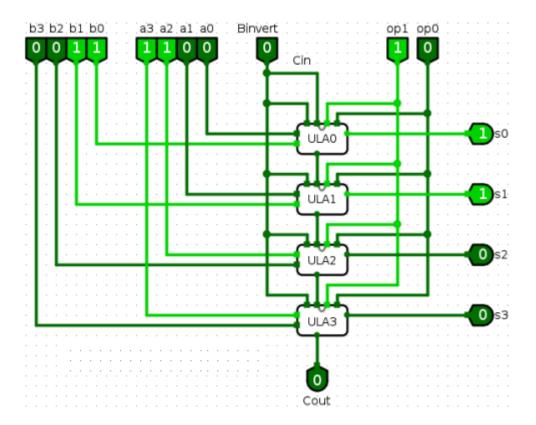
ULA 4 bits











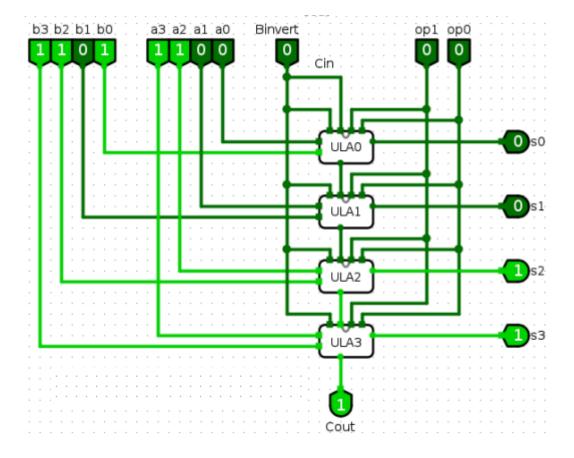


tabela de resultados

Instruções Realizadas	Binário (A, B, Op.code)	Valor em Hexa (0x)	Resultado em Binário
AND (A,B)	0010 0001 00	(0000 1000 0100) = 0x084	0000
OR (A,B)	0010 0011 01	(0000 1000 1101) = 0x08D	0011
SOMA (A,B)	0010 0011 11	(0000 1000 1111) = 0x08F	0101
NOT (A)	1100 0001 10	(0011 0000 0110) = 0x306	0011
AND (B,A)	1100 1101 00	(0011 0011 0100) = 0x334	1100

Parte 2

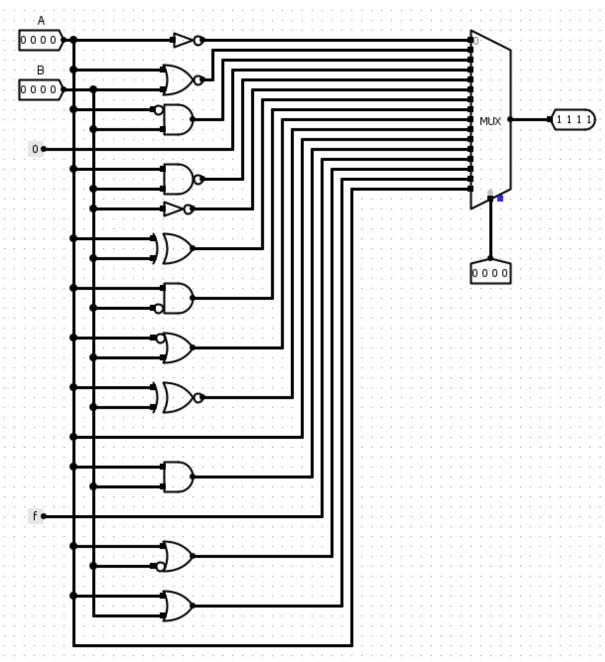


Tabela de resultados

Instruções	Binário	Resultado da Operação
450	0100 0101 0000	1011 = 0xB
CB1	1100 1011 0001	0000 = 0x0
A32	1010 0011 0010	0001 = 0x1
C43	1100 0100 0011	0000 = 0x0
124	0001 0010 0100	1111 = 0xF
785	0111 1000 0101	0111 = 0x7
9B6	1001 1011 0110	0010 = 0x2
CD7	1100 1101 0111	0000 = 0x0
FE8	1111 1110 1000	1110 = 0xE
649	0110 0100 1001	1101 = 0xD
D9A	1101 1001 1010	1001 = 0x9
FCB	1111 1100 1011	1100 = 0xC
63C	0110 0011 1100	1111 = 0xF
98D	1001 1000 1101	1111 = 0xF
76E	0111 0110 1110	0111 = 0x7
23F	0010 0011 1111	0010 = 0x2