SSPCCNS Register SSPC Centrel Register SET - SSPEN - 1								
Hand the property of the prop	MSSP I2C Peripheral SSPCON1 Register	SSP Control Register 1						
With Company of the Company of Table 1962	Bit 0 - WCOL = 0	Bit 1 - SSPOV = 0	Bit 2 = SSPEN = 1	Bit 3 - CKP	Bit 4 = 1	Bit 5 = 1	Bit 6 = 1	Bit 7 = 0
Heave the set of the	No collision	No overflow	Enables Serial port	Enable Clock/ Hold clock Low	I2C Peripheral mode			
Heave the set of the	SSPCON2 Register	SSP Control Register 2						
Second	Bit 0 = 1	Bit 1 - read	Bit 2 = 0	Bit 3	Bit 4	Bit 5	Bit 6	Bit 7 = 1
Mathematical Methods Mathemati	General Call Interrupt Enabled	Status of Acknowledge	х	х	х	х	х	Enable clock stretch
Mathematical Methods Mathemati	SSBCON2 Pogistor	SSB Control Rogistor 2		(0.255)				
Section Sect	Bit 0	Bit 1	Bit 2		Bit 4	Bit 5	Bit 6	Bit 7
Reference of the control of the cont	stats bit	Disable stop interrupt	Start interrupt	Disable buffer overwrite	100ns SDA hold time	Disable bus collison interrupt	Disable address holding	Disable Data holding
Reference of the control of the cont	CCDADD Deviation	MCCD Address Desirbased and						
Table 1988 1989 19			Rit 2	Rit 3 -	Rit 4	Rit 5	Bit 6	Rit 7
Series of the control	x	7 bit address of device = address = 10 he	ex					
Series of the control								
The control of the co	SSPSTAT Register Bit 0 = Read	SSP Status Register	Bit 2 = read	Rit 3 read	Bit 4 read	Rit 5 read	Bit 6 = 1	Rit 7 -
## Part								
Control Cont								
Control Cont								
Heaver the property of the pro	Interrupt on Change							
Heave the second	IOCBP Register	Interrupt on change positive edge Port B	3 Register					
Heave the property of the prop	Bit 0 -	Bit 1 -	Bit 2 =				Bit 6 =	Bit 7 = 0
March Marc	Positive edge Interrupt on change B0	Positive edge Interrupt on change B1	Positive edge Interrupt on change B2	Positive edge Interrupt on change B3	Disable Positive edge Interrupt on change I	Rest of port		
March Marc	IOCNP Register	Interrupt on change positive edge Port F	3 Register					
NAME NAME Section 1982 Section 1982 <th< td=""><td>Bit 0 -</td><td>Bit 1 -</td><td>Bit 2 =</td><td>Bit 3 -</td><td>Bit 4</td><td></td><td>Bit 6 =</td><td>Bit 7 = 0</td></th<>	Bit 0 -	Bit 1 -	Bit 2 =	Bit 3 -	Bit 4		Bit 6 =	Bit 7 = 0
Heater thank the section of the sect	Negative edge Interrupt on change BO	Negative edge Interrupt on change B1	Negative edge Interrupt on change B2	Negative edge Interrupt on change B3	Disable negative edge Interrupt on change	Rest of port		
Heater thank the section of the sect	Dorte							
pick q1 q2 q		All Inputs						
Second	Bit 0	Bit 1						
interchance 4 (a) 4 (b)	Mode Select	Button input 1	Button input 2	Button input 3	Button input 4	Button input 5	Button input 6	NC
interchance 4 (a) 4 (b)	Port R	RO- h3 Innuts	R3- R7 outnuts					
				Bit 3	Bit 4	Bit 5	Bit 6	Bit 7
Belance Bela								LED Indicator -Encoder B Counter Clockwise turn
Beautifue form								
Interest of the part of the pa		C0-C2, C5-C7 Outputs, C3 and C4 Inputs	nu a	Dia 3	Dis 4	Dh. C	Die C	nia 7
Property of the part				I2C - SCL				
Manual Properties Manu						,		
Bigraphis Bigr								
reference of the controlled protocols of the controlled pr	Intcon Register		Dis 2 -	Dia 3	Dis 4	Die F	Dia C -	Bib 7 0
Part		X X	X					
pict of the pict of								
gial globe dead montage particular segments dead montage particular segm	PIE1		81. 8					
Part								
Control Stores	disabled				Bit 4 disabled		Bit 6 = disabled	
Content	disabled	disabled		Enable MSSP Interrupt (I2C)	Bit 4 disabled		Bit 6 = disabled	
Secret S	disabled	disabled		Enable MSSP Interrupt (I2C) Bit 3	Bit 4 disabled		Bit 6 = disabled	
## 1	disabled PIR1	disabled		Enable MSSP Interrupt (I2C) Bit 3	Bit 4 disabled		Bit 6 = disabled	
Count 2	disabled PIR1 Other registers	disabled Peripheral Interrupt request Register	disabled	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag	Bit 4 disabled		Bit 6 = disabled	
B	disabled PIR1 Other registers Count 1 Bit 0 -	disabled Peripheral Interrupt request Register Stores count of inner loop for delay	disabled	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag	disabled	disabled	disabled	disabled
B	disabled PIR1 Other registers Count 1 Bit 0 -	disabled Peripheral Interrupt request Register Stores count of inner loop for delay	disabled	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag	disabled	disabled	disabled	disabled
Samus Save Save contents of status rigidar 255 257 258	disabled PIR1 Other registers Count 1 Bit 0 - Count of inner nested loop	disabled Peripheral Interrupt request Register Stores count of inner loop for delay Bit 1-	disabled 25 Bit 2 =	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag S Bit 3 -	disabled	disabled	disabled	disabled
Bit 2	disabled PIR1 Other registers Count 1 Bit 0 - Count of inner nested loop Count 2	disabled Peripheral Interrupt request Register Stores count of Inner loop for delay Bit 1- Stores count of outer loop for delay	disabled 25 Bit 2 =	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag IS Bit 3 -	disabled	disabled Bit 5 =	disabled Bit 6 =	disabled Bit 7 =0
Bit 2	disabled PIR1 Other registers Count 1 Bit 0 - Count dinner nested loop Count 2 Bit 0 -	disabled Peripheral Interrupt request Register Stores count of Inner loop for delay Bit 1- Stores count of outer loop for delay	disabled 25 Bit 2 =	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag IS Bit 3 -	disabled	disabled Bit 5 =	disabled Bit 6 =	disabled Bit 7 =0
Wave Supermin Su	disabled Other registers Count 1 Bit 0 - Count of other nested loop Count 2 Bit 0 - Count of other nested loop Count of outer nested loop	disabled Peripheral Interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 -	disabled 25 Bit 2 = 25 Bit 2 = 24 Bit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag 5 Bit 3 -	disabled	disabled Bit 5 =	disabled Bit 6 =	disabled Bit 7 =0
Wave Saves contents of working register Set S	disabled PIR1 Other registers Count 1 Bit 0 - Count of nner nested loop Count 2 Bit 0 - Count of outer nested loop Satus Save	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1- Stores count of outer loop for delay Bit 1- Stores count of outer loop for delay Bit 1-	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag 55 Bit 3 - Bit 3 -	disabled Bit 4 Bit 4	disabled Bit 5 = Bit 5 =	disabled Bit 6 =	disabled Bit 7 = 0 Bit 7 = 0
Bit 2	disabled Other registers Count 1 Bit 0 - Count of inner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 -	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1- Stores count of outer loop for delay Bit 1- Stores count of outer loop for delay Bit 1-	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag 55 Bit 3 - Bit 3 -	disabled Bit 4 Bit 4	disabled Bit 5 = Bit 5 =	disabled Bit 6 =	disabled Bit 7 = 0 Bit 7 = 0
Contents of working register	distabled PIRI Other registers Count 1 Bit 0 - Count of inner nested loop Count 2 Bit 0 - Count of count of inner nested loop Status Save Bit 0 - Contents of working register	disabled Peripheral Interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 -	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag 55 Bit 3 - Bit 3 -	disabled Bit 4 Bit 4	disabled Bit 5 = Bit 5 =	disabled Bit 6 =	disabled Bit 7 = 0 Bit 7 = 0
Excoder A Sets enables for interrupts on change for motor A Sets enables for interrupts on change input B Sets enables	disabled PIR1 Other registers Count 1 Bit 0 - Count of niner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1- Stores count of outer loop for delay Bit 1- Saves contents of status register Bit 1- Saves contents of working register Saves contents of working register	disabled	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag SS Bit 3 - Bit 3 - Bit 3 -	disabled Bit 4 Bit 4	disabled Bit 5 = Bit 5 =	disabled Bit 6 = Bit 6 =	disabled Bit 7 = 0 Bit 7 = 0
Bit 0 -	distabled PIRI Other registers Count 1 Bit 0 - Count of inner nested loop Count 2 Bit 0 - Count of count of inner nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 -	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1- Stores count of outer loop for delay Bit 1- Saves contents of status register Bit 1- Saves contents of working register Saves contents of working register	disabled	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag MSSP Interrupt (I2C) Flag S5 Bit 3 - Bit 3 - Bit 3 -	disabled Bit 4 Bit 4	disabled Bit 5 = Bit 5 =	disabled Bit 6 = Bit 6 =	disabled Bit 7 =0 Bit 7 =0
Positive interrupt on change input A Positive interrupt on change input B Positive interrupt on change input B Negative interrupt on change input B X X X X X X X X X	disabled PIR1 Other registers Count 1 Bit 0 - Count of niner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1- Stores count of outer loop for delay Bit 1- Saves contents of status register Bit 1- Saves contents of working register Saves contents of working register	disabled	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag MSSP Interrupt (I2C) Flag S5 Bit 3 - Bit 3 - Bit 3 -	disabled Bit 4 Bit 4	disabled Bit 5 = Bit 5 =	disabled Bit 6 = Bit 6 =	disabled Bit 7 = 0 Bit 7 = 0
Input Old A Stores last input state of encoder A Bit 0 - Bit 1 - Bit 2 = Bit 3 - Bit 4 Bit 5 = Bit 6 - Bit 7 = 0	disabled PIR1 Other registers Count 1 Bit 0 - Count of niner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Encoders Encoders	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1- Stores count of outer loop for delay Bit 1- Saves contents of status register Bit 1- Saves contents of working register Bit 1- Saves contents of working register Bit 1-	25 25 25 26 27 27 27 27 27 27 27	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag SS	disabled Bit 4 Bit 4 Bit 4	disabled Bit 5 = Bit 5 = Bit 5 = Bit 5 =	Bit 6 = Bit 6 = Bit 6 =	disabled Bit 7 = 0 Bit 7 = 0 Bit 7 = 0
Bit 0 -	distabled PIR1 Other registers Count 1 Bit 0 - Count of inner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Faceders of working register Encoders	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 -	disabled 25 Bit 2 = 25	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag S Bit 3 -	Bit 4 Bit 4 Bit 4 Bit 4	disabled Bit 5 = Bit 5 = Bit 5 = Bit 5 =	Bit 6 = Bit 6 = Bit 6 =	Bit 7 = 0 Bit 7 = 0 Bit 7 = 0 Bit 7 = 0
Bit 0 -	distabled PIR1 Other registers Count 1 Bit 0 - Count of inner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Faceders of working register Encoders	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 -	disabled 25 Bit 2 = 25	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag S Bit 3 -	Bit 4 Bit 4 Bit 4 Bit 4	disabled Bit 5 = Bit 5 = Bit 5 = Bit 5 =	Bit 6 = Bit 6 = Bit 6 =	Bit 7 = 0 Bit 7 = 0 Bit 7 = 0 Bit 7 = 0
Encoder 8 Sets enables for interrupts on change for encoder 8 Bit 0	disabled PIR1 Other registers Count 1 BI 0 - Count 2 BI 0 - Count 2 BI 0 - Count of under nested loop Count 2 BI 0 - Count of outer nested loop Status Save BI 0 - Counterin of working register W Save BI 0 - Counterin of working register Encoder A BI 0 - Producter nested loop BI 0 - The nested of working register BI 0 - The nested of working register Encoder A BI 0 - Producter in the nested loop BI 0 - The noter A Input Old A	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1- Stores count of outer loop for delay Bit 1- Saves contents of status register Bit 1- Saves contents of working register Bit 1- Sets enables for interrupts on change fo Bit 1- Negative interrupt on change input A Stores last input state of encoder A	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 = 25 Bit 2 = 35 Bit 2 = 75 Bit 2 =	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag SS Bit 3 -	disabled Bit 4 Bit 4 Bit 4 Bit 4	Bit 5 = Bit 5 = Bit 5 = Bit 5 = X	Bit 6 = Bit 6 = Bit 6 = Bit 6 = X	Bit 7 = 0
Bit 0 -	distabled PIR1 Other registers Count 1 Bit 0 - Count of niner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Fooder A Bit 0 - Positive Interval on change input A Input Old A Bit 0 - Bit 0 - Bit 0 - Positive Interval on change input A	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 -	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 = 25 Bit 2 = 35 Bit 2 = 75 Bit 2 =	Enable MSSP Interrupt (I2C) Bit 3 MSSP Interrupt (I2C) Flag SS Bit 3 -	Bit 4	Bit 5 = Bit 5 = Bit 5 = Bit 5 = X	Bit 6 = X Bit 6 =	Bit 7 = 0
Bit 0 -	distabled PIR1 Other registers Count 1 Bit 0 - Count of niner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Fooder A Bit 0 - Positive Interval on change input A Input Old A Bit 0 - Bit 0 - Bit 0 - Positive Interval on change input A	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 -	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 = 25 Bit 2 = 35 Bit 2 = 75 Bit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag SS Bit 3 -	Bit 4	Bit 5 = Bit 5 = Bit 5 = Bit 5 = X	Bit 6 = X Bit 6 =	Bit 7 = 0
Positive interrupt on change input A Positive interrupt on change input A Positive interrupt on change input B Negative interrupt on change input B X X X X X X X X X	distabled PIR1 Other registers Count 1 Bit 0 - Count of near nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Positive interrupt on change input A Input Old A Bit 0 - Storded Value of last input A	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Negative interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 - stored value of last input B	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 = 25 Bit 2 = 25 Bit 2 = 7 Foreigner A Bit 2 = 7 Foreigner	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag SS Bit 3 -	Bit 4	Bit 5 = Bit 5 = Bit 5 = Bit 5 = X	Bit 6 = X Bit 6 =	Bit 7 = 0
Bit 0 -	distabled PIR1 Other registers Count 1 Bit 0 - Count of nner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Positive interrupt on drange input A Input Old A Bit 0 - Stored value of last input A Encoder B Bit 0 - Bit 0 - Stored value of last input A	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 - stored value of last input B Sets enables for interrupts on change fo Bit 1 -	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 = 25 Bit 2 = 25 Bit 2 = 45 Bit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag S Bit 3 - Bit	Bit 4	BR 5 = BR 5 = BR 5 = X	Bit 6 = X Bit 6 = X Bit 6 = X	Bit 7 = 0 Sit 7 = 0 Sit 7 = 0
Bit 0 -	distabled PIR1 Other registers Count 1 Bit 0 - Count of nner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Positive interrupt on drange input A Input Old A Bit 0 - Stored value of last input A Encoder B Bit 0 - Bit 0 - Stored value of last input A	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 - stored value of last input B Sets enables for interrupts on change fo Bit 1 -	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 = 25 Bit 2 = 25 Bit 2 = 45 Bit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag S Bit 3 - Bit	Bit 4	BR 5 = BR 5 = BR 5 = X	Bit 6 = X Bit 6 = X Bit 6 = X	Bit 7 = 0 Sit 7 = 0 Sit 7 = 0
X X stored value of last input A stored value of last input B x	distabled PIR1 Other registers Count 1 Bit 0 - Count of Inner nested loop Count 2 Bit 0 - Count of Count of Inner nested loop Count 2 Bit 0 - Count of Outer nested loop Status Save Bit 0 - Counters of working register W Save Bit 0 - Counters of working register W Save Bit 0 - Counters of working register Throughous the Counters of Working register Incoder A Bit 0 - Counters of Working register Incoder A Bit 0 - Stored value of Last Input A Encoder B Bit 0 - Positive Interrupt on change input A Encoder B Bit 0 - Positive Interrupt on change input A	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Saves contents of working register Bit 1 - Sate snables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 - stored value of last input B Sets enables for interrupts on change fo Bit 1 - Negative interrupts on change fo Bit 1 - Negative interrupts on change input A	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 = 25 Bit 2 = 25 Bit 2 = 45 Bit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag S Bit 3 - Bit	Bit 4	BR 5 = BR 5 = BR 5 = X	Bit 6 = X Bit 6 = X Bit 6 = X	Bit 7 = 0 Sit 7 = 0 Sit 7 = 0
Command stablyte Stores command for each input	distabled PIRI1 Other registers Count 1 Bit 0 - Count of inner nested loop Count 2 Bit 0 - Count of other nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Paradian of working register Finance of working register Fin	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change for Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 - Sets enables for interrupts on change input A Stores last input state of encoder A Bit 1 - Sets enables for interrupts on change for Bit 1 - Sets enables for interrupts on change for Bit 1 - Sets enables for interrupts on change for Bit 1 - Sets enables for interrupts on change for Bit 1 - Sets enables for interrupts on change for Bit 1 - Sets enables for interrupts on change for Bit 1 - Sets enables for interrupts on change for Bit 1 - Sets enables for interrupts on change for Bit 1 - Sets enables for interrupts on change for Bit 1 - Sets enables for interrupts on change input A	disabled Sit 2 = 25 Sit 2 = 24 Sit 2 = 25 Sit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag SS Bit 3 - Bit 3	Bit 4 X Bit 4 X	Bit 5 = Bit 5 = Bit 5 = X Bit 5 = X Bit 5 = X Bit 5 = X	Bit 6 = Bit 6 = Bit 6 = Bit 6 = X Bit 6 = X Bit 6 = X	Bit 7 = 0 X Bit 7 = 0 X
Bit 0	distabled PIR1 Other registers Count 1 Bit 0 - Count of near nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Fooder A Bit 0 - Positive interrupt on change input A Input Old A Bit 0 - Postorer B Bit 0 - Postorer betterrupt on change input A	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Set enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 - stored value of last input B Sets enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder B Bit 1 - Negative interrupt on change input A Stores last input state of encoder B Bit 1 -	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag SS Interrupt (IZC) Flag SS Int 3 - SIT	Bit 4 X Bit 4 X	BR 5 = X BR 5 = X BR 5 = X BR 5 = X	Bit 6 = Bit 6 = Bit 6 = Bit 6 = X	Bit 7 = 0 A Bit 7 = 0 X Bit 7 = 0 X Bit 7 = 0 X
Input 1 is pressed input 2 is pressed button 3 is pressed button 4 is pressed encoder A clockwise turne encoder A counter clockwise turne Encoder B clockwise turne Encoder B counter clockwise turne Command encoder enable command s for encoder input Bit 0 - Bit 1 - Bit 2 = Bit 3 - Bit 4 Bit 5 - Bit 6 - Bit 7 = 0 X X X X X X Encoder A turned clockwise Encoder A turned clockwise Encoder B turned	distabled PIR1 Other registers Count 1 Bit 0 - Count of Inner nested loop Count 2 Bit 0 - Count of Inner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Counters of working register W Save Bit 0 - Countents of working register The save of the	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 - Stores lost input state of encoder A Bit 1 - Stored value of last input B Sets enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder B Bit 1 - Negative interrupt on change input A Stores last input state of encoder B Bit 1 -	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag SS Interrupt (IZC) Flag SS Int 3 - SIT	Bit 4 X Bit 4 X	BR 5 = X BR 5 = X BR 5 = X BR 5 = X	Bit 6 = Bit 6 = Bit 6 = Bit 6 = X	Bit 7 = 0 X Bit 7 = 0 X Bit 7 = 0 X
Command encoder enable command's for encoder input Bit 0 - Bit 1 - Bit 2 - Bit 3 - Bit 4 - Bit 5 - Bit 6 - Bit 7 = 0 X X X X Encoder A turned dockwise Encoder A turned counter clockwise Encoder B turned counter clockwise Transmission Stores transmitted data Bit 1 - Bit 2 - Bit 3 - Bit 4 - Bit 5 - Bit 6 - Bit 7 = 0	distabled PIR1 Other registers Count 1 Bit 0 - Count of inner nested loop Count 2 Bit 0 - Count of count of inner nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Proceeding the count of working register Innoders Bit 0 - Postitive interrupt on change input A Input Old A Bit 0 - Postore interrupt on change input A Proceeding input A Finceder B Bit 0 - Postore interrupt on change input A Input Old A Bit 0 - Postore interrupt on change input A Input Old A Bit 0 - Postore interrupt on change input A Input Old B Bit 0 - Command interrupt on change input A Command Comman	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 - stored value of last input B Sets enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder B Bit 1 - Negative interrupts on change input A Stores last input state of encoder B Bit 1 - Negative interrupts on change input A Stores last input state of encoder B Bit 1 - Stores last input state of encoder B Bit 2 - Stores command for each input	disabled Bit 2 = 25 Bit 2 = 35 Bit 3 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag SS Bit 3 - Segative interrupt on change input B Bit 3 - X Bit 3 - X Bit 3 - X Bit 3 - X Segative interrupt on change input B Bit 3 - X Segative interrupt on change input B Bit 3 - X Segative interrupt on change input B	Bit 4 Bit 4 Bit 4 Bit 4 Bit 4 Bit 4 X Bit 4 X Bit 4 X Bit 4 X	Bit 5 = X Bit 5 = X Bit 5 = X Bit 5 = X Bit 5 = X Bit 5 = X	Bit 6 = Bit 6 = Bit 6 = Bit 6 = X	Bit 7 = 0 X
Bit 0 - Bit 1 - Bit 2 = Bit 3 - Bit 4 Bit 5 = Bit 7 = 0	distabled PIR1 Other registers Count 1 Bit 0 - Count of Inner nested loop Count 2 Bit 0 - Count of Count of Inner nested loop Status Save Bit 0 - Counter of working register W Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Incoders Incoders of working register Incoders Incoders of working register Incoders Incoders of working register Bit 0 - Counters of working register Input Old A Bit 0 - Stored value of last input A Input Old A Bit 0 - Stored value of last input A Input Old B Bit 0 - Stored value of last input A Input Old B Bit 0 - Command Command Stabyte Bit 0 - Command Command Stabyte Bit 0 - Command Command Stabyte	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Stores contents of status register Bit 1 - Saves contents of status register Bit 1 - Sets enables for interrupts on change fo Bit 1 - Stores is it input state of encoder A Bit 1 - Stores is thingut state of encoder A Bit 1 - Stored value of last input B Sets enables for interrupts on change fo Bit 1 - Stored value of last input B Sets enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores is sit input state of encoder B Bit 1 - X Stores command for each input B	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 = 35 Bit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag St S	Bit 4 Bit 4 Bit 4 Bit 4 Bit 4 X Bit 4 X Bit 4 X Bit 4 X	BR 5 = BR 5 = BR 5 = BR 5 = X	Bit 6 = Bit 6 = Bit 6 = Bit 6 = X	Bit 7 = 0 X
X X X X Encoder A turned doctwise Encoder A turned counter clockwise Encoder B turned clockwise Encoder B turned counter clockwise Incoder 3 turned counter clockwise Incoder B turned clockwise Incoder B turned clockwise Incoder B turned clockwise Bit 0 - Bit 1 - Bit 2 - Bit 3 - Bit 4 Bit 5 - Bit 6 - Bit 7 - Bit 7 -	disabled PIR1 Other registers Count 1 Bit 0 - Count of Inner nested loop Count 2 Bit 0 - Count of Inner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Counters of working register W Save Bit 0 - Countents of working register Fonders of working register Innoders Register Re	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 - Stores last input state of encoder A Bit 1 - Stores last input state of encoder A Bit 1 - Negative interrupt on change fo Bit 1 - Negative interrupt on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder B Bit 1 - Negative interrupt on change input A Stores command for each input Bit 1 - Input 2 is pressed	disabled Bit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 = 35 Bit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag St S	Bit 4 Bit 4 Bit 4 Bit 4 Bit 4 X Bit 4 X Bit 4 X Bit 4 X	BR 5 = BR 5 = BR 5 = BR 5 = X	Bit 6 = Bit 6 = Bit 6 = Bit 6 = X	Bit 7 = 0 X
Transmission Stores transmitted data Bit 0 - Bit 1 - Bit 2 - Bit 3 - Bit 4 - Bit 5 - Bit 6 - Bit 7 = 0	distabled PIR1 Other registers Count 1 Bit 0 - Count of niner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Faceder A Bit 0 - Postitive interrupt on change input A Imput Old A Bit 0 - Stored value of last input A Faceder B Bit 0 - Postitive interrupt on change input A Faceder B Bit 0 - Postitive interrupt on change input A Faceder B Bit 0 - Tompet Old A Bit 0 - Tompet Old B Bit 0 - Stored value of last input A Command Command Command Command Command Co	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Saves contents of working register Bit 1 - Negative interrupts on change for Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 - Stores last input state of encoder A Bit 1 - Negative interrupt on change input A Stores last input state of encoder B Bit 1 - Stores last input state of encoder B Bit 1 - Stores last input state of encoder B Bit 1 - Stores sommand for each input Bit 1 - Input 2 is pressed enable command for each input enable command for encoder input enable command for encoder input enable command for encoder input	disabled Bit 2 = 25 Bit 2 = 35 Bit 3 =	Enable MSSP Interrupt (IZC) Bit 3 Strand Str	Bit 4 Bit 4 Bit 4 Bit 4 Bit 4 Bit 4 X Bit 4	Bit 5 = Bit 5 = Bit 5 = Bit 5 = X Bit	Bit 6 = Bit 6 = Bit 6 = Bit 6 = X	Bit 7 = 0 X
Bit 0 - Bit 1 - Bit 2 = Bit 3 - Bit 4 Bit 5 - Bit 6 - Bit 7 = 0	distabled PIR1 Other registers Count 1 Bit 0 - Count of niner nested loop Count 2 Bit 0 - Count of outer nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Faceder A Bit 0 - Postitive interrupt on change input A Imput Old A Bit 0 - Stored value of last input A Faceder B Bit 0 - Postitive interrupt on change input A Faceder B Bit 0 - Postitive interrupt on change input A Faceder B Bit 0 - Tompet Old A Bit 0 - Tompet Old B Bit 0 - Stored value of last input A Command Command Command Command Command Co	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 - Stores last input state of encoder A Bit 1 - Stores last input state of encoder A Bit 1 - Negative interrupt on change input A Stores last input state of encoder B Bit 1 - Negative interrupt on change input A Stores last input state of encoder B Bit 1 - Negative interrupt on change input A Stores command for each input B Bit 1 - Input 2 is pressed	disabled Sit 2 = 25 Bit 2 = 24 Bit 2 = 25 Bit 2 = 35 Bit 2 =	Enable MSSP Interrupt (IZC) Bit 3 MSSP Interrupt (IZC) Flag St	Bit 4 Bit 4 Bit 4 Bit 4 Bit 4 X Bit 4 Bit 4	Bit 5 = X Bit 5 = Bit 5	Bit 6 = Bit 6 = Bit 6 = Bit 6 = X Bit 6 = Bit	Bit 7 = 0 X Bit 7 = 0 Bit 7 = 0 X Bit 7 = 0 Bit 7 = 0
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	disabled PIR1 Other registers Count 1 Bit 0 - Count 2 Bit 0 - Count of near nested loop Count 2 Bit 0 - Count of user nested loop Status Save Bit 0 - Contents of working register W Save Bit 0 - Contents of working register Freeder A Bit 0 - Input Old A Bit 0 - Recommand Command stablyte Bit 0 - Input 1 is pressed Command Command stablyte Bit 0 - Input 1 is pressed Command Com	disabled Peripheral interrupt request Register Stores count of inner loop for delay Bit 1 - Stores count of outer loop for delay Bit 1 - Saves contents of status register Bit 1 - Saves contents of working register Bit 1 - Sets enables for interrupts on change fo Bit 1 - Negative interrupt on change input A Stores last input state of encoder A Bit 1 - Negative interrupt on change input A Sets enables for interrupts on change input A Stores last input state of encoder A Bit 1 - Negative interrupt on change input A Stores last input state of encoder B Bit 1 - Input 2 is pressed enable command for each input Bit 1 - Input 2 is pressed enable command for encoder input Bit 1 - Input 2 is pressed	disabled Bit 2 = 25 Bit 2 = 35 Bit 3 =	Enable MSSP Interrupt (IZC) Bit 3 Strong St	Bit 4 X Bit 4 Bit 4 X Bit 4 Bit 4 X Bit 4 A Bit 4 Bit	Bit 5 = Bit 5 = Bit 5 = Bit 5 = X Bit 5 = A Bit 5 = Bit	Bit 6 = X Bit 6 = Encoder B clockwise turne	Bit 7 = 0 X Bit 7 = 0 Bit 7 = 0 X Bit 7 = 0 X Bit 7 = 0 Encoder B counter clockwise turne