	60urse: CPB 435 - Proto Exping Techniques		
	Group No: 5		
	Group Specification: 7 VDC		
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DESIGN OF A TWO-STAGE CLASS A - CAPACITOR COUPLED BABIO FREQUENCY AMPLIFIER

1 Design Assumptions

- Voltage Across terminal collector VCE must be half of the rail Voltage Vcc
- Voltage Across emitter VE must be 10% of rail Voltage

VE = 10% of Vcc

- Voltage across the collector resistor Vac is
40% of 1211 Voltage

- The Transistor is Bished in a linear region

@ Component Calculations

(i) Vec = 7v

110 VCE = 0.5 VCC = 3.5 V VCEI = VCE2 = 3.5 V

(11) VAC = 0.4 VCC = 0.4 x7 = 0.74 2.8 N

(IN VREI = VRE2 = 0.1 Vcc = 0.7 V4

(N) Re = 7-2.9 = 0.42 K I - Vec - VRC 10 m A Re = 0.42 K I - Vec - VRC Ic

(V) RE = VE = VE = 9

HFE = 100 To = HEE X IB TB = 10 x 10 3 = 0.1 mA TR = 0.1 : RE = 0.7 = 69.30 A (10+0.1)x10-3 (NII) Bc. VABIZ = VBE + VBEI VBF (&1100) = 0.7V VRB12 = 0.7+0.7 = 1.4V = VRB22 (Vii) IAB12 = IRB22 = 10 IB = 10 x 0.1 = 1 m A. (ix) RB12 = VAB12 = 1.4KD Bo22 = 1.4KA (20) VR821 = VBBII = 7-1.4 = 5.6 V (x) IRBH = IB + IRBH2 = +0 + 0.1 + 1 = 1.1m A 20) RB11 = RB21 = 5.6/1.1 = 5.09 K 2

GCA CEI, CEZ, Cia, Cout

CEI = 1

Xc = 1/2 TTfc = RE/10 = 69.30 = 6.930 \$

9 = 30 × 106 H2

Ce1 = 1 2 TX 30x106 x 6.930 = 0.765 n F

CBI = CE2 = 0.765 = F

Vr = 26mV

Te = VT/= = 26×10-3 = 2.574 \(\Omega\)

2:0 = (5.09x103/11.4x103/169.30) x100 x 2.574

Zin = 16.77 X103 D

Cin1 = Cin2 = 2 TT X 30 X 10 × 16.77 X 103 Cin1 = Cin2 = 0.3163PF

= 0.01263nF 2TT x 80 x 106 x 0. 42 x 103



