

HST Project S5

CircuitVoyager Pre1



Submitted by:
Joel Schaller

Lecturer: Enrico Malacarne

22.09.2023

Abstract

Konzept (gestalterisch)

Methode

Wichtigste Ergebnisse

Contents

1	Introduction	1
1.1	"Lastenheft"	1
1.2	Mindmap	2
2	Main Body	3
2.1	"Pflichtenheft"	3
2.2	Extension PCB	4
2.3	QSPI Flash Implementation	14
3	Conclusion	15
3.1	Project Start	15
3.2	Time Plan	15
3.3	HW development	15
4	Appendix	16
4.1	Journal	16
4.2	Project Agreement	17
4.3	Interview 1	18
4.4	GANTT Chart	19
4.5	Weekly plans	20
4.6	Extension PCB Schematics	22
4.7	Extension PCB Manufacture 11.23 BOM	23
5	Credits	24
	Bibliography	25
	List of Figures	26
	List of Tables	27
	Listings	28
	Acronyms	29

1 Introduction

The goal of this project is to develop a tiny extension Board for the STM32H747i-Disco Board, to allow it to act as a DMM. Additionally, a SW, that measures the DMM Values and displays them on the Touch Display. If there's more time I could extend the Project with Measurement Logging via a SD-Card or over USB to a Desktop application.

I want to learn how to implement high speed protocols such as Mipi DSI or QPSI. Later in the last year of my apprenticeship I'd like to develop a whole DMM on my own, but with a different approach as standard ones like these from Fluke. For example, I want to make the DMM rechargeable and modernize it a bit.

To realize this project I'm going to use the following tools: Altium Designer, STM32-CubeIDE, LaTeX, TouchGFX.

Also I won't make a diary, because it's easier for me to write my findings down sorted by theme rather than date. But to keep the chronological order of the stuff I've done, there's a Journal in Chapter: [4.1].

1.1 "Lastenheft"

This is a request from the imaginary customer, I'm making this project for:

I need a prototype for a DMM, that can measure voltage, current and continuity. The DMM should have a touchscreen that displays said values. The UI should be intuitive, so everyone who's ever used a DMM can use it to. Normal features as hold, minmax should be available and it would be great if you could fit in a power mode, where the DMM uses the voltage and current measurement to calculate the drawn power from the measured device. Because this project will only be used for the proof of concept, the DMM doesn't have to support mains voltage and we also won't need any safety circuits, AC measuring or negative voltages / currents. It's mainly about the SW. So you can also use DevBoards if there are any available.

1.2 Mindmap

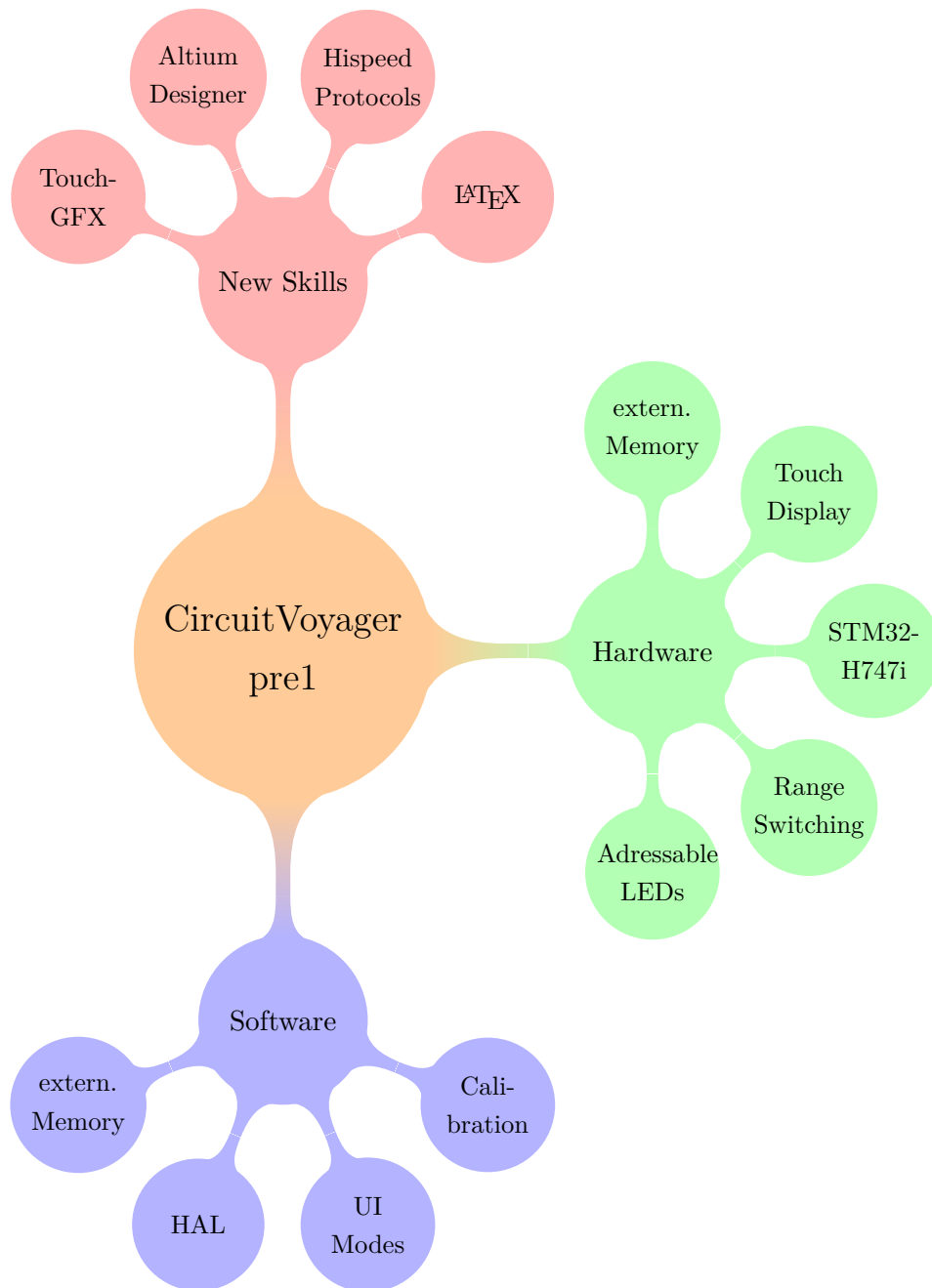


Figure 1.1: Project Mindmap

2 Main Body

2.1 "Pflichtenheft"

Cost

I've already bought two DevBoards one of them stays at TBZ and the other is at home. One of these boards was paid by Mr. Malacarne. Further expenses from the PCB will be paid by me and shouldn't exceed about 50 CHF, as the HW isn't that complicated.

Time

The most time of the project I will work at home because it's a rather big project to execute in one semester. I will also have much time in the fall holidays to work on it. The project will approximately take 100h to complete. Also the more detailed timeplan is in chapter: [4.4]

Tools

To realize this project I will mainly use, the SW STM32CubeIDE with HAL and Altium Designer. The documentation is written in LaTeX in VSCode. And I'm planning to order the PCB on JLCPCB and I will populate and reflow the PCB at ETHZ, where I'm also allowed to use the measurement equipment for the HW tests.

Technical Details

value	min.	typ.	max.	unit	description
supply voltage		5		V	over USB
curent to measure	0		1	A	
voltage to measure	0		10	V	

Table 2.1: Technical Details

2.2 Extension PCB

2.2.1 STMod+

Interface from DevBoard to Extension PCB.

- 5V Supply
- SPI
- I₂C
- ADC
- Interrupt
- PWM
- GPIOs

I will use the STMOD#14 connection that was intended to use as PWM, as a second ADC input. To measure current and voltage at the same time to later show the power consumption of the DUT.

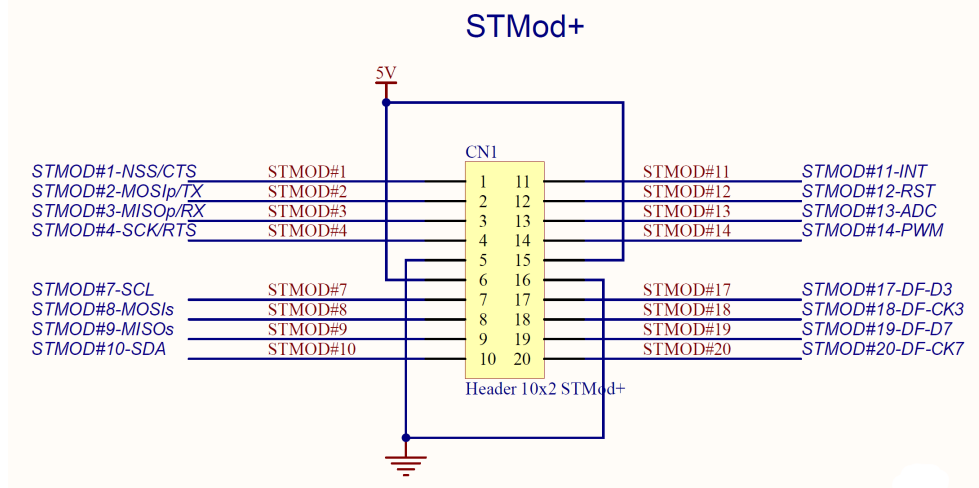


Figure 2.1: STMod+ Interface

2.2.2 Hardware concept

After some thoughts I came up with the following HW concept.

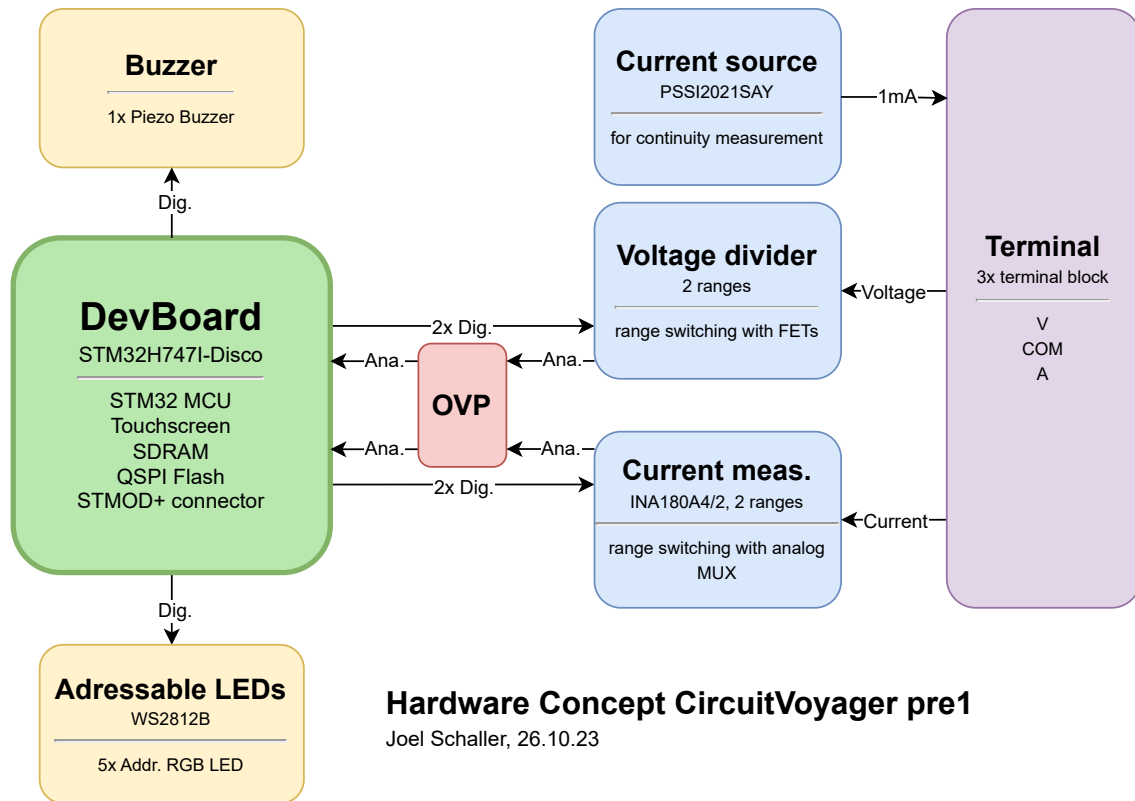


Figure 2.2: Extension PCB HW concept

Voltage measurement

To measure voltage, the DUT should be connected to the terminals V and COM. COM is connected internally to device GND. The V terminal is connected to the Voltage divider block. This block divides the input voltage down, so the ADC in the MCU doesn't overshoot. There are 2 ranges to measure voltage, which can be chosen by setting 2 digital output, that go from the MCU to the voltage divider. There's also an OVP, to protect the MCU from voltages higher than 3.3V. [1]

Current measurement

To measure current, the DUT should be connected to the terminals A and COM. COM is connected internally to device GND. The A terminal is connected to current measurement block. This block measures the current, by letting the current flow through one of two shunt resistors. The DMM can choose which resistor and

therefore range should be selected with the 2 digital Output that are connected from the MCU to the current measurement block. The voltage over the selected shunt is then amplified, by a current amplifier IC and then measured by the MCUs ADC. There's also an OVP, to protect the MCU from voltages higher than 3.3V. [1]

Continuity measurement

To measure continuity, both the voltage divider and the current source is used. The continuity between the V and COM pins is measured. For this a constant current produced by the current source is flowing out of the V terminal. Simultaneously the voltage across those terminals is measured and the resistance / continuity can be evaluated. If continuity is detected, either the buzzer beeps or the LEDs blink. [1]

2.2.3 Schematic

The schematic took me a bit longer than usual, because it's my first whole HW project in Altium before I used KiCAD and Altium is a lot more features and in my opinion is harder to learn. The schematic is in the Appendix 4.6.

Buzzer Circuit

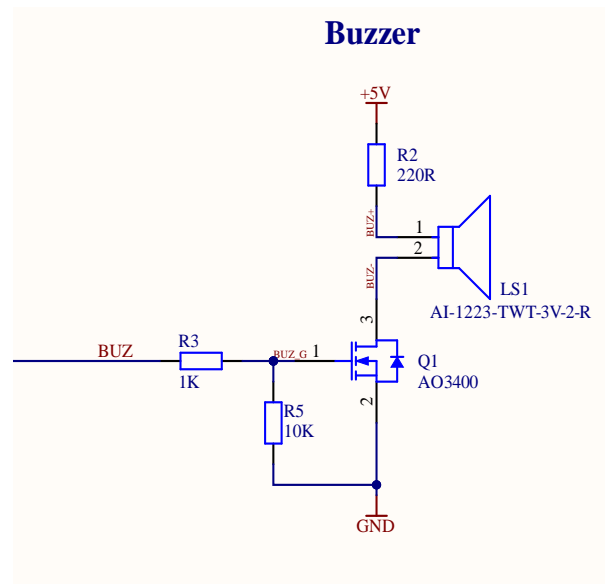


Figure 2.3: Buzzer Circuit

This is an active buzzer. If the BUZ line is pulled high by the MCU, the MOSFET starts to conduct and the buzzer starts beeping. This circuit will be used to give an acoustic feedback to the user, if for example a continuity has been detected.

The resistors R3 and R5 build a voltage divider with a ratio of 1/10. This has the advantage, that the gate capacitance of Q1 is charged with a limited current and if nothing's connected to the BUZ net the MOSFET turns the buzzer off and the whole machine isn't in an indeterminate state.

The resistor R2 limit the current flowing through LS1. As LS1 is rated for 30mA at 3V.

$$R_2 = \frac{U_{VCC} - U_{LS1}}{I_{LS1}} = \frac{5V - 3V}{30mA} = \underline{\underline{66.\bar{6}\Omega}}$$

$$P_{R2} = I^2 \cdot R = (30mA)^2 \cdot 66.\bar{6}\Omega = \underline{\underline{60mW}}$$

Finally, I've chosen a 220Ω resistor for R2. With this value the sound should be enough loud, that the user hears it. And the power loss of the resistor will be smaller. That means it should be perfectly fine to use a 0402 resistor that is rated for 62.5mW.

Adressable LEDs Circuit

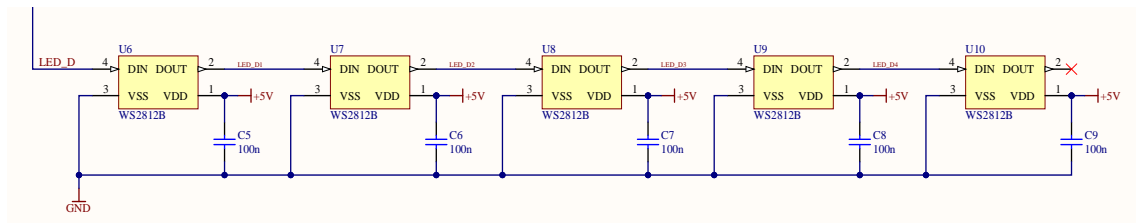


Figure 2.4: Adressable LEDs Circuit

An other option to show if continuity has been detected are these LEDs. The advantage of them is, that they only need one data connection and already include their logic and driving circuits. I've equipped the with one bulk C each, because they're integrated components and they're driven over the 5V rail, which they're specified for. But if they're driven by 5V they theoretically detect all voltages over 3.5V as a logical high. But the MCU only output 3.3V. I've used those LEDs much in past projects and this was never a problem, so I'm assuming that it should also work this time.

Overvoltage Protection Circuit

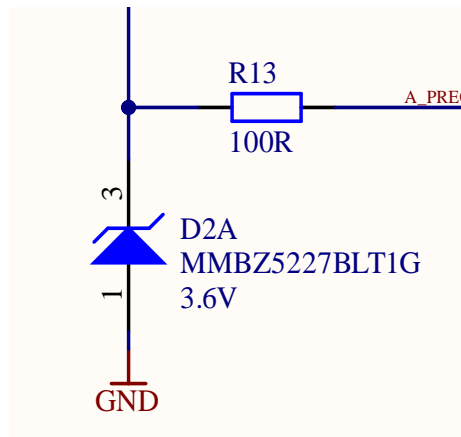


Figure 2.5: Overvoltage Protection Circuit

My Problem is, that the voltages coming from the Extension PCB, can't exceed 4V according to the MCUs datasheet. But because this PCB is powered by the 5V rail, theoretically these voltages could damage the MCU. So the goal was to develop a circuit which protects the ADC inputs. The first idea that came to my mind was to use varistors. I've already used the once in a project, but after reading some application notes on this topic, I decided to use a zenerdiode for the OVP. [2]

The zenerdiode I've used has a nominal zenervoltage of 3.6V (max. 3.78V) at 20mA. Together with R13 this should result with a maximum voltage of 3.78V at the ADC inputs. As long as the PREOV voltage doesn't exceed 5.6V.

$$U_{PREOV(MAX.)} = R_{13} \cdot I_Z + U_Z = 100\Omega \cdot 20mA + 3.6V = \underline{\underline{5.6V}}$$

$$P_{R13} = R \cdot I^2 = 100\Omega \cdot (20mA)^2 = \underline{\underline{40mW}}$$

Voltage Divider Circuit

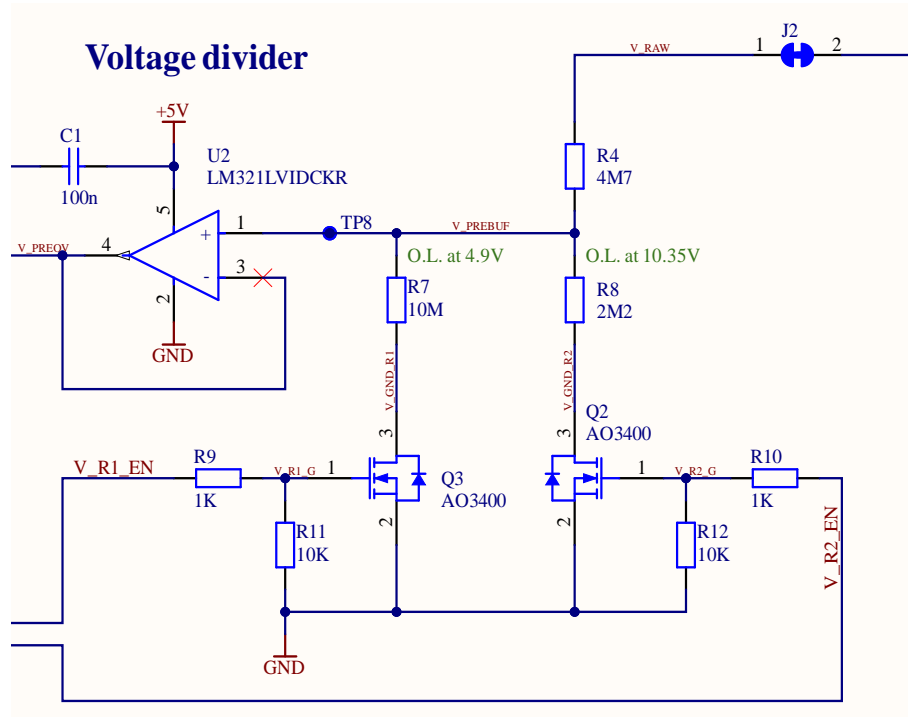


Figure 2.6: Voltage Divider Circuit

The voltage divider is used to measure voltages over the V terminal. This circuit is one approach to range switching, which is described more detailed in the following chapter [Write more detailed text about how to execute range switching and what are the pros and cons. Minimum 3 options]. This voltage divider features 2 ranges and I came up with this idea by myself. The ranges are calculated as following:

$$U_{OL(R1)} = \frac{(R_4 + R_7) \cdot U_{OL(MCU)}}{R_7} = \frac{(4.7M\Omega + 10M\Omega) \cdot 3.3V}{10M\Omega} = \underline{\underline{4.851V}}$$

$$U_{OL(R2)} = \frac{(R_4 + R_8) \cdot U_{OL(MCU)}}{R_8} = \frac{(4.7M\Omega + 2.2M\Omega) \cdot 3.3V}{2.2M\Omega} = \underline{\underline{10.35V}}$$

This divided voltage is then fed into an impedance converter which guarantees that the voltage divider isn't manipulated by the ADC internal resistance and also helps on the DevBoards analog paths, because they're very long and therefore vulnerable to electromagnetic fields.

Text to put into ranges part: This range switching part has the advantage of using many ranges at once, as for example with 2 range switches can use 3 different ranges and with 3 range switches can use 7 ranges. On the other hand it has the disadvantage of only being able to divide

the input voltage and not being able to amplify. Additionally, the internal resistance is always changing and therefore impacting the DUT. I wouldn't recommend this circuit because of said factors and only switch on the measured side. For example use different Amps / Divs to create the right voltage ranges and only switch the high Z path, that doesn't have an impact on the DUT.

Current Source Circuit

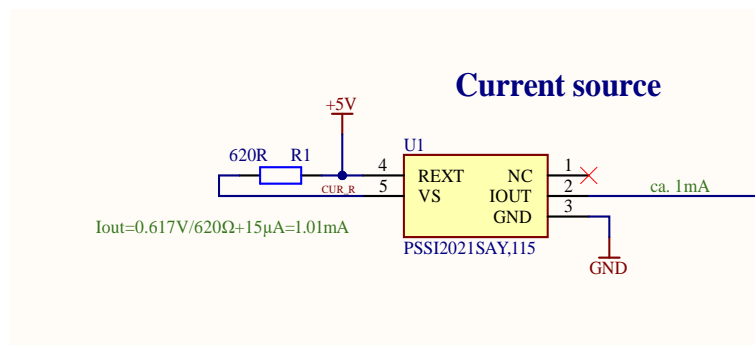


Figure 2.7: Current Source Circuit

The current source circuit will be used to measure continuity and resistance together with the voltage measuring function. A constant current will be induced to the DUT and because the voltage over the DUT can be measured, the resistance can be calculated. The constant current can be calculated as following:

$$I_{OUT} = \frac{U_{VS}}{R_1} + 15\mu A = \frac{0.617V}{620\Omega} + 15\mu A = \underline{\underline{1.01mA}}$$

But this doesn't matter that much, because the DMM will be calibrated using the solderbridge J1.

Current Measuring Circuit

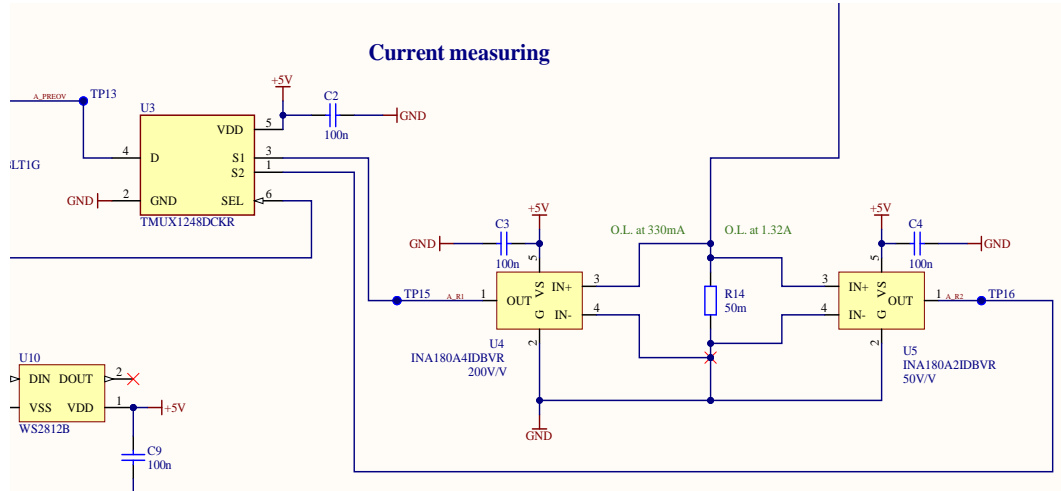


Figure 2.8: Current Measuring Circuit

The current measuring circuit is used to measure currents with the A terminal. This circuit is one approach to range switching, which is described more detailed in the following chapter [Write more detailed text about how to execute range switching and what are the pros and cons. Minimum 3 options]. This circuit features 2 ranges and I came up with this idea, while reading some articles on DMMs [3] [4]. The ranges are calculated as following:

$$I_{OL(R1)} = \frac{U_{OL(MCU)}}{R_{14} \cdot G_{U4}} = \frac{3.3V}{50m\Omega \cdot 200\frac{V}{V}} = \underline{\underline{330mA}}$$

$$I_{OL(R2)} = \frac{U_{OL(MCU)}}{R_{14} \cdot G_{U5}} = \frac{3.3V}{50m\Omega \cdot 50\frac{V}{V}} = \underline{\underline{1.32A}}$$

These voltages are then fed into an analog MUX, which switches the processed voltages to the ADC. This has the advantage, that the DMM doesn't manipulate the actual path, where the current is flowing through and therefore not influencing the DUT.

The power rating of R14 is 125mW, while the maximal power loss of R14 at 1.32A is 87.12mW.

$$P_{R14} = I^2 \cdot R = (1.32A)^2 \cdot 50m\Omega = \underline{\underline{87.12mW}}$$

2.2.4 PCB

Layout

The PCB layout was also made in Altium Designer. This was therefore also my first layout in Altium Designer. The only problem I had, were the vias and improvised them by setting their parameters manually.

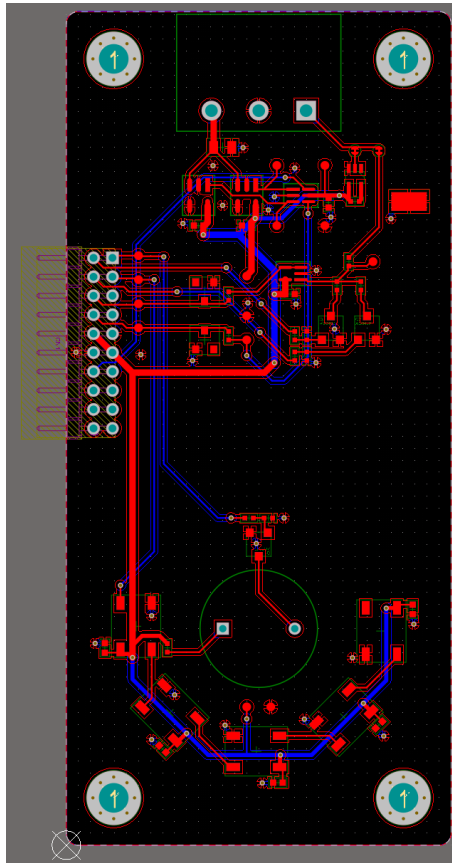


Figure 2.9: Extension PCB Layout

Later I've generated the output files (Gerber files, BOM, assembly drawing) and ordered the PCB on JLCPCB with 2 layers, tented vias, removed order number and blue solder mask. The BOM is in the appendix chapter: 4.7. The components were sourced using DigiKey and ETH SPH.

Then I've assembled the PCB by hand, which wasn't a problem at all.

Assembly

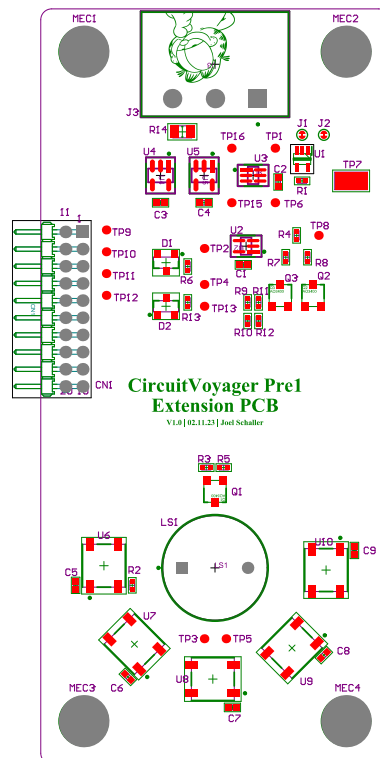


Figure 2.10: Extension PCB Assembly Drawing

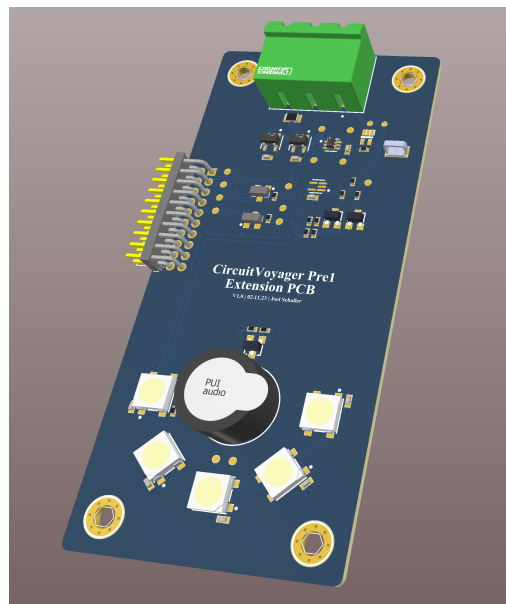


Figure 2.11: Extension PCB

PCB Bild durch echtes bild ersetzen.

Commissioning

2.3 QSPI Flash Implementation

3 Conclusion

3.1 Project Start

I've noticed that it was quite hard for me to start with the project. There are so much topics to imply, that it can get overwhelming and very emotional. I've also underestimated the time needed to set up the documentation LaTeX files and the whole planning. It can be demotivating if you're spending a lot of time and in the end didn't really start with the project. But later I was able to start the project without much trouble.

3.2 Time Plan

3.3 HW development

I've learned much in this project part. Mainly this was Altium Designer, as this was my first complete project I've realised in Altium Designer. This also led to some not nicely solved solution. All components for example have their own properties and this leads to a unreadable BOM.

But in the end I've also noticed, that the dataflow (that usually should go from left to right) goes in the wrong direction. This had already started in the HW-Chart and therefore also ended up in the schematic.

I've also used Draw.io one of the first times and by now it looks very promising. It's much easier and more straight forward than MS Visio. Everything just works as intended.

Gesamtschau, Arbeitsergebnis, Gesamturteil, evtl. Ausblick, was ich lernen konnte

4 Appendix

4.1 Journal

Date	Location	Duration	Activity
01.09.2023	TBZ	1.5h	Selected and bought DevBoard
08.09.2023	TBZ	2h	Tested DevBoard with demos
08.09.2023	TBZ	0.5h	Noted first ideas for DMM
15.09.2023	TBZ	1.5h	Written and signed Project Agreement [4.2]
21.09.2023	Home	3h	Created documentation template
22.09.2023	TBZ	2h	Started writing Journal [4.1]
24.09.2023	Home	1.5h	Made GANTT chart [4.4]
27.09.2023	Home	2h	Written detailed planning and introduction
29.09.2023	TBZ	1.5h	Added mindmap, Lasten-, Pflichtenheft
06.10.2023	TBZ	1h	Started with block diagram (extension PCB)
20.10.2023	ETH	0.5h	Started Altium Project, Schematic template
23.10.2023	ETH	3h	HW Concept / documentation
23.10.2023	ETH	3h	Start schematic / documentation
25.10.2023	Home	2h	Schematic: Current Src, Volt div
26.10.2023	ETH	1.5h	Schematic: Current meas, ERC
26.10.2023	Home	1h	Schematic: Cleanup, Comments, DS Saves
26.10.2023	Home	1h	Documentation & prepared Interview 1
27.10.2023	TBZ	1.5h	Documentation: Started with schematic part
28.10.2023	Home	2h	Documentation: schematic parts
29.10.2023	Home	1h	Docu: schematic done, resources cleanup
29.10.2023	Home	0.5h	Layout: Outline, Placement start
31.10.2023	Home	1h	Layout: Placement Done
01.11.2023	Home	1.5h	Layout: Routing Done
02.11.2023	Home	1h	Layout: Cleanup & Export
03.11.2023	TBZ	-	Interview 1: signed by mala
03.11.2023	TBZ	1h	Pepered BOM for supplying
04.11.2023	Home	1h	Documentation: PCB, Reflection

Table 4.1: Project Journal

4.2 Project Agreement

Information HST: BÜP	TBZ/EE / 2337.00 jschaller Sem: 5	HST TBZ Technische Berufsschule Zürich Abteilung Elektro/Elektronik
1_Project_Agreement.docx		
<h3>1 Projektvereinbarung</h3>		
Verfasser/innen: Joel Schaller Titel: CircuitVoyager pre1		Klasse: BEN21
<div style="border: 1px solid black; padding: 5px;"> <p>1. Thema (Hintergrund, Überblick, gegenwärtiger Wissensstand)</p> <p>Develop a tiny extension Board for the STM32H747i-Disco Board, to allow it to act as a DMM. Additionally, a software, that measures the DMM Values and displays them on the Touch Display. If there's more time I could extend the Project with Measurement Logging via a SD-Card or over USB to a Desktop application.</p> </div> <div style="border: 1px solid black; padding: 5px;"> <p>2. Eigene Fragestellung / Untersuchungsgegenstand</p> <p>2.1 Eigene Fragestellung (Leitfrage) How to implement the following functions / protocols? (QSPI Flash, SDRAM, TouchGFX, Mipi DSI) and if the time is sufficient: (FAT with SDcards, Bootloaders)</p> <p>2.2 Hypothese (Vermutung über das Ergebnis) I want to learn, working with High Speed MCUs and implement such protocols.</p> <p>2.3 Methoden und Vorgehen (mindestens 2 Methoden müssen angewendet werden) HW-Dev (Altium), SW-Dev (STM32Cube with HAL), Documentation in LaTeX</p> <p>2.4 Hilfsmittel Internet, literature</p> <p>2.5 Kontaktpersonen, Informationsstellen, Institutionen Teachers, Instructor at ETH, Dad</p> </div> <div style="border: 1px solid black; padding: 5px;"> <p>3. Persönlicher Bezug / Motivation</p> <p>In the next 2 Years I want to develop my own DMM, because I think there's much to improve with standard DMMs as Fluke. For Example: Touch Display, Rechargeable Battery...</p> </div> <div style="border: 1px solid black; padding: 5px;"> <p>4. Bewertungsform</p> <p>This Project will only be done by me. Time: about 28 lesson and unknown time at home. Project delivery on: 12.01.2023</p> </div> <div style="border: 1px solid black; padding: 5px;"> <p>5. Besprechungstermine mit Lehrperson (vorgeschrieben sind zwei Besprechungen)</p> <p>Termin 1: 27.10.2023 Termin 2: 01.12.2023</p> </div> <div style="border: 1px solid black; padding: 5px; margin-top: 10px;"> <p>Datum: 15.09.23 Die Lernenden: </p> <p>Datum: 15.9.23 Die Lehrperson: </p> </div> <div style="border: 1px solid black; padding: 5px; margin-top: 5px; font-size: x-small;"> <p>DMM = Digital Multimeter TouchGFX = Graphical Designer for Embedded Touch Displays</p> </div>		
BEN21	Seite 1 (1)	1_Project_Agreement.docx

Figure 4.1: Project Agreement

4.3 Interview 1


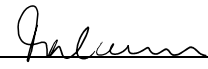

Projektbewertung HST / BÜP: Projektarbeit 2023	MAL / 2109.00 -- Sem: 5	HST / BÜP 96_Interview_1.docx
 TBZ Technische Berufsschule Zürich Abteilung Elektro/Elektronik		
<h3>Protokoll der Zwischenbesprechung</h3>		
Zwischenbesprechung 1		Datum: 27.10.23
Name: Joel Schaller		Klasse: BEN21a
Titel der Arbeit: CircuitVoyager pre1		
Dieses Formular ist soweit möglich ausgefüllt an die Zwischenbesprechung mitzubringen. Weiter sind möglichst alle Unterlagen mitzubringen, vor allem das Geschriebene (Arbeit, Dokumentation, aktuelles Projektjournal, Zeitplan, Notizen, Korrespondenzen) sowie die wichtigsten Informationsmaterialien (Bücher, Datenblätter etc.)		
Stand der Arbeit / Zeitplan		
<ul style="list-style-type: none"> - Viel Doku geschrieben (17 Seiten) mit ganzem LaTeX aufgesetzt. (sehr zeitaufwändig gewesen) - Projektplanung inklusive GANTT, Lasten-/Pflichtenheft. - DevBoard ausgetestet mit Demo Code. - HW Block Diagramm / Konzept erstellt. - Schema aufgesetzt und fertiggezeichnet. 		
Nächste Schritte		
<ul style="list-style-type: none"> - Layout für Extension PCB erstellen. - Extension PCB bestücken. - SDRAM und QSPI implementieren. 		
Änderung der Projektvereinbarung (Bestätigung und vorliegend Begründung)		
Keine Änderungen, auch wenn ich in meinem Zeitplan sehr hintendrin bin. Doch ich denke ich kann besonders in der Software gut wieder aufholen und werde Alles geben, wieder „on time“ zu sein.		
Probleme, Schwierigkeiten fachlicher Art		
eingetretene und noch zu erwartende Probleme, Massnahmen, Lösungen		
Direkte Probleme hatte ich bisher nicht. Ich kam immer gut an die Infos, die ich brauchte. Jedoch bin ich im Zeitplan etwas hintendrin, wie oben beschrieben.		
Zürich, 03.11.23	Unterschrift Lehrperson 	
Zürich, 27.10.23	Unterschrift Lernende 	
96_Interview_1.docx	1(1)	03.11.2023 / jschaller

Figure 4.2: Interview 1

4.4 GANTT Chart

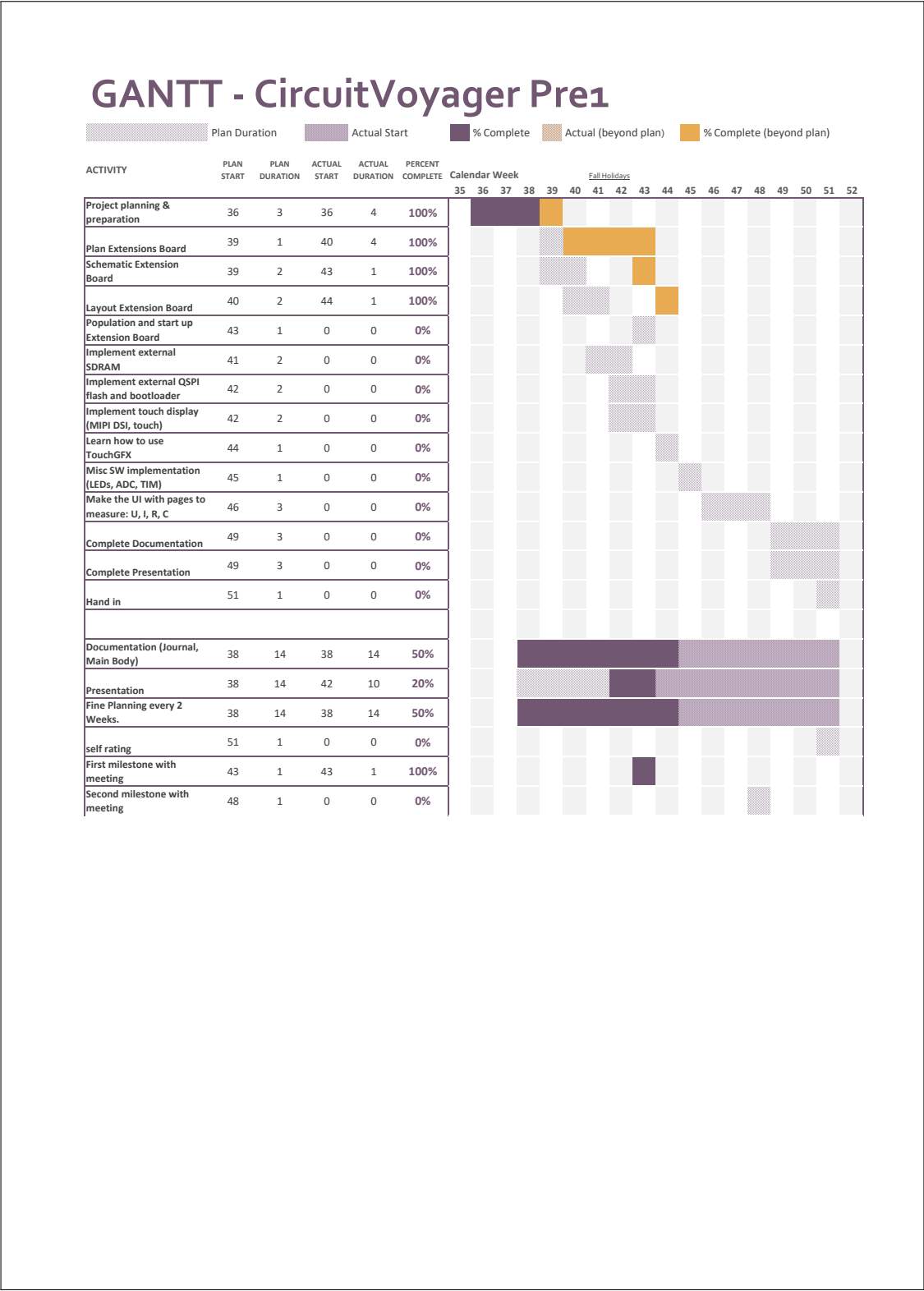


Figure 4.3: GANTT Chart

4.5 Weekly plans

4.5.1 KW39 & 40

- Write introduction
- Planning: Cost, Tools, When, Why
- Create project diagram (learning process)
- "Lastenheft"
- "Pflichtenheft"
- Make a HW-Digram for the Extension PCB.
- Make the schematic of the Extension PCB.
 - Part to measure voltage.
 - Part to measure current.
 - Part to measure continuity.
 - Addressable LEDs.
- Start with the Layout of the Extension PCB.
- Reflection of the start of the project.

4.5.2 KW41 & 42

Fall holidays. I planned to invest much time in the holidays, but it turned out that my plans changed, and I was busy.

4.5.3 KW43 & 44

Mainly catching up.

- Make a HW-Digram for the Extension PCB.
- Make the schematic of the Extension PCB.
 - Part to measure voltage.
 - Part to measure current.
 - Part to measure continuity.

- Addressable LEDs.
- Make the whole Layout of the Extension PCB.
- Order the Extension PCB and the components.
- Reflection of the start of the project.

and maybe if the time is sufficient I could start with implementing the SDRAM and QSPI Flash.

4.6 Extension PCB Schematics

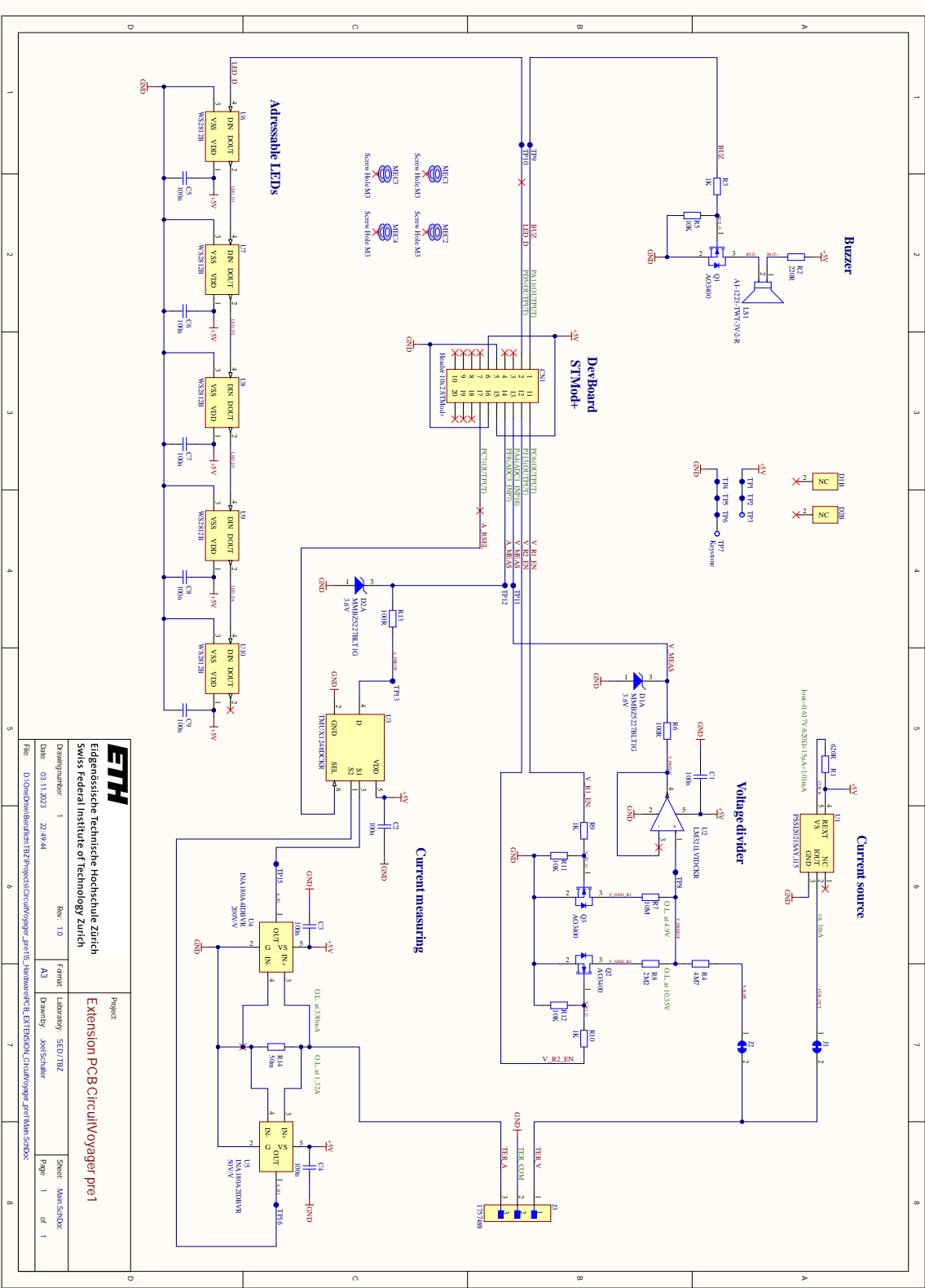


Figure 4.4: Extension PCB Schematics

4.7 Extension PCB Manufacture 11.23 BOM

Designator	Comment	Value	Description	Quantity	Digi-Key Part number
C1, C2, C3, C4, C5, C6, C7, C8, C9	C0402	100n	CERCAP 100n 16v 5% X7R 0402	9	311-1338-1-ND
CN1	Header 10x2 STMMod+	Header 10x2	Header 10x2, 2mm, RA, TH, Height 3.20mm/3.	1	2073-BF060-20A-B-0200-0280-0305-L-D-ND
D1, D2	MMB5222/BLT1G		Zener Voltage Regulator, 225 mW, 3-pin SOT-2	2	MMB5222/BLT1GOSCT-ND
I3	1757488		TERMI BLOCK HDR 3POS 90DEG SMM	1	
LS1	AI-1223-TWTF-3V-2-R		BUZZER MAGNETIC 3V 12MM TH	1	668-1456-ND
Q1, Q2, Q3	AO3400		N-channel Enhancement Mode Field-Effect Tr	3	
R1	R0402	620R	RES 620R 62.5mW 1% 0402	1	311-620LRCT-ND
R2	R0402	220R	RES 220R 62.5mW 1% 0402	1	311-220LRCT-ND
R3, R9, R10	R0402	1K	RES 1K 62.5mW 1% 0402	3	311-1.00KLRCT-ND
R4	R0402	4M7	RES 4M7 62.5mW 1% 0402	1	13-RC0402FR-074M7LCT-ND
R5, R11, R12	R0402	10K	RES 10K 62.5mW 1% 0402	3	311-10.0KLRCT-ND
R6, R13	R0402	100R	RES 100R 62.5mW 1% 0402	2	311-100LRCT-ND
R7	R0402	10M	RES 10M 62.5mW 1% 0402	1	311-10.0MLRCT-ND
R8	R0402	2M2	RES 2M2 62.5mW 1% 0402	1	13-RC0402FR-072M2LCT-ND
R14	R0805	50m	RES 0R05 125mW 1% 0805	1	13-PE0805FRF070R05LCT-ND
U1	PSS12021SAV,115		NEXPERIA - PSS12021SAV,115 - IC, CURRENT SC	1	1727-4328-1-ND
U2	LM321LV/DCKR		IC OPAMP GP 1 CIRCUIT SC70-5	1	296-53426-1-ND
U3	TMUX1248DCKR		3-LOW RON, 5-V, 2:1 (SPDT) GENE	1	296-TMUX1248DCKRCT-ND
U4	INA180A4IDBVR	200V/V	Operational Amplifier, 1 Func, 500uV Offset-M	1	296-47655-1-ND
U5	INA180A2IDBVR	50V/V	Operational Amplifier, 1 Func, 500uV Offset-M	1	296-47653-1-ND
U6, U7, U8, U9, U10	WS2812B		Intelligent Control LED Integrated Light Source	5	
TP7	KeyStone		TP SMD 3.81x 2.03mm	1	36-5019CT-ND
Digi-Key					
Already supplied					

Figure 4.5: Extension PCB Manufacture 11.23 BOM

5 Credits

Bibliography

- [1] ElectroNoobs, “Arduino 5 in 1 multimeter v2.0.” Available at http://electronoobs.com/eng_arduino_tut112.php (2019/11/29).
- [2] A. Walsh, “Protecting adc inputs.” Available at <https://www.analog.com/en/technical-articles/protecting-adc-inputs.html>.
- [3] A. Garaipoom, “Digital multimeter circuit using icl7107.” Available at <https://www.eleccircuit.com/digital-multimeter-circuit-using-icl7107/> (2022/08/16).
- [4] T. Instruments, “An-202 a digital multimeter using the add3501.” Available at https://www.ti.com/lit/an/snoa592c/snoa592c.pdf?ts=1698230599203&ref_url=https%253A%252F%252Fwww.ti.com%252Fsitesearch%252Fde-de%252Fdocs%252Funiversalsearch.tsp%253FlangPref%253Dde-DE%2526searchTerm%253Dmultimeter%2526nr%253D5689 (2015/02).

List of Figures

1.1	Project Mindmap	2
2.1	STMod+ Interface	4
2.2	Extension PCB HW concept	5
2.3	Buzzer Circuit	6
2.4	Adressable LEDs Circuit	7
2.5	Overvoltage Protection Circuit	8
2.6	Voltage Divider Circuit	9
2.7	Current Source Circuit	10
2.8	Current Measuring Circuit	11
2.9	Extension PCB Layout	12
2.10	Extension PCB Assembly Drawing	13
2.11	Extension PCB	13
4.1	Project Agreement	17
4.2	Interview 1	18
4.3	GANTT Chart	19
4.4	Extension PCB Schematics	22
4.5	Extension PCB Manufacture 11.23 BOM	23

List of Tables

2.1	Technical Details	3
4.1	Project Journal	16

Listings

Acronyms

CircuitVoyager The Name of the DMM I'm developing.

DevBoard main microcontroller development board. (STM32H747I-Disco)

DMM digital multimeter

HW Hardware

SW Software

SPI Serial Peripheral Interface (low level protocol)

SDRAM Synchronous Dynamic Random Access Memory (external RAM)

UI User Interface

MCU Micro Controlling Unit

Mipi DSI Digital Serial Interface (Display Protocol)

FAT File Allocation System (Low Level Filesystem)

HAL Hardware Abstraction Layer (STM32 Abstraction Library)

ETHZ Eidgenössische Technische Hochschule

TBZ Technische Berufsschule Zürich

ADC Analog Digital Converter

TIM Timer (Hardware Block in STM32)

PCB Printed Circuit Board

DUT Device under test

OVP Over voltage protection

TouchGFX Graphical UI designer for STM32 MCUs

QPSI Quad SPI

OL Overload

MUX Multiplexer