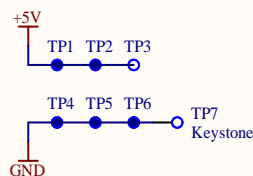
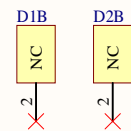
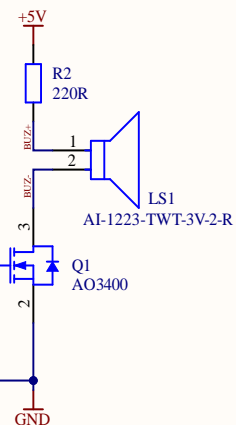
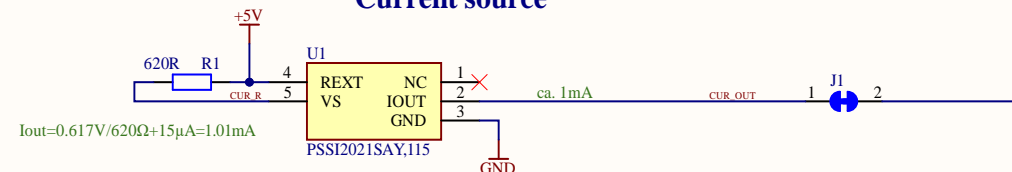


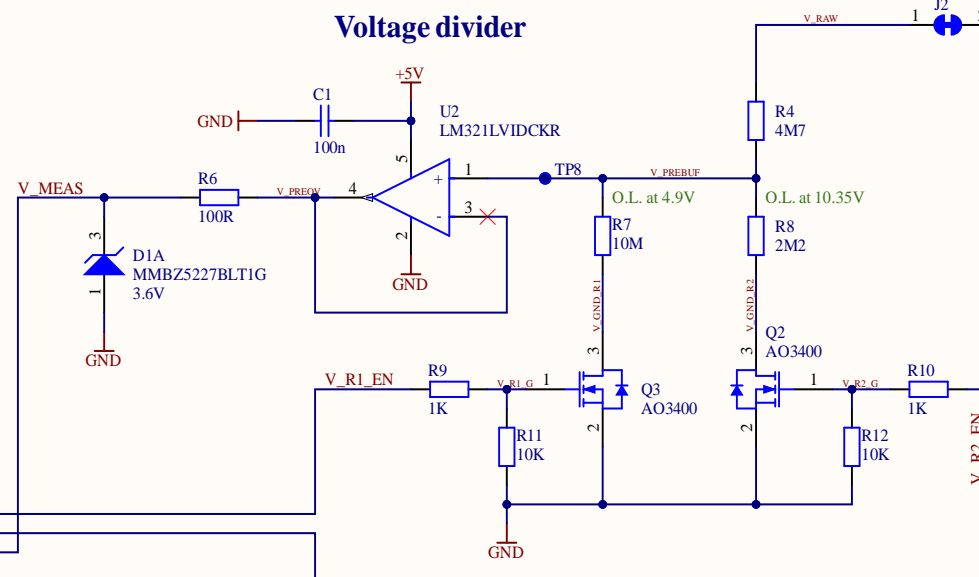
Buzzer



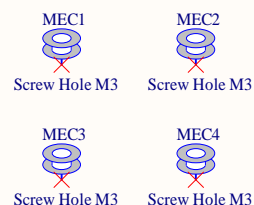
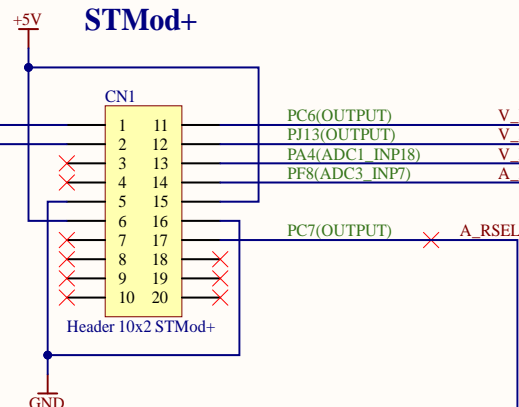
Current source



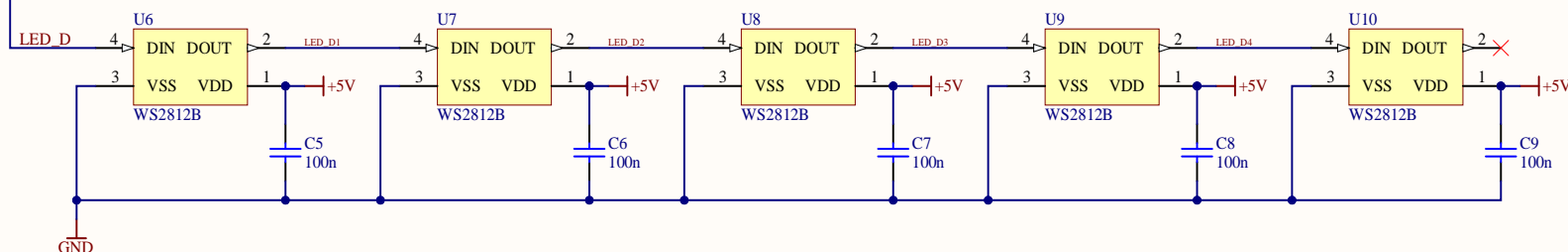
Voltage divider



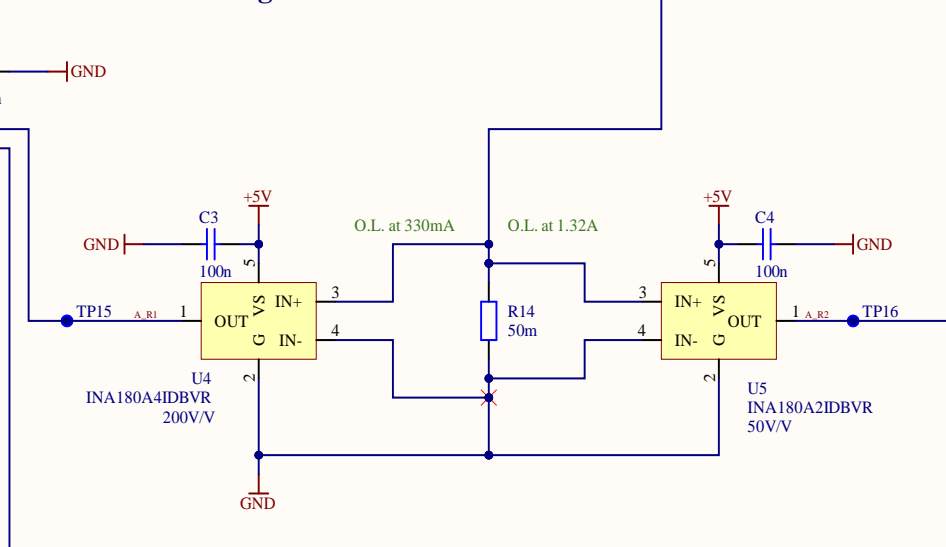
DevBoard STMod+



Adressable LEDs



Current measuring



Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Project:

Extension PCB CircuitVoyager pre1

Drawing number: 1

Rev: 1.0

Format:

Laboratory: SED / TBZ

Sheet: Main.SchDoc

Date: 16.12.2023 12:02:14

Drawn by: Joel Schaller

Page 1 of 1

File: D:\OneDrive\Beruflich\TBZ\Projects\CircuitVoyager_pre1\5_Hardware\PCB_EXTENSION_CircuitVoyager_pre1Main.SchDoc