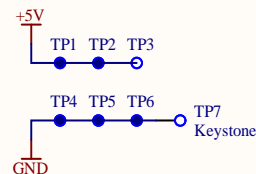
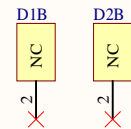
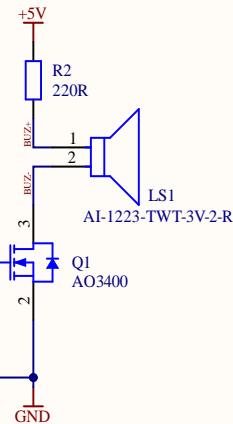
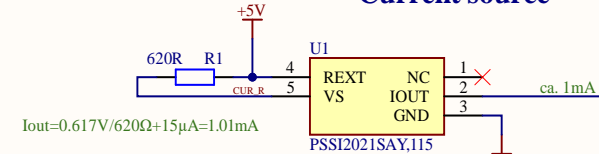


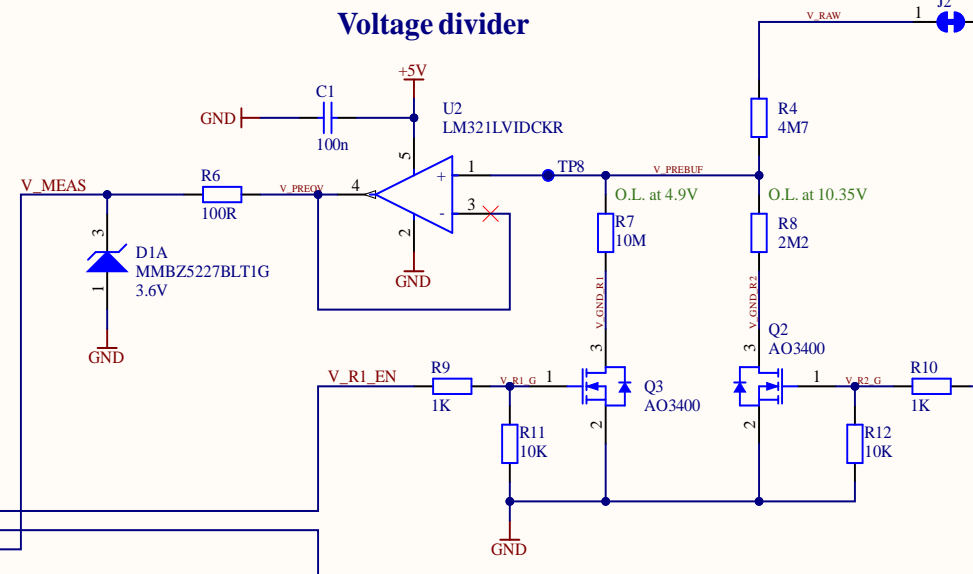
Buzzer



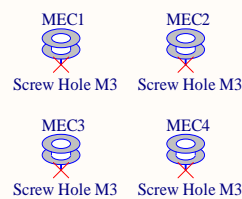
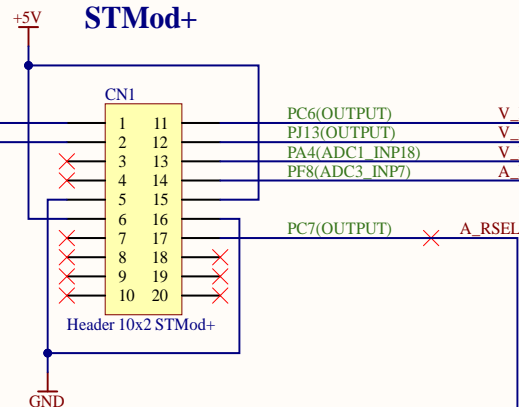
Current source



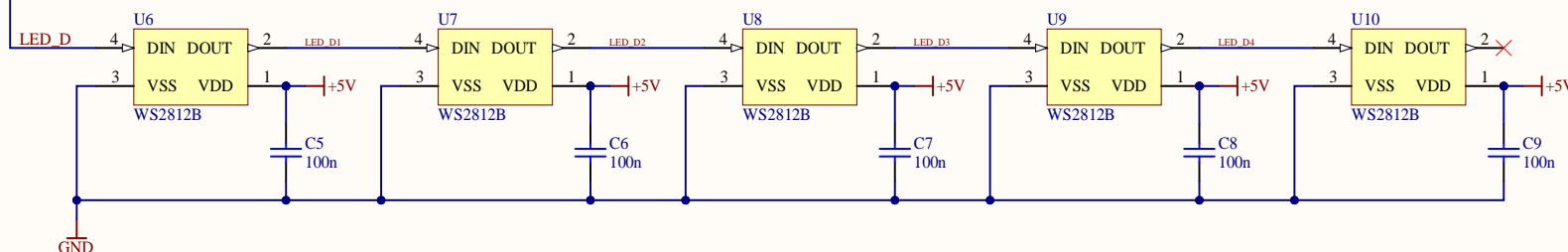
Voltage divider



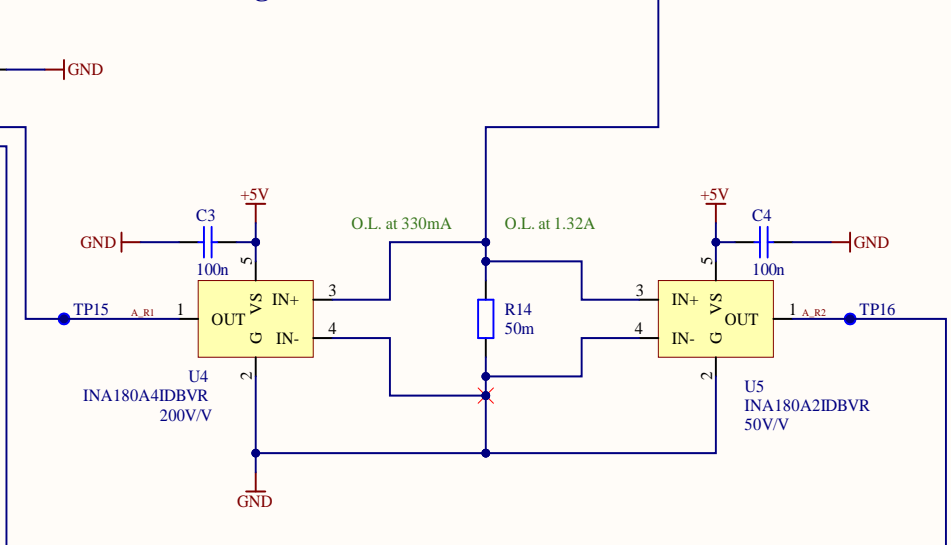
DevBoard STMod+



Adressable LEDs



Current measuring



ETH Eidgenössische Technische Hochschule Zürich Swiss Federal Institute of Technology Zurich			Project: Extension PCB CircuitVoyager pre1	
Drawing number: 1	Rev: 0.0	Format: A3	Laboratory: SED / TBZ	Sheet: Main.SchDoc
Date: 31.10.2023 21:58:39			Drawn by: Joel Schaller	Page 1 of 1
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