

Assignment9-Answer

True or False:

1. [1 pts] The von Neumann architecture was invented by von Neumann. False
2. [1 pts] RISC is comparatively faster than CISC. True

Short Answer:

3. [1 pts] CPU can be seen as the combination of ALU and Control Unit
4. [3 pts] Write down the five components of the von Neumann architecture, which part does REGISTER belongs to?

Memory unit, Arithmetic/logic unit, Input unit, Control Unit, Output Unit

Register belongs to memory unit(or control unit, IR, PC etc.).

5. [1.5 pts] Three necessary requirements for computer:

Program control, Internally-stored-program, Changeable program

6. [1 pts] Rank Main Memory, Cache, Registers, Magnetic Disk in the order of access time(from quick to slow)



7. [1.5 pts] Draw down the graph of the fetch-execute cycle.

