


- \* Difference b/w RISC and CISC.
- \* RISC design philosophy (4 Rules) (pg. 4)
- \* How ARM instruction set differ from pure RISC. (pg no 6)
- \* Block diagram of ARM based embedded device (Pg. 7)
- \*  ARM Bus protocols (7-8 pg) + pg-11, pg-12 peripherals and memory.
- \* Initialization of Boot code.

## \* Chapter - 2

- \* <sup>Core</sup> ARM, DATA flow model (pg-20)
- \* LPSR registers (22-23).
- \* Complete ARM register set (24-Pg).
- \* Difference b/w ARM & THUMB instruction set (Pg-27)
- \* ARM processor MODES.
- \* ARM-7 3 stage pipeline with reg. (Pg-30)
- \* Exception interrupt and vector table (Pg. 33)