

USN: 25822CS038

SDM College of Engineering & Technology, Dharwad – 2

Department of Computer Science & Engineering

Internal Assessment I

Semester & Branch : IV Sem. CSE (A & B Division)

Date: 26 -04-2024

Course Code & Title: 22UCSC402 PCPI

Time: 9.30-10.30am

Course Instructors : Prof.Nita K/Prof. Sharada H N

Max.Marks: 20

Note: Answer any one from Q1 and Q2. Q3 is compulsory.

Q.1. a) Explain the different operating modes in ARM7 processor.

[Marks 5: CO1]

b) Illustrate with an example, the three stage pipeline of ARM7 processor.

[Marks 5: CO2]

Q.2. a) Differentiate between Microprocessor and Microcontroller.

[Marks 5: CO1]

b) Draw and explain the CPSR register with the following

CPSR = NczViFt_svc bit specification.

[Marks 5: CO2]

Q.3. a) Explain how the ARM7 instruction differs from pure RISC architecture.

[Marks 5: CO1]

b) Illustrate the Data flow model of ARM 7 processor.

[Marks 5: CO2]