USN: 2 S D 7 / C S 6 1 7

SDM College of Engineering & Technology, Dharwad – 2
Department of Computer Science & Engineering
Internal Assessment II

Semester & Branch : IV Sem. CSE (A & B Division)
Course Code & Title: 22UCSC402 ARM Processor
Course Instructors : Prof.Nita K/Prof. Sharada H N

Date: 18 -05-2024
Time: 9.30-10.30am
Max.Marks: 20

Note: Answer <u>any one</u> from Q1 and Q2. <u>Q3 is compulsory</u>.

Q.1. a) Write an ALP to transfer a block of 10 bytes from code memory to data memory.
[Marks 5: CO 3,4]

b) Write the steps required for enabling and disabling IRQ exception.
[Marks 5: CO4]

Q.2. a) Write an ALP to find the largest number in a given array of 10 numbers.
[Marks 5: CO 3,4]

b) Illustrate the nested interrupt handling schemes, with a neat diagram.
[Marks 5: CO4]

Q.3. a) Write an ALP to reverse the given string "ARM Microcontroller",0.
[Marks 5: CO 3,4]

b) Compare the ARM and THUMB mode of ARM7 processor.   [Marks 5: CO4]