

**Course Learning Objectives (CLOs):** This course focuses on the following learning perspectives:

- Understand the Basic architecture and Peripherals associated with Micro-processors & Micro-controllers.
- Understand the internal architecture, instruction set of ARM7 microcontroller, assembling process & implement small programs.
- Design & develop Assembly Language Program /& C program for a given real time application.
- Demonstrate working knowledge of the necessary steps and methods used to interface ARM7 to devices such as motors, LCD, ADC, and DAC etc.

**Course Outcomes (COs):**

<b>Description of the Course Outcome:</b> At the end of the course the student will be able to:		<b>Mapping to POs(1-12) / PSOs (13-16)</b>		
		<b>Substantial Level (3)</b>	<b>Moderate Level (2)</b>	<b>Slight Level (1)</b>
<b>CO-1</b>	<b>Explain</b> the Basic architecture and Peripherals associated with Micro-processors & Micro-controllers	-	1	-
<b>CO-2</b>	<b>Explain</b> the features of embedded systems, architecture of ARM7 and applications.	-	1	-

<b>CO-3</b>	<b>Illustrate</b> the ARM and THUMB instruction sets.	-	2,5	13
<b>CO-4</b>	<b>Write</b> an ASM / Embedded C program using the instruction set of ARM and THUMB to solve the engineering problems.	-	3	-
<b>CO-5</b>	<b>Design and Write</b> ARM (LPC2148) program for specific applications.	-	5	3,12

<b>POs/PSOs</b>	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>	<b>6</b>	<b>7</b>	<b>8</b>	<b>9</b>	<b>10</b>	<b>11</b>	<b>12</b>	<b>13</b>	<b>14</b>	<b>15</b>	<b>16</b>
<b>Mapping Level</b>	2.0	2.0	1.5	-	2.0	-	-	-	-	-	-	1.0	1.0	-	-	-

**Pre-requisites:** Number systems, Digital systems and Computer Organization

**Contents:**

#### **Unit-I**

**Introduction to Processor & Controllers:** Evolution of Microprocessor, Block diagram and Features of Microprocessor & Micro-controller, Comparison of Microprocessor and Microcontroller, The RISC & CISC design philosophy. **7 Hrs**

#### **Unit-II**

**Peripheral Interfacing with Microprocessor:** Static and Dynamic memories, Vector interrupt table, Interrupt service routine, Interfacing of microprocessor with Programmable Interrupt Controller 8259, DMA controller 8257, Programmable peripheral Interface-8255. **9 Hrs**

#### **Unit-III**

**ARM Embedded Systems and ARM Processor Fundamentals:** ARM design philosophy, embedded system hardware- AMBA bus protocol, embedded system software- applications. ARM core data flow model, Registers, CPSR-Processor modes, Banked registers. Pipeline- Characteristics **7 Hrs**

#### **Unit-IV**

**ARM Instruction Set:** Fundamentals of ARM instructions, Barrel shifter, Classification and explanation of instructions with examples-Data processing, Branch, Load-store, SWI and Program Status. IRQ and FIQ exceptions, Comparison between exception and interrupts. Interrupt handling schemes- nested interrupt handler, non-nested interrupt handler. Basic interrupt stack design.

**Introduction to THUMB and ARM Programming:** Introduction to THUMB, Differences between ARM and THUMB, Register usage in Thumb, ARM Thumb Interworking. General Structure of ARM assembly module, Assembler directives. Simple ALP programs on Arithmetic & logical operations, Factorial, string operation, sorting, searching, and Scan. **8 Hrs**

### Unit-V

**Peripheral Interfacing:** Salient features of LPC2148 ARM CPU, applications, block diagram, memory mapping. Functional features of Interrupt controller, RTC, USB, UART, I2C, SPI, SSP controllers, watch dog timers and other system control units. GPIO, PLL & Timers: Features, Register description with example and Applications. Interfacing of Stepper motor, DC Motor, LED interface. **8 Hrs**

#### Reference Books:

- 1) Ramakant A Gayakwad, "Microprocessor and Interfacing ", 4/E, Tata McGraw Hill, 2009
- 2) B.Ram, "Microprocessors and Interfacing",
- 3) Atul P Godse and Mrs. Deepali A Godse, "Microprocessors and Interfacing",
- 4) William Hohl, "ARM Assembly Language", CRC Press.
- 5) Steve Furber, ARM System-on-chip Architecture, Pearson Education, 2012
- 6) LPC 2148 User Manual